**NEC Corporation**

**Express5800/R110h-1 (Intel Xeon E3-1270 v5)**

<table>
<thead>
<tr>
<th>SPECfp(^\circ)_rate2006 = 197</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006 = 192</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 9006  
**Test date:** Dec-2015  
**Hardware Availability:** Mar-2016  
**Test sponsor:** NEC Corporation  
**Tested by:** NEC Corporation

**Hardware**

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E3-1270 v5</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 4.00 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>3600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>4 cores, 1 chip, 4 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 7.2 (Maipo)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel:</td>
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<tr>
<td>File System:</td>
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</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Cycles</th>
</tr>
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<tbody>
<tr>
<td>410.bwaves</td>
<td>8</td>
</tr>
<tr>
<td>416.gamess</td>
<td>8</td>
</tr>
<tr>
<td>433.milc</td>
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<tr>
<td>434.zeusmp</td>
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<tr>
<td>435.gromacs</td>
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<tr>
<td>436.cactusADM</td>
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<tr>
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<td>444.namd</td>
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<td>459.GemsFDTD</td>
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<tr>
<td>465.tonto</td>
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</tr>
<tr>
<td>470.lbm</td>
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<tr>
<td>481.wrf</td>
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<tr>
<td>482.sphinx3</td>
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**SPECfp\_rate2006 = 197**  
**SPECfp\_rate_base2006 = 192**
## SPEC CFP2006 Result

**NEC Corporation**

Express5800/R110h-1 (Intel Xeon E3-1270 v5)

**SPECfp_rate2006 = 197**  
**SPECfp_rate_base2006 = 192**

<table>
<thead>
<tr>
<th>CPU2006 license: 9006</th>
<th>Test date: Dec-2015</th>
</tr>
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<tbody>
<tr>
<td>Test sponsor: NEC Corporation</td>
<td>Hardware Availability: Mar-2016</td>
</tr>
<tr>
<td>Tested by: NEC Corporation</td>
<td>Software Availability: Nov-2015</td>
</tr>
</tbody>
</table>

| L3 Cache: 8 MB I+D on chip per chip | System State: Run level 3 (multi-user) |
| Other Cache: None | Base Pointers: 32/64-bit |
| Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E) | Peak Pointers: 32/64-bit |
| Disk Subsystem: 1 x 500 GB SATA, 7200 RPM | Other Software: None |
| Other Hardware: None | |

### Results Table

<table>
<thead>
<tr>
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<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>194</td>
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<td>194</td>
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<td>194</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS Settings:  
Power Management Policy: Custom  
Energy Performance: Performance
NEC Corporation

Express5800/R110h-1 (Intel Xeon E3-1270 v5)

SPECfp_rate2006 = 197
SPECfp_rate_base2006 = 192

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2015
Hardware Availability: Mar-2016
Software Availability: Nov-2015

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
   icc  -m64

C++ benchmarks:
   icpc  -m64

Fortran benchmarks:
   ifort  -m64

Benchmarks using both Fortran and C:
   icc  -m64  ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
   -xCORE-AVX2  -ipo  -03  -no-prec-div  -opt-prefetch  -auto-p32
   -ansi-alias  -opt-mem-layout-trans=3
   Continued on next page
SPEC CFP2006 Result

NEC Corporation
Express5800/R110h-1 (Intel Xeon E3-1270 v5)

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SPECfp_rate_base2006 = 192

CPU2006 license: 9006
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Test date: Dec-2015
Tested by: NEC Corporation
Hardware Availability: Mar-2016
Software Availability: Nov-2015

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.games: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64

Continued on next page
**Peak Portability Flags (Continued)**

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

**Peak Optimization Flags**

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-1lp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

Continued on next page
NEC Corporation

Express5800/R110h-1 (Intel Xeon E3-1270 v5)

SPECfp_rate2006 = 197
SPECfp_rate_base2006 = 192

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2015
Hardware Availability: Mar-2016
Software Availability: Nov-2015

Peak Optimization Flags (Continued)

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.xml

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For other inquiries, please contact webmaster@spec.org.

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