### Lenovo Group Limited

**Lenovo ThinkServer TS150**

(3.30 GHz, Intel Xeon E3-1225 v5)

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>9017</th>
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<tr>
<td>Test sponsor:</td>
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<tr>
<th>Test</th>
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<th>SPECfp_base2006</th>
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<tr>
<td>410.bwaves</td>
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<tr>
<td>482.sphinx3</td>
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</tbody>
</table>

| SPECfp Base2006 | 93.5 |
| SPECfp2006     | 95.8 |

### Hardware

- **CPU Name:** Intel Xeon E3-1225 v5
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz
- **CPU MHz:** 3300
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip
- **CPU(s) orderable:** 1 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core

### Software

- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) Kernel 3.12.28-4-default
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
## Lenovo Group Limited

**Lenovo ThinkServer TS150**

(3.30 GHz, Intel Xeon E3-1225 v5)

### SPEC CFP2006 Result

**SPECfp2006 = 95.8**

**SPECfp_base2006 = 93.5**

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L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)

Disk Subsystem: 1 x 800 GB SATA SSD

Other Hardware: None

Base Pointers: 64-bit

Peak Pointers: 32/64-bit

Other Software: None

---

### Results Table

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<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
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<td>96.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

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### Platform Notes

- BIOS configuration:
  - EIST Support set to Enabled
  - C1E Support set to Enabled
  - C State Support set to Enabled
  - Turbo Mode set to Enable
  - Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
  - $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab8e28219e1
  - running on TS150 Mon Jan 4 09:46:30 2016

This section contains SUT (System Under Test) info as seen by
Lenovo Group Limited
Lenovo ThinkServer TS150
(3.30 GHz, Intel Xeon E3-1225 v5)

SPECfp2006 = 95.8
SPECfp_base2006 = 93.5

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Test date: Jan-2016
Tested by: Lenovo Group Limited
Hardware Availability: Oct-2015
Software Availability: Aug-2015

Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1225 v5 @ 3.30GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal:       32933544 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 0
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12"
    VERSION_ID="12"
    PRETTY_NAME="SUSE Linux Enterprise Server 12"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
  Linux TS150 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4)
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 4 05:01

SPEC is set to: /home/cpu2006-1.2-ic16.0
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/sda3    xfs  693G  27G  667G  4% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

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Lenovo Group Limited
Lenovo ThinkServer TS150
(3.30 GHz, Intel Xeon E3-1225 v5)

SPECfp2006 = 95.8
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CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Platform Notes (Continued)

BIOS LENOVO FWKT32A 12/25/2015
Memory:
  4x Samsung M378A1G43DB0-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags
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Lenovo ThinkServer TS150
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Software Availability: Aug-2015

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
           -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
           -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page
Lenovo Group Limited
Lenovo ThinkServer TS150
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**Software Availability:** Aug-2015

## Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html  

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml  

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For other inquiries, please contact webmaster@spec.org.

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