



# SPEC® CINT2006 Result

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**NEC Corporation**

**SPECint®2006 = 68.4**

Express5800/T110h-S (Intel Xeon E3-1220 v5)

**SPECint\_base2006 = 66.2**

**CPU2006 license:** 9006

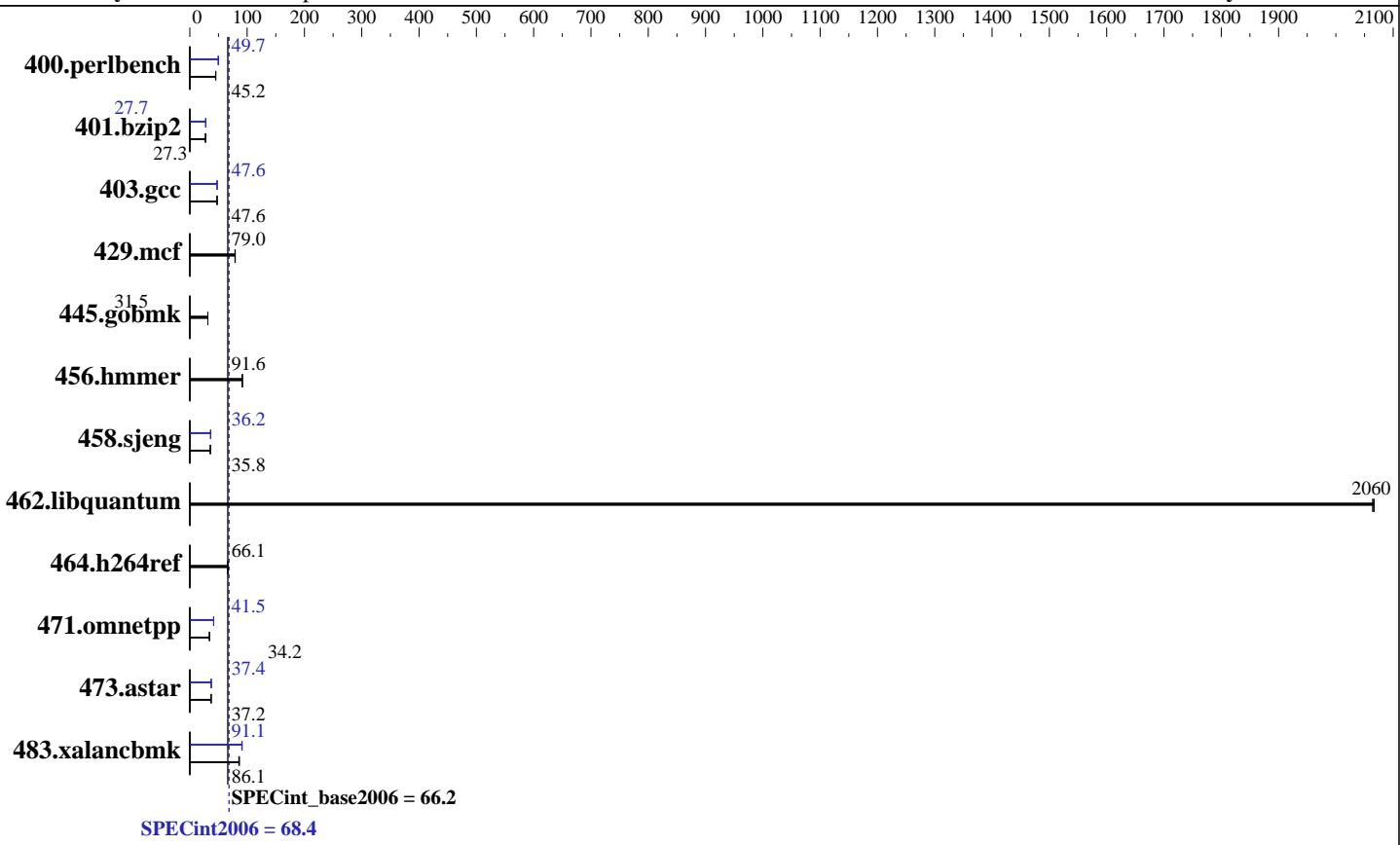
**Test sponsor:** NEC Corporation

**Tested by:** NEC Corporation

**Test date:** Dec-2015

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015



## Hardware

CPU Name: Intel Xeon E3-1220 v5  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC4-2133P-E)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 Compiler: Kernel 3.10.0-327.el7.x86\_64  
 C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	216	45.3	216	45.1	<b>216</b>	<b>45.2</b>	<b>197</b>	<b>49.7</b>	195	50.0	198	49.5
401.bzip2	354	27.3	<b>353</b>	<b>27.3</b>	353	27.3	348	27.7	<b>348</b>	<b>27.7</b>	348	27.7
403.gcc	169	47.6	<b>169</b>	<b>47.6</b>	170	47.5	169	47.6	<b>169</b>	<b>47.6</b>	169	47.5
429.mcf	115	79.3	<b>115</b>	<b>79.0</b>	116	78.7	115	79.3	<b>115</b>	<b>79.0</b>	116	78.7
445.gobmk	<b>334</b>	<b>31.5</b>	334	31.4	334	31.5	<b>334</b>	<b>31.5</b>	334	31.4	334	31.5
456.hmmer	102	91.5	<b>102</b>	<b>91.6</b>	102	91.7	102	91.5	<b>102</b>	<b>91.6</b>	102	91.7
458.sjeng	338	35.8	<b>338</b>	<b>35.8</b>	338	35.8	334	36.3	334	36.2	<b>334</b>	<b>36.2</b>
462.libquantum	10.0	2070	<b>10.0</b>	<b>2060</b>	10.0	2060	10.0	2070	<b>10.0</b>	<b>2060</b>	10.0	2060
464.h264ref	<b>335</b>	<b>66.1</b>	335	66.0	334	66.3	<b>335</b>	<b>66.1</b>	335	66.0	334	66.3
471.omnetpp	183	34.1	182	34.4	<b>183</b>	<b>34.2</b>	<b>150</b>	<b>41.5</b>	150	41.6	151	41.5
473.astar	189	37.2	<b>189</b>	<b>37.2</b>	189	37.1	189	37.2	188	37.4	<b>188</b>	<b>37.4</b>
483.xalancbmk	<b>80.1</b>	<b>86.1</b>	79.8	86.4	80.7	85.5	<b>75.7</b>	<b>91.1</b>	75.7	91.2	76.1	90.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:

Power Management Policy: Custom  
Energy Performance: Performance

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled



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## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):

`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

445.gobmk: `-DSPEC_CPU_LP64`

456.hammer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

464.h264ref: `-DSPEC_CPU_LP64`

471.omnetpp: `-D_FILE_OFFSET_BITS=64`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`  
`-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)`  
`-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch`  
`-ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)`  
`-ipo(pass 2) -O3(pass 2) -no-prec-div`  
`-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32`  
`-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc`  
`-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hammer: `basepeak = yes`

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## Peak Optimization Flags (Continued)

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-110h-RevA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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