## Lenovo Group Limited

**Lenovo ThinkServer TS450**  
(3.60 GHz, Intel Xeon E3-1270 v5)

**SPECfp®2006 =** 101  
**SPECfp_base2006 =** 99.3

### Hardware

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon E3-1270 v5</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 4.00 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>3600</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>4 cores, 1 chip, 4 cores/chip</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System:</td>
<td>SUSE Linux Enterprise Server 12 (x86_64)</td>
</tr>
<tr>
<td></td>
<td>Kernel 3.12.28-4-default</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;</td>
</tr>
<tr>
<td></td>
<td>Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
</tbody>
</table>

### Specified Programs

<table>
<thead>
<tr>
<th>Program</th>
<th>SPECfp®2006</th>
<th>SPECfp_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td></td>
<td></td>
</tr>
<tr>
<td>416.gamess</td>
<td></td>
<td></td>
</tr>
<tr>
<td>433.milc</td>
<td></td>
<td></td>
</tr>
<tr>
<td>434.zeusmp</td>
<td></td>
<td></td>
</tr>
<tr>
<td>435.gromacs</td>
<td></td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td></td>
<td></td>
</tr>
<tr>
<td>437.leslie3d</td>
<td></td>
<td></td>
</tr>
<tr>
<td>444.namd</td>
<td></td>
<td></td>
</tr>
<tr>
<td>447.dealII</td>
<td></td>
<td></td>
</tr>
<tr>
<td>450.soplex</td>
<td></td>
<td></td>
</tr>
<tr>
<td>453.povray</td>
<td></td>
<td></td>
</tr>
<tr>
<td>454.calculix</td>
<td></td>
<td></td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td></td>
<td></td>
</tr>
<tr>
<td>465.tonto</td>
<td></td>
<td></td>
</tr>
<tr>
<td>470.lbm</td>
<td></td>
<td></td>
</tr>
<tr>
<td>481.wrf</td>
<td></td>
<td></td>
</tr>
<tr>
<td>482.sphinx3</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Note:**  
Continued on next page
Lenovo Group Limited

Lenovo ThinkServer TS450
(3.60 GHz, Intel Xeon E3-1270 v5)

SPECfp2006 = 101
SPECfp_base2006 = 99.3

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx8 PC4-2133P-U)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None
Other Software: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>95.9</td>
<td>142</td>
<td>95.6</td>
<td>142</td>
<td><strong>95.9</strong></td>
<td><strong>142</strong></td>
<td>95.9</td>
<td>142</td>
</tr>
<tr>
<td>416.gamess</td>
<td>378</td>
<td>51.7</td>
<td><strong>378</strong></td>
<td><strong>51.7</strong></td>
<td>378</td>
<td>51.8</td>
<td>344</td>
<td>57.0</td>
</tr>
<tr>
<td>433.milc</td>
<td>80.1</td>
<td>115</td>
<td>79.9</td>
<td>115</td>
<td><strong>79.9</strong></td>
<td><strong>115</strong></td>
<td>80.1</td>
<td>115</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>42.7</td>
<td>213</td>
<td><strong>42.8</strong></td>
<td><strong>213</strong></td>
<td>42.8</td>
<td>213</td>
<td>42.7</td>
<td>213</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>101</td>
<td>70.7</td>
<td>102</td>
<td>70.3</td>
<td><strong>102</strong></td>
<td><strong>70.3</strong></td>
<td>101</td>
<td>70.7</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>32.0</td>
<td>373</td>
<td><strong>32.1</strong></td>
<td><strong>372</strong></td>
<td>32.2</td>
<td>371</td>
<td>32.0</td>
<td>373</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td><strong>86.1</strong></td>
<td><strong>109</strong></td>
<td>86.3</td>
<td>109</td>
<td>86.0</td>
<td>109</td>
<td><strong>86.1</strong></td>
<td><strong>109</strong></td>
</tr>
<tr>
<td>444.namd</td>
<td><strong>208</strong></td>
<td><strong>38.5</strong></td>
<td>208</td>
<td>38.5</td>
<td>209</td>
<td>38.5</td>
<td><strong>205</strong></td>
<td><strong>39.2</strong></td>
</tr>
<tr>
<td>447.dealII</td>
<td>136</td>
<td>84.0</td>
<td>136</td>
<td>84.1</td>
<td>136</td>
<td>83.8</td>
<td>136</td>
<td>84.0</td>
</tr>
<tr>
<td>450.soplex</td>
<td>144</td>
<td>57.9</td>
<td>141</td>
<td>59.2</td>
<td><strong>144</strong></td>
<td><strong>58.0</strong></td>
<td>144</td>
<td>57.9</td>
</tr>
<tr>
<td>453.povray</td>
<td>71.2</td>
<td>74.7</td>
<td><strong>71.1</strong></td>
<td><strong>74.8</strong></td>
<td>70.1</td>
<td>75.9</td>
<td>61.8</td>
<td>86.0</td>
</tr>
<tr>
<td>454.calculix</td>
<td>102</td>
<td>80.5</td>
<td><strong>103</strong></td>
<td><strong>80.4</strong></td>
<td>103</td>
<td>80.3</td>
<td>101</td>
<td>81.7</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>126</td>
<td>84.0</td>
<td>126</td>
<td>84.1</td>
<td><strong>126</strong></td>
<td><strong>84.0</strong></td>
<td>125</td>
<td>85.2</td>
</tr>
<tr>
<td>465.tonto</td>
<td>143</td>
<td>68.9</td>
<td>143</td>
<td>68.7</td>
<td><strong>143</strong></td>
<td><strong>68.7</strong></td>
<td><strong>130</strong></td>
<td><strong>75.6</strong></td>
</tr>
<tr>
<td>470.lbm</td>
<td>72.7</td>
<td>189</td>
<td>72.6</td>
<td>189</td>
<td><strong>72.7</strong></td>
<td><strong>189</strong></td>
<td>72.7</td>
<td>189</td>
</tr>
<tr>
<td>481.wrf</td>
<td>85.0</td>
<td>131</td>
<td><strong>84.9</strong></td>
<td><strong>132</strong></td>
<td>84.8</td>
<td>132</td>
<td>85.0</td>
<td>131</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td><strong>183</strong></td>
<td><strong>107</strong></td>
<td>182</td>
<td>107</td>
<td>183</td>
<td>106</td>
<td><strong>183</strong></td>
<td><strong>107</strong></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
- EIST Support set to Enabled
- Intel (R) Hyper-Threading set to Disabled
- C1E Support set to Enabled
- C State Support set to Enabled
- Turbo Mode set to Enable

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on TS450-SLE12 Fri Jan 22 18:39:00 2016

Continued on next page
Lenovo Group Limited

Lenovo ThinkServer TS450
(3.60 GHz, Intel Xeon E3-1270 v5)

SPECfp2006 = 101
SPECfp_base2006 = 99.3

CPU2006 license: 9017
Test date: Jan-2016
Test sponsor: Lenovo Group Limited
Hardware Availability: Oct-2015
Tested by: Lenovo Group Limited
Software Availability: Aug-2015

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz
1 "physical id" s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal: 32933324 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

uname -a:
Linux TS450-SLE12 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 22 14:34

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 600G 7.6G 593G 2% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

Continued on next page
Lenovo Group Limited  
Lenovo ThinkServer TS450  
(3.60 GHz, Intel Xeon E3-1270 v5)  

SPECfp2006 = 101  
SPECfp_base2006 = 99.3

CPU2006 license: 9017  
Test sponsor: Lenovo Group Limited  
Tested by: Lenovo Group Limited  

Test date: Jan-2016  
Hardware Availability: Oct-2015  
Software Availability: Aug-2015

Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO FWKT32A   12/25/2015
Memory:  
4x Samsung M378A1G43DB0-CPB 8 GB 2 rank 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:  
  icc   -m64

C++ benchmarks:  
  icpc  -m64

Fortran benchmarks:  
  ifort -m64

Benchmarks using both Fortran and C:  
  icc   -m64 ifort -m64

Base Portability Flags

410.bwaves:  -DSPEC_CPU_LP64
416.gamess:  -DSPEC_CPU_LP64
433.milc:  -DSPEC_CPU_LP64
434.zeusmp:  -DSPEC_CPU_LP64
435.gromacs:  -DSPEC_CPU_LP64 -nofor_main
436.cactusADM:  -DSPEC_CPU_LP64 -nofor_main
437.leslie3d:  -DSPEC_CPU_LP64
444.namd:  -DSPEC_CPU_LP64
447.dealII:  -DSPEC_CPU_LP64
450.soplex:  -DSPEC_CPU_LP64

Continued on next page
Lenovo Group Limited
Lenovo ThinkServer TS450
(3.60 GHz, Intel Xeon E3-1270 v5)

SPECfp2006 = 101
SPECfp_base2006 = 99.3

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Test date: Jan-2016
Tested by: Lenovo Group Limited
Hardware Availability: Oct-2015
Software Availability: Aug-2015

Base Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Peak Portability Flags

Same as Base Portability Flags
Lenovo Group Limited

Lenovo ThinkServer TS450
(3.60 GHz, Intel Xeon E3-1270 v5)

SPECfp2006 = 101
SPECfp_base2006 = 99.3

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jan-2016
Hardware Availability: Oct-2015
Software Availability: Aug-2015

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

Continued on next page
### Lenovo Group Limited

**Lenovo ThinkServer TS450 (3.60 GHz, Intel Xeon E3-1270 v5)**

| SPECfp2006 | 101 |
| SPECfp_base2006 | 99.3 |

| CPU2006 license: | 9017 | Test date: | Jan-2016 |
| Test sponsor: | Lenovo Group Limited | Hardware Availability: | Oct-2015 |
| Tested by: | Lenovo Group Limited | Software Availability: | Aug-2015 |

#### Peak Optimization Flags (Continued)

- **435.gromacs**
  - basepeak = yes

- **436.cactusADM**
  - basepeak = yes

- **454.calculix**
  - -xCORE-AVX2
  - -ipo
  - -O3
  - -no-prec-div
  - -auto-ilp32
  - -ansi-alias

- **481.wrf**
  - basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


---

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Mar 8 12:33:03 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 March 2016.