SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

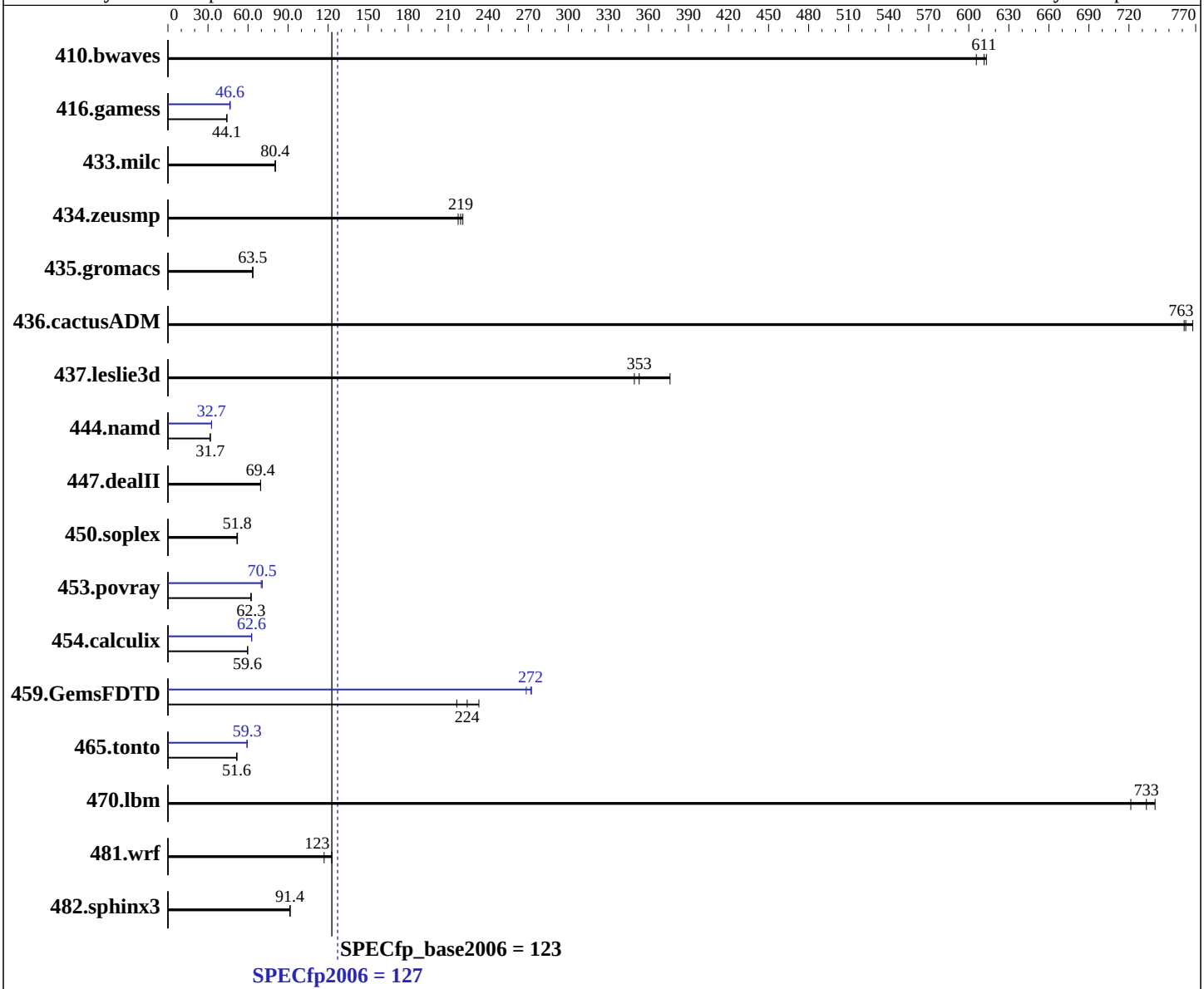
SuperServer 6028R-WTR
(X10DRW-i , Intel Xeon E5-2667 v4)

SPECfp®2006 = 127

SPECfp_base2006 = 123

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015



Hardware

CPU Name:	Intel Xeon E5-2667 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2, Kernel 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Supermicro

SuperServer 6028R-WTR
(X10DRW-i , Intel Xeon E5-2667 v4)

SPECfp2006 = 127

SPECfp_base2006 = 123

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Sep-2015

L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 200 GB SATA III SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	22.4	606	**22.2**	**611**	22.2	613	22.4	606	**22.2**	**611**	22.2	613
416.gamess	443	44.2	444	44.1	**444**	**44.1**	**420**	**46.6**	423	46.3	420	46.6
433.milc	114	80.3	114	80.6	**114**	**80.4**	114	80.3	114	80.6	**114**	**80.4**
434.zeusmp	41.9	217	41.2	221	**41.5**	**219**	41.9	217	41.2	221	**41.5**	**219**
435.gromacs	112	63.5	112	63.6	**112**	**63.5**	112	63.5	112	63.6	**112**	**63.5**
436.cactusADM	15.6	768	15.7	761	**15.7**	**763**	15.6	768	15.7	761	**15.7**	**763**
437.leslie3d	**26.6**	**353**	26.9	349	25.0	376	**26.6**	**353**	26.9	349	25.0	376
444.namd	253	31.7	**253**	**31.7**	253	31.7	**246**	**32.7**	246	32.6	245	32.7
447.dealII	165	69.4	**165**	**69.4**	165	69.5	165	69.4	**165**	**69.4**	165	69.5
450.soplex	**161**	**51.8**	160	52.1	161	51.8	**161**	**51.8**	160	52.1	161	51.8
453.povray	85.3	62.3	85.7	62.1	**85.4**	**62.3**	75.2	70.7	**75.4**	**70.5**	76.2	69.8
454.calculix	138	59.7	138	59.6	**138**	**59.6**	131	62.8	132	62.6	**132**	**62.6**
459.GemsFDTD	**47.3**	**224**	45.5	233	49.0	216	39.5	268	**39.0**	**272**	38.9	272
465.tonto	190	51.7	191	51.5	**191**	**51.6**	166	59.3	166	59.3	**166**	**59.3**
470.lbm	19.0	721	18.6	740	**18.7**	**733**	19.0	721	18.6	740	**18.7**	**733**
481.wrf	90.8	123	95.5	117	**91.0**	**123**	90.8	123	95.5	117	**91.0**	**123**
482.sphinx3	213	91.5	213	91.4	**213**	**91.4**	213	91.5	213	91.4	**213**	**91.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Settings:
Early Snoop = Disable
Enforce POR = Disabled
Memory Frequency = 2400
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on X10DRW-01 Tue Mar  8 04:30:36 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
          2 "physical id"s (chips)
          32 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 8
          siblings  : 16
          physical 0: cores 0 2 3 4 8 10 11 12
          physical 1: cores 0 2 3 4 8 10 11 12
       cache size : 25600 KB

    From /proc/meminfo
       MemTotal:       264039840 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.2 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.2"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

    uname -a:
       Linux X10DRW-01 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
       x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Mar 8 04:29

    SPEC is set to: /usr/cpu2006
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda2      xfs   183G  5.3G  178G   3% /
    Additional information from dmidecode:

       Warning: Use caution when you interpret this section. The 'dmidecode' program
       reads system data which is "intended to allow hardware to be accurately
       determined", but the intent may not be met, as there are frequent changes to
       hardware, firmware, and the "DMTF SMBIOS" standard.

      BIOS American Megatrends Inc. 2.0 12/17/2015
      Memory:
```

Continued on next page

Platform Notes (Continued)

```
      16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz

   (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
     450.soplex: -DSPEC_CPU_LP64
     453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
       465.tonto: -DSPEC_CPU_LP64
```

Continued on next page

Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jun 30 12:43:35 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 4 April 2016.