SPEC® CFP2006 Result

Huawei
Huawei CH121 V3 (Intel Xeon E5-2683 v4)

SPECfp®_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

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Copies
410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
470.lbm
481.wrf
482.sphinx3
483.openoffice
484.libquantum
485.mc
491.milc
492.gromacs
Huawei CH121 V3 (Intel Xeon E5-2683 v4)

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
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<tbody>
<tr>
<td>CPU Name: Intel Xeon E5-2683 v4</td>
<td>Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz</td>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>CPU MHz: 2100</td>
<td>Auto Parallel: No</td>
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<tr>
<td>FPU: Integrated</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>CPU(s) orderable: 1.2 chip</td>
<td>Base Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Peak Pointers: 32/64-bit</td>
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<tr>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
<td>Other Software: None</td>
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<tr>
<td>L3 Cache: 40 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache: None</td>
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<tr>
<td>Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
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<tr>
<td>Disk Subsystem: 1 x 600GB SAS, 10000 RPM</td>
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<tr>
<td>Other Hardware: None</td>
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</tbody>
</table>

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Results Table

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</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
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Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /spec/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Nov 25 05:06:22 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2683 v4 @ 2.10GHz
  2 "physical id"s (chips)
  64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores : 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores : 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 20480 KB

From /proc/meminfo
MemTotal: 63565104 kB
MemFree: Total: 0
HugePages_Total: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Non-compliant
SPEC CFP2006 Result

Huawei

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Platform Notes (Continued)

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Nov 23 05:55

SPEC is set to: /spec/spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 549G 121G 428G 23% /

Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insysde Corp. 3.09 02/22/2016
Memory:
8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/spec16/libs/32:/spec/spec16/libs/64:/spec/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transarent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model

Non-Compliant
Huawei

Huawei CH121 V3 (Intel Xeon E5-2683 v4)

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<tr>
<th>SPECfp_rate2006</th>
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<tbody>
<tr>
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### Base Compiler Invocation

- C benchmarks: `icc -m64`
- C++ benchmarks: `icpc -m64`
- Fortran benchmarks: `ifort -m64`
- Benchmarks using both Fortran and C: `icc -m64 ifort -m64`

### Base Portability Flags

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`
- 435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
- 436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
- 437.leslie3d: `-DSPEC_CPU_LP64`
- 444.namd: `-DSPEC_CPU_LP64 -nofor_main`
- 447.dealII: `-DSPEC_CPU_LP64`
- 450.soplex: `-DSPEC_CPU_LP64`
- 453.povray: `-DSPEC_CPU_LP64`
- 454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
- 459.GemsFDTD: `-DSPEC_CPU_LP64 -nofor_main`
- 465.tonto: `-DSPEC_CPU_LP64`
- 470.lbm: `-DSPEC_CPU_LP64`
- 481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- 482.sphinx3: `-DSPEC_CPU_LP64`

### Base Optimization Flags

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
tce   -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
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436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
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447.dealII: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -opt-mem-layout-trans=3 (pass 2)
-prof-use (pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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