Huawei

Huawei 5288 V3 (Intel Xeon E5-2630 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPECfp®2006 = NC
SPECfp_base2006 = NC

Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Non-Compliant

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
Huawei 5288 V3 (Intel Xeon E5-2630 v4)

**SPEC CFP2006 Result**

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**CPU2006 license:** 3175  
**Test date:** Feb-2016  
**Test sponsor:** Huawei  
**Hardware Availability:** Mar-2016  
**Tested by:** Huawei  
**Software Availability:** Mar-2016

---

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### Hardware

<table>
<thead>
<tr>
<th>Feature</th>
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</tr>
</thead>
<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon E5-2630 v4</td>
</tr>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.10 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>2200</td>
</tr>
<tr>
<td>FPU:</td>
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</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>20 cores, 2 chips, 10 cores/chip</td>
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<tr>
<td>CPU(s) orderable:</td>
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<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache:</td>
<td>25 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache:</td>
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<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)</td>
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<td>Disk Subsystem:</td>
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<td>Other Hardware:</td>
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### Software

<table>
<thead>
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<tr>
<td>Operating System:</td>
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<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel:</td>
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<td>File System:</td>
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<td>System State:</td>
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<td>Base Pointers:</td>
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Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1

Continued on next page
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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
- 2 "physical id"s (chips)
- 20 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 10
  - siblings : 10
  - physical 0: cores 0 2 3 4 8 9 10 11 12
  - physical 1: cores 0 1 3 4 8 9 10 11 12
- cache size : 25600 KB

From /proc/meminfo
- MemTotal: 263570712 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.0 (Maipo)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="7.0"
  - PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  - ANSI_COLOR="0;31"
  - CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
- redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
- Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 15 12:57
SPEC is set to: /spec16

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SPEC CFP2006 Result

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Platform Notes (Continued)

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 443G 12G 431G 3% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met due to frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 7.11 02/14/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 2133 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "10"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64
C++ benchmarks:
icpc -m64
Fortran benchmarks:
ifort -m64
Huawei
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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

Base Portability Flags

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias
```
Huawei

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SPECfp_base2006 = NC

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</table>

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### Peak Compiler Invocation

- **C benchmarks:**
  - icc -m64
- **C++ benchmarks:**
  - icpc -m64
- **Fortran benchmarks:**
  - ifort -m64
- **Benchmarks using both Fortran and C:**
  - icc -m64 ifort -m64

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

- **C benchmarks:**
  - 433.milc: basepeak = yes
  - 470.lbm: basepeak = yes
  - 482.sphinx3: basepeak = yes
- **C++ benchmarks:**
  - 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    - ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    - par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
    - auto-ilp32
  - 447.dealII: basepeak = yes
  - 450.soplex: basepeak = yes
- **Fortran benchmarks:**
  - 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    - ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    - par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

Continued on next page
Peak Optimization Flags (Continued)

453.povray (continued):
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-prefetch options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

466.cactusADM: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html
## SPEC CFP2006 Result

### Huawei

**Huawei 5288 V3(Intel Xeon E5-2630 v4)**

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<th>Category</th>
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You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)

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For questions about this result, please contact the tester.

For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.


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