Huawei 5288 V3(Intel Xeon E5-2623 v4)

<table>
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<th>CPU2006 license: 3175</th>
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<table>
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<tr>
<td>416.gamess</td>
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<tr>
<td>433.milc</td>
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<td>434.zeusmp</td>
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<td>435.gromacs</td>
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SPECfp_rate_base2006 = NC
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**Hardware**
- **CPU Name:** Intel Xeon E5-2623 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz
- **CPU MHz:** 2600
- **FPU:** Integrated
- **CPU(s) enabled:** 8 cores, 2 chips, 4 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1.2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 10 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
- **Disk Subsystem:** 1 x 500 GB SATA, 7200 RPM
- **Other Hardware:** None

**Software**
- **Operating System:** Red Hat Enterprise Linux Server release 7.0 (Gnome) 3.10.0-123.el7.x86_64
- **Compiler:** 
  - C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
  - Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** No
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32/64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** None
Huawei

Huawei 5288 V3(Intel Xeon E5-2623 v4)

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CPU2006 license: 3175
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Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set SnooP mode to ES mode
Set Patrol Scrub to Disable
Huawei

Huawei 5288 V3(Intel Xeon E5-2623 v4)

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SPECfp_rate_base2006 = NC

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Platform Notes (Continued)

Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5e2859f7eab8129219e1
running on localhost.localdomain Tue Mar 8 02:54:35 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2623 v4 @ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 10240 KB

From /proc/meminfo
MemTotal: 263571176 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.0 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.0"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

Continued on next page
SPEC CFP2006 Result

Huawei

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Platform Notes (Continued)

run-level 3 Mar 5 04:55
SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 443G 39G 404G 9% /
Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
8x Samsung M393A2G40EB1-PC1 16 GB 1 rank 2400 MHz, configured at 2133 MHz
8x Samsung M393A2G40EB1-PC1 16 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH="/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>/proc/sys/vm/drop_caches
numactl --interleave=all runspec <etc>

Non-Compliant

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64 -nofor_main
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Continued on next page
Huawei

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### Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-opt-prefetch`  
- `-opt-mem-layout-trans=3`

### Peak Compiler Invocation

<table>
<thead>
<tr>
<th>C benchmarks:</th>
<th>icc -m64</th>
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<tr>
<td>C++ benchmarks:</td>
<td>icpc -m64</td>
</tr>
<tr>
<td>Fortran benchmarks:</td>
<td>ifort -m64</td>
</tr>
<tr>
<td>Benchmarks using both Fortran and C:</td>
<td>icc -m64 ifort -m64</td>
</tr>
</tbody>
</table>

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

#### C benchmarks:
- `433.milc`: `basepeak = yes`
- `270.lbm`: `basepeak = yes`
- `482.sphinx3`: `basepeak = yes`

#### C++ benchmarks:
- `444.namd`: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32`
Huawei

SPEC CFP2006 Result

Huawei 5288 V3(Intel Xeon E5-2623 v4)

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem:layout-trans=3(pass 2) -prof-use(pass 2) -ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level= -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem:layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes

Non-Compliant
Huawei
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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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