SPEC® CFP2006 Result

Huawei

Huawei XH620 V3 (Intel Xeon E5-2630 v4)

<table>
<thead>
<tr>
<th>SPECfp®_rate2006</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>NC</td>
</tr>
</tbody>
</table>

CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei  
Test date: Feb-2016  
Hardware Availability: Mar-2016  
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

<table>
<thead>
<tr>
<th>Copies</th>
<th>410.bwaves</th>
<th>416.gamess</th>
<th>433.milc</th>
<th>434.zeusmp</th>
<th>435.gromacs</th>
<th>436.cactusADM</th>
<th>437.leslie3d</th>
<th>444.namd</th>
<th>447.dealII</th>
<th>450.soplex</th>
<th>453.povray</th>
<th>454.calculix</th>
<th>459.GemsFDTD</th>
<th>465.tonto</th>
<th>470.lbm</th>
<th>481.wrf</th>
<th>482.sphinx3</th>
</tr>
</thead>
</table>

Non-Compliant
Huawei

Huawei XH620 V3 (Intel Xeon E5-2630 v4)

### SPEC CFP2006 Result

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#### Hardware

<table>
<thead>
<tr>
<th>Field</th>
<th>Specification</th>
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</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2630 v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.10 GHz</td>
</tr>
<tr>
<td>CPU MHZ</td>
<td>2200</td>
</tr>
<tr>
<td>FPU</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled</td>
<td>20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable</td>
<td>1,2 chip</td>
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<tr>
<td>Primary Cache</td>
<td>32 KB I + 32 KB D on chip per core</td>
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<tr>
<td>Secondary Cache</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache</td>
<td>25 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)</td>
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<tr>
<td>Disk Subsystem</td>
<td>1 x 500 GB SATA, 7200 RPM</td>
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<tr>
<td>Other Hardware</td>
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#### Software

<table>
<thead>
<tr>
<th>Field</th>
<th>Specification</th>
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<tbody>
<tr>
<td>Operating System</td>
<td>Red Hat Enterprise Linux Server release 7.0 (el7)</td>
</tr>
<tr>
<td>Compiler C/C++</td>
<td>Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
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<tr>
<td>Compiler Fortran</td>
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<td>Auto Parallel</td>
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<td>File System</td>
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<tr>
<td>System State</td>
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<tr>
<td>Base Pointers</td>
<td>32/64-bit</td>
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<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
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<tr>
<td>Other Software</td>
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</table>
SPEC CFP2006 Result

Huawei
Huawei XH620 V3(Intel Xeon E5-2630 v4)

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Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

A config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable

Non-Compliant
SPEC CFP2006 Result

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Platform Notes (Continued)

Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5e285932ceab88219e1
running on localhost.localdomain Sun Feb 21 16:52:46 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
2 "physical id"s (chips)
40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal: 263568392 kB
HugePages_Total:       0
HugePages_Free:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

Continued on next page
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Platform Notes (Continued)

run-level 3 Feb 17 06:23

SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 443G 80G 363G 19% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 7.11 02/14/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 2133 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH="/spec16/lib32:/spec16/lib64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model

Base Compiler Invocation

C benchmarks:
 icc -m64

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**Base Compiler Invocation (Continued)**

C++ benchmarks:
```
icpc -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc -m64 ifort -m64
```

**Base Portability Flags**

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

**Base Optimization Flags**

C benchmarks:
```
xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```
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Base Optimization Flags (Continued)
Fortran benchmarks:
- xCORE-AVX2
- -ipo -03 -no-prec-div -opt-prefetch
Benchmarks using both Fortran and C:
- xCORE-AVX2
- -ipo -03 -no-prec-div -opt-prefetch auto-p32
- -ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation
C benchmarks:
icc -m64
C++ benchmarks (except as noted below):
icpc -m64
450.soplex: icpc -m32 -l/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
Fortran benchmarks:
ifort -m64
Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags
410.bwaves: -DSPEC_CPU_LP64
416.csmith: -DSPEC_CPU_LP64
429.gcc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
451.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.le3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
Huawei

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Peak Portability Flags (Continued)

465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -fno-alias -auto-ilkp32

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pas 2) -prof-gen:threadsafe(pas 1)
-ipo(pas 2) -O3(pas 1) -no-prec-div(pas 2)
-par-num-threads=1(pas 1) -prof-use(pas 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:
435.gromacs: -xCORE-AVX2(pas 2) -prof-gen:threadsafe(pas 1)
-ipo(pas 2) -O3(pas 1) -no-prec-div(pas 2)
-par-num-threads=1(pas 1) -prof-use(pas 2) -unroll4 -auto
-ipc -opt-mem-layout-trans=3
-parallel -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
**SPEC CFP2006 Result**

*Huawei*

**Huawei XH620 V3 (Intel Xeon E5-2630 v4)***

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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on Open Source Software availability policy.

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