Huawei

Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

<table>
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<td>CPU2006 license: 3175</td>
<td>Test date: Mar-2016</td>
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<td>Test sponsor: Huawei</td>
<td>Hardware Availability: Mar-2016</td>
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<td>Tested by: Huawei</td>
<td>Software Availability: Mar-2016</td>
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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

Copies

| 410.bwaves                       |
| 416.gamess                        |
| 433.milc                          |
| 434.zeusmp                        |
| 435.gromacs                       |
| 436.cactusADM                     |
| 437.leslie3d                      |
| 444.namd                          |
| 447.dealII                        |
| 450.soplex                        |
| 453.povray                        |
| 454.calculix                      |
| 459.GemsFDTD                      |
| 465.tonto                         |
| 4.0.lbm                           |
| 481.wrf                           |
| 482.sphinx3                       |
Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

Huawei

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

CPU Name: Intel Xeon E5-2660 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 1 T SATA, 7200 RPM
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Non-Compliant
Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Results Table

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numaclt commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU run and reporting rules.

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb866 6fa285b82ceab81e28219e1
running on localhost.localdomain Thu Mar 10 05:37:33 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 7
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

From /proc/meminfo
MemTotal: 65566032 kB
BufPages: Total: 0
HugePages: Total: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
SPEC CFP2006 Result

Huawei

Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016
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Platform Notes (Continued)

uname -a:
    Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
    EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 9 05:12

SPEC is set to: /speccpu/spec16
    Filesystem Type Size Used Avail Use% Mounted on
    /dev/sda2 ext4 865G 58G 764G 8% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
    8x NO DIMM NO DIMM 3 rank
    8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
    8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
RedHat EL 7.1
Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
File system page cache cleared with:
    echo 1> /proc/sys/vm/drop_caches
runcspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006 = NC</th>
<th>SPECfp_rate_base2006 = NC</th>
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CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

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---

**Base Compiler Invocation**

<table>
<thead>
<tr>
<th>C benchmarks:</th>
<th>icc -m64</th>
</tr>
</thead>
<tbody>
<tr>
<td>C++ benchmarks:</td>
<td>icpc -m64</td>
</tr>
<tr>
<td>Fortran benchmarks:</td>
<td>ifort -m64</td>
</tr>
<tr>
<td>Benchmarks using both Fortran and C:</td>
<td>icc -m64 ifort -m64</td>
</tr>
</tbody>
</table>

---

**Base Portability Flags**

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64 -nofor_main
450.soplex: -DSPEC_CPU_LP64 -nofor_main
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64 -nofor_main
470.lbm: -DSPEC_CPU_LP64 -nofor_main
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

---

**Base Optimization Flags**


Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ipo -O3 -no-prec-div -opt-prefetch -ipo -O3 -no-prec-div -opt-prefetch -ipo -O3 -no-prec-div -opt-prefetch -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

Non-Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

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<tr>
<th>Benchmark</th>
<th>Flags</th>
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<td>-D_FILE_OFFSET_BITS=64</td>
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<td>-DSPEC_CPU_LP64</td>
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<td>454.calculix</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
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<td>459.GemsFDTD</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
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**Peak Optimization Flags**

**C benchmarks:**

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<tr>
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<td>433.milc</td>
<td>basepeak = yes</td>
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<tr>
<td>470.lbm</td>
<td>basepeak = yes</td>
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<tr>
<td>482.sphinx3</td>
<td>basepeak = yes</td>
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**C++ benchmarks:**

<table>
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<td>444.namd</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32</td>
</tr>
<tr>
<td>447.dealII</td>
<td>basepeak = yes</td>
</tr>
<tr>
<td>450.soplex</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3</td>
</tr>
<tr>
<td>453.povray</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias</td>
</tr>
</tbody>
</table>

For Fortran benchmarks:

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Continued on next page
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2660 v4)

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- **CPU2006 license:** 3175
- **Test sponsor:** Huawei
- **Test date:** Mar-2016
- **Tested by:** Huawei
- **Hardware Availability:** Mar-2016
- **Software Availability:** Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

### Peak Optimization Flags (Continued)

- **410.bwaves:** basepeak = yes
- **416.gamess:** -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
  -inline-level=0 -scalar-rep-
- **434.zeusmp:** basepeak = yes
- **437.leslie3d:** basepeak = yes
- **459.GemsFDTD:** basepeak = yes
  -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
  -inline-calloc -opt-malloc-options=3
- **465.tonto:** -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
  -inline-calloc -opt-mem-layout-trans=3
- **435.gromacs:** -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
  -prof-use(pass 2) -opt-prefetch -auto-ilp32
- **436.cactusADM:** basepeak = yes
  -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
  -prof-use(pass 2) -opt-prefetch -auto-ilp32
- **437.leslie3d:** basepeak = yes
- **454.calculix:** basepeak = yes
- **481.wrf:** basepeak = yes

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html](http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)
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**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Mar-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).