SPEC® CFP2006 Result

Huawei

Huawei RH2288 V3 (Intel Xeon E5-2650 v4)

<table>
<thead>
<tr>
<th>SPECfp®2006 = NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_base2006 = NC</td>
</tr>
</tbody>
</table>

CPU2006 license: 3175
Test sponsor: Huawei
Test date: Mar-2016
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">generality</a>.

Non-Compliant

```
410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFD
465.tonto
468.lbm
481.wrf
482.sphinx3
```
Huawei RH2288 V3 (Intel Xeon E5-2650 v4)

Hardware
- CPU Name: Intel Xeon E5-2650 v4
- CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
- CPU MHz: 2200
- CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip
- CPU(s) orderable: 1, 2 chip
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core
- L3 Cache: 30 MB I+D on chip per chip
- Other Cache: None
- Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
- Disk Subsystem: 1 x 1000 GB SATA, 7200rpm
- Other Hardware: None

Software
- Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
- Compiler:
  - C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
  - Fortran: Version 16.0.0.101 of Intel Fortran
- Auto Parallel: Yes
- File System: ext4
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: 32/64-bit
- Other Software: None

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
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<tbody>
<tr>
<td>410.bwaves</td>
<td>NC</td>
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<td>NC</td>
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<tr>
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<td>NC</td>
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<tr>
<td>437.leslie3d</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>444.namd</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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</tr>
<tr>
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<td>NC</td>
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</tr>
<tr>
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</tr>
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<td>481.wrf</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #"e3fbb8667b5a285932ceab81e28219e1"
running on localhost.localdomain Thu Jan 16 11:09:35 2014

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) CPU E5-2650 v4 @ 2.20GHz
- 2 "physical id"s (chips)
- 24 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 12
  - siblings : 12
  - physical 0: cores 0 2 3 4 5 8 9 10 11 12 13
  - physical 1: cores 0 1 3 4 5 8 9 10 11 12 13
- cache size : 30720 KB

From /proc/meminfo
- MemTotal: 263570248 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.0 (Maipo)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="7.0"
- PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
- CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
- Redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
- system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
- Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
  EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
- run-level 3
- SPEC is set to: /spec16

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Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>ext4</td>
<td>913G</td>
<td>91G</td>
<td>776G</td>
<td>11%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met when there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 7.11 02/14/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "14"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
```bash
icc -m64
```

C++ benchmarks:
```bash
icpc -m64
```

Fortran benchmarks:
```bash
ifort -m64
```
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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```bash
icc   -m64 ifort -m64
```

Base Portability Flags

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
  - ansi-alias

C++ benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
  - ansi-alias

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Peak Compiler Invocation

| C benchmarks: | icc -m64 |
| C++ benchmarks: | icpc -m64 |
| Fortran benchmarks: | ifort -m64 |
| Benchmarks using both Fortran and C: | icc -m64 ifort -m64 |

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

| C benchmarks: |
| 433.milc: basepeak = yes |
| 470.lbm: basepeak = yes |
| 482.sphinx3: basepeak = yes |

| C++ benchmarks: |
| 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32 |
| 447.dealII: basepeak = yes |
| 450.soplex: basepeak = yes |
| 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 |

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Peak Optimization Flags (Continued)

453.povray (continued):
   -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.game5: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

453.Gem5: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
            -parallel-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

811.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For other inquiries, please contact webmaster@spec.org.

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