Huawei XH620 V3 (Intel Xeon E5-2640 v4) SPECint\_rate2006 = NC
SPECint\_rate_base2006 = NC

<table>
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<tbody>
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</tr>
</tbody>
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Huawei

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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

<table>
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</tr>
<tr>
<td>473.astar</td>
</tr>
<tr>
<td>483.xalancbmk</td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>Hardware Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon E5-2640 v4</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz</td>
</tr>
<tr>
<td>CPU MHz: 2000</td>
</tr>
<tr>
<td>FPU: Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable: 1,2 chip</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3 Cache: 25 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other Cache: None</td>
</tr>
<tr>
<td>Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)</td>
</tr>
<tr>
<td>Disk Subsystem: 1 x 1000 GB SATA, 7200rpm</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Software Characteristics</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
</tr>
<tr>
<td>Auto Parallel: No</td>
</tr>
<tr>
<td>File System: ext4</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 32-bit</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Other Software: Microquill SmartHeap V10.2</td>
</tr>
</tbody>
</table>
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.
Huawei

Huawei XH620 V3 (Intel Xeon E5-2640 v4)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes (Continued)

running on localhost.localdomain Mon Jan 20 12:11:52 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
2 "physical id"s (chips)
40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal:       263568392 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jan 20 12:10

Continued on next page
SPEC CINT2006 Result

Huawei
Huawei XH620 V3 (Intel Xeon E5-2640 v4)

SPECint_rate2006 = NC
SPECint_rate_base2006 = NC
CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

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Platform Notes (Continued)

SPEC is set to: /spec16
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      ext4  913G   94G  773G  11% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 7.11 02/14/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 2133 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent huge pages enabled with:
transparent=always /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runcp command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model

Base Compiler Invocation

C benchmarks:
  icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
  icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

**Base Portability Flags**

- 400.perlbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX -DIA32
- 401.bzip2: -D_FILE_OFFSET_BITS=64
- 403.gcc: -D_FILE_OFFSET_BITS=64
- 429.mcf: -D_FILE_OFFSET_BITS=64
- 445.gobmk: -D_FILE_OFFSET_BITS=64
- 456.hmmer: -D_FILE_OFFSET_BITS=64
- 458.sjeng: -D_FILE_OFFSET_BITS=64
- 462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
- 464.h264ref: -D_FILE_OFFSET_BITS=64
- 471.omnetpp: -D_FILE_OFFSET_BITS=64
- 473.astar: -D_FILE_OFFSET_BITS=64
- 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

**Base Optimization Flags**

- C benchmarks:
  - -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
  - -opt-mem-layout-trns=3
- C++ benchmarks:
  - -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
  - -opt-mem-layout-trns=3 -Wl,-z,muldefs -L/sh -lsmartheap

**Base Other Flags**

- C benchmarks:
  - -Dalloca=_alloca

**Peak Compiler Invocation**

- C benchmarks (except as noted below):
  - icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
  - 400.perlbmk: icc -m64

Continued on next page
SPEC CINT2006 Result

Huawei
Huawei XH620 V3 (Intel Xeon E5-2640 v4)

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CPU2006 license: 3175
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Peak Compiler Invocation (Continued)

401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

Non Compliant
Huawei

Huawei XH620 V3 (Intel Xeon E5-2640 v4)

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SPECint_rate_base2006 = NC

CPU2006 license: 3175
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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2 (pass 2) -prof-use (pass 2) -par-num-threads=1 (pass 1) -ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xCORE-AVX2 (pass 2) -prof-use (pass 2) -par-num-threads=1 (pass 1) -ansi-alias -opt-mem-layout-trans=3
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2 (pass 2) -prof-use (pass 2) -par-num-threads=1 (pass 1) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

C++ benchmarks:

471.omnetpp: -xCORE-AVX2 (pass 2) -prof-use (pass 1) -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2) -par-num-threads=1 (pass 1) -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes
492.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Non-Compliant
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**CPU2006 license:** 3175

**Test sponsor:** Huawei

**Tested by:** Huawei

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Mar-2016

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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)

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For questions about this result, please contact the tester.

For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.


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