SPEC® CINT2006 Result

Huawei

Huawei XH622 V3 (Intel Xeon E5-2643 v4)

SPECint®2006 = NC
SPECint_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016

Test sponsor: Huawei
Hardware Availability: Mar-2016

Tested by: Huawei
Software Availability: Mar-2016

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**Hardware**

- **CPU Name:** Intel Xeon E5-2643 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz
- **CPU MHz:** 3400
- **FPU:** Integrated
- **CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip
- **CPU(s) orderable:** 1,2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 20 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
- **Disk Subsystem:** 1 x 1000 GB SATA, 7200rpm
- **Other Hardware:** None

**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
- **Compiler:** CIC++ Version 16.0.0.101 of Intel C++ Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32/64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.2
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Results Table

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</tbody>
</table>

Submit Notes

The config file option "submit" was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIO configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e8219e1
running on localhost.localdomain Sun Feb  2 12:51:50 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

Continued on next page
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Platform Notes (Continued)

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 2 3 6 7
physical 1: cores 0 2 3 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal: 263571640 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 2 12:51
SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 913G 108G 759G 13% /

Continued on next page
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### Platform Notes (Continued)

Warning: Use caution when you interpret this section. The `dmidecode` program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- **BIOS**: Insyde Corp. 3.12 03/03/2016
- **Memory**:
  - 8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
  - 8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

---

### General Notes

Environment variables set by `runspec` before the start of the run:

- `KMP_AFFINITY = "granularity=fine,compact,1,0"
- `LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
- `OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1.

Transparent Huge Pages enabled with:

```bash
echo always > /sys/kernel/mm/transparent_hugepage/enabled
echo --interleave=all > /sys/kernel/mm/transparent_hugepage/enabled
echo --disable > /sys/kernel/mm/transparent_hugepage/disabled
```

The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.

The results have been measured on a Huawei XH620 V3 model.

---

### Base Compiler Invocation

- **C benchmarks**:
  - `icc -m64`
- **C++ benchmarks**:
  - `icpc -m64`
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Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs /sh -L/usr/lib64 -lsmartheap64

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -ansi-alias
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32
429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Non-Compliant

Continued on next page
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<td>445.gobmk</td>
<td>-xCORE-AVX2 (pass 2) -prof-gen:threadsafepass1 -prof-use (pass 2) -par-num-threads=1 (pass 1) -ansi-alias</td>
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<tr>
<td>456.hmmer</td>
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<tr>
<td>462.libquantum</td>
<td>basepeak = yes</td>
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C++ benchmarks:

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<td>-xCORE-AVX2 (pass 2) -prof-gen:threadsafepass1 -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2) -par-num-threads=1 (pass 1) -prof-use (pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,undefined -L/sh -lsmarthep</td>
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<td>473.astar</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto -32 -Wl,-z,undefined -L/sh -lsmarthep64</td>
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Peak Other Flags

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.  
Originally published on  7 April 2016.  
Originally published on  7 April 2016.