SPEC® CFP2006 Result

Huawei
Huawei 5288 V3 (Intel Xeon E5-2660 v4)

SPECfp®_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175  Test date: Mar-2016
Test sponsor: Huawei  Hardware Availability: Mar-2016
Tested by: Huawei  Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Copies

410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
466.lbm
481.wrf
482.sphinx3
**SPEC CFP2006 Result**

**Huawei**

**Huawei 5288 V3 (Intel Xeon E5-2660 v4)**

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### Hardware

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<tr>
<th>Specification</th>
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<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon E5-2660 v4</td>
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<tr>
<td><strong>CPU Characteristics:</strong></td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
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<tr>
<td><strong>CPU MHz:</strong></td>
<td>2000</td>
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<tr>
<td><strong>FPU:</strong></td>
<td>Integrated</td>
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<tr>
<td><strong>CPU(s) enabled:</strong></td>
<td>28 cores, 2 chips, 14 cores/chip, 2 threads/core</td>
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<td><strong>CPU(s) orderable:</strong></td>
<td>1.2 chip</td>
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<td><strong>Primary Cache:</strong></td>
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<td><strong>L3 Cache:</strong></td>
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### Software

<table>
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<tr>
<td><strong>Operating System:</strong></td>
<td>Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
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<tr>
<td><strong>Compiler:</strong></td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<td><strong>System State:</strong></td>
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<tr>
<td><strong>Base Pointers:</strong></td>
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<td><strong>Peak Pointers:</strong></td>
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<td><strong>Other Software:</strong></td>
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</table>
Huawei

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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numaactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"
Huawei

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Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Baseboard Management Controller used to adjust the fan speed to 100%
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $e3fbb8667b1885932ceab81e28219e1
running on localhost.localdomain Fri May 14 14:07 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
2 "physical id"s (chips)
56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 7
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

From /proc/meminfo
MemTotal: 263566032 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
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Platform Notes (Continued)
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jan 27 14:05

SPEC is set to: /spec16
Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/17/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

General Notes
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
SPEC CFP2006 Result

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<table>
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<tr>
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Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

Base Portability Flags

- `410.bwaves: -DSPEC_CPU_LP64`
- `416.gamess: -DSPEC_CPU_LP64`
- `433.milc: -DSPEC_CPU_LP64`
- `434.zeusmp: -DSPEC_CPU_LP64`
- `435.gromacs: -DSPEC_CPU_LP64 -nofor_main`
- `436.cactusADM: -DSPEC_CPU_LP64 -nofor_main`
- `437.leslie3d: -DSPEC_CPU_LP64`
- `444.namd: -DSPEC_CPU_LP64 -nofor_main`
- `447.dealII: -DSPEC_CPU_LP64`
- `450.soplex: -DSPEC_CPU_LP64`
- `455.povray: -DSPEC_CPU_LP64`
- `456.calculix: -DSPEC_CPU_LP64 -nofor_main`
- `459.GemsFDTD: -DSPEC_CPU_LP64 -nofor_main`
- `465.tonto: -DSPEC_CPU_LP64`
- `470.lbm: -DSPEC_CPU_LP64`
- `481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- `482.sphinx3: -DSPEC_CPU_LP64`

Base Optimization Flags

- **C benchmarks:**
  - `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
  - `-ansi-alias -opt-mem-layout-trans=3`

Continued on next page
Huawei

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**SPECfp_rate_base2006 = NC**

<table>
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### Base Optimization Flags (Continued)

For C++ benchmarks:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-opt-prefetch`  
- `-auto-p32`  
- `-ansi-alias`  
- `-opt-mem-layout-trans=3`

For Fortran benchmarks:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-opt-prefetch`

For benchmarks using both Fortran and C:
- `-xCORE-AVX2`  
- `-ipo`  
- `-O3`  
- `-no-prec-div`  
- `-opt-prefetch`  
- `-auto-p32`  
- `-ansi-alias`  
- `-opt-mem-layout-trans=3`

### Peak Compiler Invocation

For C benchmarks:
- `icc`  
- `-m64`

For C++ benchmarks (except as noted below):
- `icpc`  
- `-m64`

450.soplex: `icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

For Fortran benchmarks:
- `ifort`  
- `-m64`

For benchmarks using both Fortran and C:
- `icc`  
- `-m64`
- `ifort`  
- `-m64`

### Peak Portability Flags

- 310.bwaves: `--DSPEC_CPU_LP64`
- 416.gamess: `--DSPEC_CPU_LP64`
- 433.milc: `--DSPEC_CPU_LP64`
- 434.zeusmp: `--DSPEC_CPU_LP64`
- 435.gromacs: `--DSPEC_CPU_LP64`  
- `--nofor_main`
- 436.cactusADM: `--DSPEC_CPU_LP64`  
- `--nofor_main`
- 437.leslie3d: `--DSPEC_CPU_LP64`
- 444.namd: `--DSPEC_CPU_LP64`
- 447.dealII: `--DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -fno-alias -auto-ilp32
447.dealII: basepeak = yes
459.solver: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
            -prof-use(pass 2) -opt-malloc-options=3

Fortran benchmarks:

Continued on next page
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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-calloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
Huawei

Huawei 5288 V3 (Intel Xeon E5-2660 v4)

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**CPU2006 license:** 3175
**Test sponsor:** Huawei
**CPU2006 license:** 3175
**Tested by:** Huawei

**Test date:** Mar-2016
**Hardware Availability:** Mar-2016
**Software Availability:** Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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