Huawei RH2288 V3 (Intel Xeon E5-2650L v4)

| SPECint®2006 | = | NC |
| SPECint_base2006 | = | NC |

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

### Hardware

- **CPU Name:** Intel Xeon E5-2650L v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 2.50 GHz
- **CPU MHz:** 1700
- **FPU:** Integrated
- **CPU(s) enabled:** 28 cores, 2 chips, 14 cores/chip
- **CPU(s) orderable:** 1,2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 35 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
- **Disk Subsystem:** 1 x 1000 GB SATA, 7200rpm
- **Other Hardware:** None

### Software

- **Operating System:** Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32/64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V10.2
## SPEC CINT2006 Result

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Seconds</th>
<th>Ratio</th>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The config file option 'submit' was used.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Platform Notes**

- **BIOS configuration:**  
  - Set Power Efficiency Mode to Custom  
  - Set Snooze Mode to ES mode  
  - Set Patrol Scrub to Disable  
  - Set Hyper-Threading to Disable  
- Sysinfo program /spec16/config/sysinfo.rev6914  
  - $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1 running on localhost.localdomain Mon Feb 24 17:22:20 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
Huawei RH2288 V3 (Intel Xeon E5-2650L v4)

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Huawei

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Platform Notes (Continued)

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v4 @ 1.70GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 14
  siblings : 14
  physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
  cache size : 35840 KB

From /proc/meminfo
MemTotal: 263569784 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 23 22:21
SFP is set to: /spec16
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/sda2  ext4 913G 108G 759G 13% /

Additional information from dmidecode:

Continued on next page
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Platform Notes (Continued)

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.12 03/03/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

icc -m64
icpc -m64

C++ benchmarks:
icp -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
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Base Portability Flags (Continued)

- 401.bzip2: -D SPEC_CPU_LP64
- 403.gcc: -D SPEC_CPU_LP64
- 429.mcf: -D SPEC_CPU_LP64
- 445.gobmk: -D SPEC_CPU_LP64
- 456.hmmer: -D SPEC_CPU_LP64
- 458.sjeng: -D SPEC_CPU_LP64
- 462.libquantum: -D SPEC_CPU_LP64 -D SPEC_CPU_LINUX
- 464.h264ref: -D SPEC_CPU_LP64
- 471.omnetpp: -D SPEC_CPU_LP64
- 473.astar: -D SPEC_CPU_LP64
- 483.xalancbmk: -D SPEC_CPU_LP64 -D SPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Non-Compliant
SPEC CINT2006 Result

Huawei

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Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

<table>
<thead>
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</tr>
</thead>
<tbody>
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<td>-DSPEC_CPU_LP64</td>
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<td>-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX</td>
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<td>471.omnetpp</td>
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<td>-DSPEC_CPU_LP64</td>
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Peak Optimization Flags

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<td>400.perlbench</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch -ansi-alias</td>
</tr>
<tr>
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<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias</td>
</tr>
<tr>
<td>453.gcc</td>
<td>-xCORE-AVX2 -ipo -03 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32</td>
</tr>
<tr>
<td>429.mcf</td>
<td>-xCORE-AVX2 -ipo -03 -no-prec-div -parallel -opt-prefetch -auto-p32</td>
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SPEC CINT2006 Result

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 2)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 1) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Daloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
## SPEC CINT2006 Result

### Huawei

**Huawei RH2288 V3 (Intel Xeon E5-2650L v4)**

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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