| SPECfp®2006 = NC | SPECfp_base2006 = NC |

Huawei

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC OSG policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

```
410.bwaves
416.gamess
433.milc
434.zeusmp
435.gromacs
436.cactusADM
437.leslie3d
444.namd
447.dealII
450.soplex
453.povray
454.calculix
459.GemsFDTD
465.tonto
470.lbm
481.wrf
482.sphinx3
```
### SPEC CFP2006 Result

**Huawei**

Huawei XH628 V3 (Intel Xeon E5-2650L v4)  |  SPECfp2006 = NC  |  SPECfp_base2006 = NC

- **CPU2006 license:** 3175
- **Test sponsor:** Huawei
- **Tested by:** Huawei
- **Test date:** Mar-2016
- **Hardware Availability:** Mar-2016
- **Software Availability:** Mar-2016

**SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.**

<table>
<thead>
<tr>
<th><strong>Hardware</strong></th>
<th><strong>Software</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon E5-2650L v4</td>
<td><strong>Operating System:</strong> Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td><strong>CPU Characteristics:</strong> Intel Turbo Boost Technology up to 2.50 GHz</td>
<td><strong>Compiler:</strong> C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td><strong>CPU MHz:</strong> 1700</td>
<td><strong>Auto Parallel:</strong> Yes</td>
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<tr>
<td><strong>FPU:</strong> Integrated</td>
<td><strong>File System:</strong> ext4</td>
</tr>
<tr>
<td><strong>CPU(s) enabled:</strong> 28 cores, 2 chips, 14 cores/chip</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
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<tr>
<td><strong>CPU(s) orderable:</strong> 1,2 chip</td>
<td><strong>Base Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>Primary Cache:</strong> 32 KB I + 32 KB D on chip per core</td>
<td><strong>Peak Pointers:</strong> 32/64-bit</td>
</tr>
<tr>
<td><strong>Secondary Cache:</strong> 256 KB I+D on chip per core</td>
<td><strong>Other Software:</strong> None</td>
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<td><strong>L3 Cache:</strong> 35 MB I+D on chip per chip</td>
<td><strong>Other Hardware:</strong> None</td>
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<tr>
<td><strong>Other Cache:</strong> None</td>
<td><strong>Memory:</strong> 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R) 1 x 1000 GB SATA, 7200rpm</td>
</tr>
<tr>
<td><strong>Memory:</strong> 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
<td><strong>Disk Subsystem:</strong> 1 x 1000 GB SATA, 7200rpm</td>
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<tr>
<td><strong>Disk Subsystem:</strong> 1 x 1000 GB SATA, 7200rpm</td>
<td><strong>Other Hardware:</strong> None</td>
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## SPEC CFP2006 Result

**Huawei**

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

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**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Test date:** Mar-2016  
**Hardware Availability:** Mar-2016  
**Tested by:** Huawei  
**Software Availability:** Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

### Results Table

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<tr>
<th>Benchmark</th>
<th>Seconds</th>
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<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

BIOS configuration:
- Set Power Efficiency Mode to Custom
- Set Snoop Mode to HS mode
- Set Patrol Scrub to Disable
- Set Hyper-Threading to Disable

Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1 running on localhost.localdomain Wed Feb 26 05:35:07 2014

Continued on next page
Huawei

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability policy on spec.org/cpu2006/Docs/runrules.html#rule_1.3.2 SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
  2 "physical id"s (chips)
  28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 263569784 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 25 11:05
SPEC is set to: /spec16

Continued on next page
Huawei

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

SPEC CFP2006 Result

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SPECfp2006 = NC
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Huawei

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Platform Notes (Continued)

Filesystem   Type  Size  Used  Avail  Use%  Mounted on
/dev/sda2    ext4  913G  108G  759G  13%  /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met if there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.12 03/03/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl command:
numactl -i -interleave=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
icpc  -m64

Continued on next page

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
SPEC CFP2006 Result

Huawei

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---

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort \ -m64

Benchmarks using both Fortran and C:
icc \ -m64 ifort \ -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Non-compliant
SPEC CFP2006 Result

Huawei

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

SPECfp2006 = NC

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CPU2006 license: 3175
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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64
C++ benchmarks:
icpc -m64
Fortran benchmarks:
ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
704.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page

Non-compliant
Huawei

Huawei XH628 V3 (Intel Xeon E5-2650L v4)

| SPECfp2006 = | NC |
| SPECfp_base2006 = | NC |

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
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Peak Optimization Flags (Continued)

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<th>Options</th>
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<td>dealII</td>
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<td>soplex</td>
<td>basepeak = yes</td>
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<td>povray</td>
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<tr>
<td>Fortran benchmarks</td>
<td></td>
</tr>
<tr>
<td>bwaves</td>
<td>basepeak = yes</td>
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<td>gamess</td>
<td>-xCORE-AVX2 (pass 2) -prof-gen:threads= safe (pass 1) -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2) -par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll 2 -inline-level=0 -scalar-rep</td>
</tr>
<tr>
<td>zeusmp</td>
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<td>leslie3d</td>
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<td>GemsFDTD</td>
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</tr>
<tr>
<td>tonto</td>
<td>-xCORE-AVX2 (pass 2) -prof-gen:threads= safe (pass 1) -ipo (pass 2) -O3 (pass 2) -no-prec-div (pass 2) -par-num-threads=1 (pass 1) -prof-use (pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll 4</td>
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Benchmarks using both Fortran and C:

<table>
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<tr>
<th>Benchmark</th>
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<tbody>
<tr>
<td>gromacs</td>
<td>basepeak = yes</td>
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<tr>
<td>cactusADM</td>
<td>basepeak = yes</td>
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<tr>
<td>calculix</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias</td>
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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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