Huawei RH2288H V3 (Intel Xeon E5-2603 v4)  

| SPECfp®2006 | NC |
| SPECfp_base2006 | NC |

- CPU2006 license: 3175
- Test sponsor: Huawei
- Tested by: Huawei
- Hardware Availability: Mar-2016
- Software Availability: Mar-2016
- Test date: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

### Non-Compliant

- 410.bwaves
- 416.gamess
- 433.milc
- 434.zeusmp
- 435.gromacs
- 436.cactusADM
- 437.leslie3d
- 444.namd
- 447.dealII
- 450.soplex
- 453.povray
- 454.calculix
- 459.GemsFD
- 465.tonto
- 481.wrf
- 482.sphinx3
Huawei RH2288H V3 (Intel Xeon E5-2603 v4)

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
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<td>CPU Name:</td>
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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2603 v4)

SPEC CFP2006 Result

 SPECfp2006 = NC
 SPECfp_base2006 = NC

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Non-Compliant

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
- Set Power Efficiency Mode to Custom
- Set Snoop Mode to HS mode
- Set Patrol Scrub to Disable

Sysinfo program /speccpu/spec16/config/sysinfo.rev6914

$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Mar 4 10:31:32 2016

Continued on next page
Huawei RH2288H V3 (Intel Xeon E5-2603 v4) SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
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Tested by: Huawei

Test date: Mar-2016
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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name: Intel(R) Xeon(R) CPU E5-2603 v4 @ 1.70GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 6
siblings: 6
physical 0: cores 0 2 3 4 5
physical 1: cores 0 2 3 4 5
cache size: 15360 KB

From /proc/meminfo
MemTotal: 263571640 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 4 10:26

SPEC is set to: /speccpu/spec16

Filesystem Type Size Used Avail Use% Mounted on
Continued on next page
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Huawei RH2288H V3 (Intel Xeon E5-2603 v4)

| SPECfp2006 =   | NC |
| SPECfp_base2006 = | NC |

CPU2006 license: 3175  
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Base Compiler Invocation (Continued)

```plaintext
icc  -m64 ifort -m64
```

Base Portability Flags

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias
```
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Peak Compiler Invocation

C benchmarks:
- icc -m64

C++ benchmarks:
- icpc -m64

Fortran benchmarks:
- ifort -m64

Benchmarks using both Fortran and C:
- icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

C++ benchmarks:
- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32
- 447.dealII: basepeak = yes
- 450.soplex: basepeak = yes
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
SPEC CFP2006 Result

Huawei

Huawei RH2288H V3 (Intel Xeon E5-2603 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

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Peak Optimization Flags (Continued)

453.povray (continued):
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gameess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 1) -O3(pass 1) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-special-all-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

467.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

'81.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

Non Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2603 v4)

<table>
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You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml](http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml)

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 19 April 2016.