SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by the SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

CPU2006 license: 3175
Test date: Mar-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Software Availability: Mar-2016
Non-Compliant
SPEC CFP2006 Result

Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

| SPECfp2006 | = | NC |
| SPECfp_base2006 | = | NC |

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

### Hardware

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<tr>
<th>Feature</th>
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<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2609 v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
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<tr>
<td>CPU MHz</td>
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<tr>
<td>FPU</td>
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<td>CPU(s) enabled</td>
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<tr>
<td>CPU(s) orderable</td>
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<tr>
<td>Primary Cache</td>
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<tr>
<td>Secondary Cache</td>
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<tr>
<td>L3 Cache</td>
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<tr>
<td>Other Cache</td>
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<tr>
<td>Memory</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 1866 MHz)</td>
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<td>Disk Subsystem</td>
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<tr>
<td>Other Hardware</td>
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### Software

<table>
<thead>
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<th>Value</th>
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<tbody>
<tr>
<td>Operating System</td>
<td>Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
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<tr>
<td>Compiler</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel</td>
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<tr>
<td>File System</td>
<td>ext4</td>
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<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
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<tr>
<td>Baseline Pointers</td>
<td>64-bit</td>
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<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other Software</td>
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</tbody>
</table>

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Huawei
Huawei CH222 V3 (Intel Xeon E5-2609 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

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Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>410.bwaves</td>
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<td>NC</td>
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<tr>
<td>416.gamess</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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<tr>
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<td>NC</td>
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<td>NC</td>
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</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1

Continued on next page
Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) CPU E5-2609 v4 @ 1.70GHz
- 2 "physical id"'s (chips)
- 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores: 8
- siblings: 8
- physical 0: cores 0 2 3 4 5 6
- physical 1: cores 0 2 3 4 5 6
- cache size: 20480 KB

From /proc/meminfo
- MemTotal: 263571176 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release*, /etc/*version*
- os-release:
  - NAME="Red Hat Enterprise Linux Server"
  - VERSION="7.0 (Maipo)"
  - ID="rhel"
  - ID_LIKE="fedora"
  - VERSION_ID="7.0"
  - PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  - ANSI_COLOR="0;31"
  - CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
  - redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  - system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  - system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
- Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 9 10:11

SPEC is set to: /speccpu/spec16

Filesystem Type Size Used Avail Use% Mounted on

Continued on next page
SPEC CFP2006 Result

Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

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Test date: Mar-2016
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Platform Notes (Continued)
/dev/sda2      ext4  1.6T   41G  1.5T   3% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 1867 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 1867 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo ALWAYS > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
icpc  -m64

Continued on next page
Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

SPECfp2006 = NC
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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

continued on next page
SPEC CFP2006 Result

Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

Huawei

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
  433.milc: basepeak = yes
  70.lbm: basepeak = yes
  482.sphinx3: basepeak = yes

C++ benchmarks:
  444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
              -auto-ilp32

Continued on next page
Peak Optimization Flags (Continued)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Optimization Flags</th>
</tr>
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<tbody>
<tr>
<td>447.dealII</td>
<td>basepeak = yes</td>
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<tr>
<td>450.soplex</td>
<td>basepeak = yes</td>
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<tr>
<td>453.povray</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)</td>
</tr>
<tr>
<td></td>
<td>-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)</td>
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<tr>
<td></td>
<td>-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4</td>
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<td>-ansi-alias</td>
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Fortran benchmarks:

<table>
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<th>Optimization Flags</th>
</tr>
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<tbody>
<tr>
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<td>basepeak = yes</td>
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<tr>
<td>416.gamess</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)</td>
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<td>-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)</td>
</tr>
<tr>
<td></td>
<td>-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2</td>
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<tr>
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<td>-inline-level= -scalar-rep-</td>
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</table>

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Optimization Flags</th>
</tr>
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<tbody>
<tr>
<td>434.zeusmp</td>
<td>basepeak = yes</td>
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<td>437.leslie3d</td>
<td>basepeak = yes</td>
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<td>459.GemsFDTD</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)</td>
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<td>-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2</td>
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<td>-inline-level=0 -opt-prefetch -parallel</td>
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<th>Optimization Flags</th>
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<tr>
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<td>-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)</td>
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Benchmarks using both Fortran and C:

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Optimization Flags</th>
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<tbody>
<tr>
<td>435.gromacs</td>
<td>basepeak = yes</td>
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<tr>
<td>436.cactusADM</td>
<td>basepeak = yes</td>
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<tr>
<td>454.calculix</td>
<td>-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias</td>
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</tbody>
</table>

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Non-Compliant
Huawei

Huawei CH222 V3 (Intel Xeon E5-2609 v4)

| SPECfp2006 = | NC |
| SPECfp_base2006 = | NC |

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags source by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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