SPEC® CFP2006 Result

Huawei

Huawei RH2288H V3 (Intel Xeon E5-2643 v4)

<table>
<thead>
<tr>
<th>SPECfp®_rate2006 = NC</th>
<th>SPECfp_rate_base2006 = NC</th>
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<tbody>
<tr>
<td>CPU2006 license: 3175</td>
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<table>
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<tr>
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</tr>
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<td>453.povray</td>
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<td>454.calculix</td>
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<td>482.sphinx3</td>
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Non-Compliant
Huawei RH2288H V3 (Intel Xeon E5-2643 v4)

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### Hardware
- **CPU Name:** Intel Xeon E5-2643 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz
- **CPU MHz:** 3400
- **FPU:** Integrated
- **CPU(s) enabled:** 12 cores, 2 chips, 6 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1, 2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 20 MB I+D on chip per chip
- **Memory:** 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
- **Disk Subsystem:** 1 x 1 T SATA, 7200 RPM
- **Other Hardware:** None

### Software
- **Operating System:** Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64
- **Compiler:** 
  - C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
  - Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** No
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32/64-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** None
**SPEC CFP2006 Result**

**Huawei**

Huawei RH2288H V3 (Intel Xeon E5-2643 v4)

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### Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
SPEC CFP2006 Result

Huawei

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Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Sysinfo program /spec/cpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb866 d5a2859b2ceab81e28219e1

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
  2 "physical id"s (chips)
  24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 12
 physical 0: cores 1 2 3 6 7
 physical 1: cores 0 2 3 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal: 63570248 kB
 HugePages_Total: 0
 Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Non-compliant
# SPEC CFP2006 Result

## Huawei

### Huawei RH2288H V3 (Intel Xeon E5-2643 v4)

<table>
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<th>Specification</th>
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---

## Platform Notes (Continued)

```
uname -a:
    Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
    EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
unlevel 3 Mar 23 05:59

SPEC is set to: /speccpu/spec16
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda2      ext4  865G   32G  790G   4% /

Additional information from dmidecode:
    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Insyde Corp. 3.09 02/22/2016
    Memory:
    8x NO DIMM NO DIMM  3 rank
    8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
    8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)
```

## General Notes

- **Environment variables set by runspec before the start of the run:**
  - `LD_LIBRARY_PATH` = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

- **Binaries compiled on a system with:**
  - 1x Intel Core i5-4670K CPU + 32GB
  - RedHat EL 7.1

- **Transparent Huge Pages enabled with:**
  - `echo always > /sys/kernel/mm/transparent_hugepage/enabled`

- **Filesystem page cache cleared with:**
  - `echo 1 > /proc/sys/vm/drop_caches`

- **runspec command invoked through numactl i.e.:**
  - `numactl --interleave=all runspec <etc>`
## SPEC CFP2006 Result

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3175

### Test sponsor
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### Tested by
Huawei

### Test date
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### Software Availability
Mar-2016

### Base Compiler Invocation

- **C benchmarks**:
  - `icc -m64`

- **C++ benchmarks**:
  - `icpc -m64`

- **Fortran benchmarks**:
  - `ifort -m64`

- **Benchmarks using both Fortran and C**:
  - `icc -m64 ifort -m64`

### Base Portability Flags

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### Base Optimization Flags

- **C benchmarks**:
  - `-xCORE-AVX2`
  - `-ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
  - `-ansi-alias -opt-mem-layout-trans=3`

---

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both C and Fortran:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes

Continued on next page
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Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes
C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inlinellevel=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

Continued on next page

Non-Compliant
Huawei

Huawei RH2288H V3 (Intel Xeon E5-2643 v4)

<table>
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CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

**Peak Optimization Flags (Continued)**

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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