Huawei

Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

<table>
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<tr>
<th>SPECfp\textsuperscript{®} _rate\textsuperscript{2006}</th>
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<tbody>
<tr>
<td>SPECfp_rate_base\textsuperscript{2006}</td>
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CPU2006 license: 3175  
Test sponsor: Huawei  
Tested by: Huawei  
Test date: Mar-2016  
Hardware Availability: Mar-2016  
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

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<tr>
<td>410.bwaves</td>
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<tr>
<td>416.gamess</td>
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<td>444.namd</td>
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<td>447.dealII</td>
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Non-Compliant
SPEC CFP2006 Result

Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

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**Hardware**

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2637 v4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 3.70 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>3500</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>8 cores, 2 chips, 4 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1.2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
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<tr>
<td>L3 Cache:</td>
<td>15 MB I+D on chip per chip</td>
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<tr>
<td>Other Cache:</td>
<td>None</td>
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<tr>
<td>Memory:</td>
<td>256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)</td>
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<tr>
<td>Disk Subsystem:</td>
<td>1 x 2 T SATA, 7200 RPM</td>
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<td>Other Hardware:</td>
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</table>

**Software**

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</th>
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<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<td>File System:</td>
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<td>System State:</td>
<td>Run level 3 (multi-user)</td>
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<td>Base Pointers:</td>
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<td>Peak Pointers:</td>
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<td>Other Software:</td>
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Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

**SPEC CFP2006 Result**

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numaclt commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

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Huawei

Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

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Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to ES mode
Set Patrol Scrub to Disable
Sysinfo program /speccpu/spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb86b f6a285f22e3ab81e28219e1

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2637 v4 @ 3.50GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
physical 1: cores 0 1 2 3
cache size : 15360 KB

From /proc/meminfo
MemTotal: 63571176 kB
Bcache: Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
orangepen
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR=0;31
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
SPEC CFP2006 Result

Huawei

Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

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Platform Notes (Continued)

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Mar 16 06:24
SPEC is set to: /speccpu/spec16
Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Insyde Corp. 3.09 02/22/2016
Memory:
8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
File system page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

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Base Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gameess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -opt-prefetch  -auto-p32
-ansi-alias  -opt-mem-layout-trans=3

Continued on next page
Huawei RH2288H V3 (Intel Xeon E5-2637 v4)

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SPECfp_rate_base2006 = NC

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### Base Optimization Flags (Continued)

- C++ benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
  -ansi-alias -opt-mem-layout-trans=3

- Fortran benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

- Benchmarks using both Fortran and C:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
  -ansi-alias -opt-mem-layout-trans=3

### Peak Compiler Invocation

- C benchmarks: icc -m64
- C++ benchmarks: icpc -m64
- Fortran benchmarks: ifort -m64
- Benchmarks using both C and Fortran:
  -icc -m64 ifort -m64

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

- C benchmarks:
  - 433.milc: basepeak = yes
  - 470.lbm: basepeak = yes

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Peak Optimization Flags (Continued)

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -no-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
           -prof-use(pass 2) -fno-alias -auto-ilp32

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

438.gromacs: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
           -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -opt-prefetch -auto-ilp32
             -opt-prefetch -auto-ilp32
             -opt-prefetch -auto-ilp32
             -opt-prefetch -auto-ilp32
             -opt-prefetch -auto-ilp32
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Non-Compliant
SPEC CFP2006 Result

Huawei

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
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