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**SPEC® CFP2006 Result**

**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL80 Gen9
(2.00 GHz, Intel Xeon E5-2660 v4)

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**SPECfp®_rate2006 = Not Run**

**SPECfp_rate_base2006 = 846**

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<table>
<thead>
<tr>
<th>Test date:</th>
<th>Mar-2016</th>
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<td>Nov-2015</td>
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</table>

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**Hardware**

- **CPU Name:** Intel Xeon E5-2660 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz
- **CPU MHZ:** 2000
- **FPU:** Integrated
- **CPU(s) enabled:** 28 cores, 2 chips, 14 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1.2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core

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**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.2 (Maipo)
  Kernel 3.10.0-327.el7.x86_64
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** No
- **File System:** xfs

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CPU2006 license: 3
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System State:  Run level 3 (multi-user)
Base Pointers:  32/64-bit
Peak Pointers:  32/64-bit
Other Software:  None

L3 Cache:  35 MB I+D on chip per chip
Other Cache:  None
Memory:  256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem:  1 x 400 GB SAS SSD, RAID 0
Other Hardware:  None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>56</td>
<td>1135</td>
<td>671</td>
<td>1137</td>
<td>669</td>
<td>1138</td>
<td>669</td>
<td>411.gamess</td>
<td>56</td>
<td>1169</td>
<td>938</td>
<td>1173</td>
<td>935</td>
<td>1177</td>
</tr>
<tr>
<td>433.milc</td>
<td>56</td>
<td>804</td>
<td>639</td>
<td>805</td>
<td>639</td>
<td>804</td>
<td>639</td>
<td>436.cactusADM</td>
<td>56</td>
<td>601</td>
<td>1110</td>
<td>598</td>
<td>1120</td>
<td>601</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>56</td>
<td>509</td>
<td>1000</td>
<td>514</td>
<td>992</td>
<td>515</td>
<td>990</td>
<td>437.leslie3d</td>
<td>56</td>
<td>1117</td>
<td>471</td>
<td>1116</td>
<td>472</td>
<td>1117</td>
</tr>
<tr>
<td>444.namd</td>
<td>56</td>
<td>594</td>
<td>755</td>
<td>596</td>
<td>754</td>
<td>596</td>
<td>754</td>
<td>447.dealII</td>
<td>56</td>
<td>421</td>
<td>1520</td>
<td>427</td>
<td>1500</td>
<td>423</td>
</tr>
<tr>
<td>450.soplex</td>
<td>56</td>
<td>948</td>
<td>493</td>
<td>947</td>
<td>493</td>
<td>948</td>
<td>493</td>
<td>453.povray</td>
<td>56</td>
<td>241</td>
<td>1230</td>
<td>243</td>
<td>1230</td>
<td>243</td>
</tr>
<tr>
<td>454.calculix</td>
<td>56</td>
<td>321</td>
<td>1440</td>
<td>322</td>
<td>1440</td>
<td>322</td>
<td>1440</td>
<td>459.GemsFDTD</td>
<td>56</td>
<td>1323</td>
<td>449</td>
<td>1323</td>
<td>449</td>
<td>1323</td>
</tr>
<tr>
<td>465.tonto</td>
<td>56</td>
<td>588</td>
<td>937</td>
<td>590</td>
<td>935</td>
<td>593</td>
<td>929</td>
<td>470.lbm</td>
<td>56</td>
<td>862</td>
<td>892</td>
<td>862</td>
<td>893</td>
<td>862</td>
</tr>
<tr>
<td>481.wrf</td>
<td>56</td>
<td>797</td>
<td>785</td>
<td>798</td>
<td>784</td>
<td>796</td>
<td>786</td>
<td>482.sphinx3</td>
<td>56</td>
<td>1324</td>
<td>825</td>
<td>1323</td>
<td>825</td>
<td>1324</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesysten page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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SPEC CFP2006 Result
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SPECfp_rate2006 =  Not Run
SPECfp_rate_base2006 =  846

CPU2006 license: 3
Test sponsor:  HPE
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Platform Notes

BIOS Configuration:
HP Power Profile set to Custom
HP Power Regulator to HP Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
QPI Snoop Configuration set to Cluster on Die
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb866765a285932ceab81e82219e1

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

From /proc/meminfo
MemTotal:       263707436 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
Continued on next page
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### Platform Notes (Continued)

Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux  
run-level 3 Mar 2 12:15  
SPECC is set to: /home/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 411G 109G 303G 27% /home  

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U15 02/22/2016  
Memory:  
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

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### General Notes

Environment variables set by runspec before the start of the run:  
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"  

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

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### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

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### Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/Flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
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For other inquiries, please contact webmaster@spec.org.

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