SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

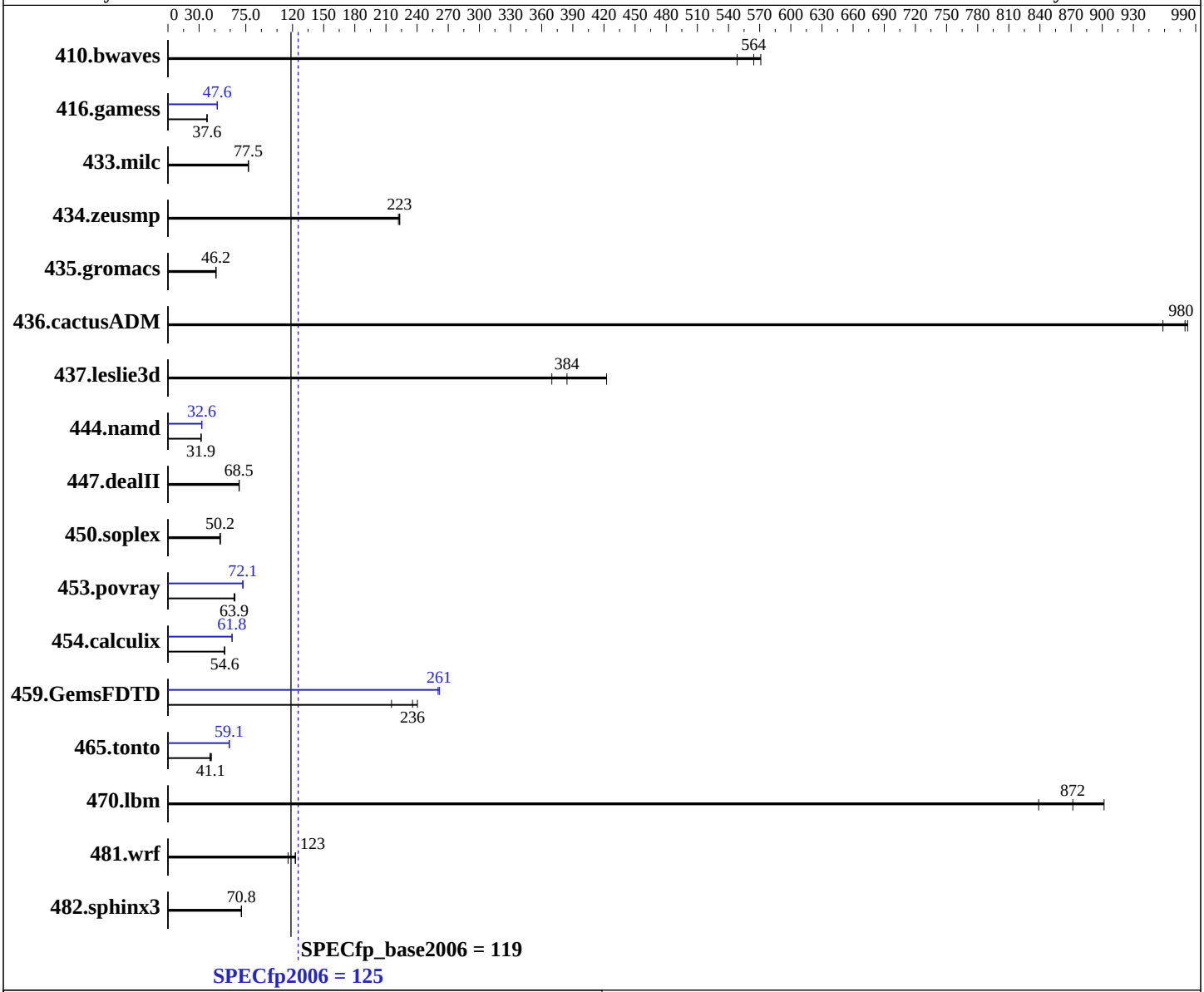
ProLiant XL420 Gen9
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECfp®2006 = 125

SPECfp_base2006 = 119

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Nov-2015



Hardware

CPU Name:	Intel Xeon E5-2699 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	44 cores, 2 chips, 22 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) Kernel 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant XL420 Gen9
(2.20 GHz, Intel Xeon E5-2699 v4)

SPECfp2006 = 125

SPECfp_base2006 = 119

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Nov-2015

L3 Cache:	55 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem:	1 x 400 GB SAS SSD, RAID 0
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24.8	548	23.8	571	**24.1**	**564**	24.8	548	23.8	571	**24.1**	**564**
416.gamess	519	37.7	521	37.6	**520**	**37.6**	410	47.7	414	47.3	**411**	**47.6**
433.milc	**118**	**77.5**	118	77.6	119	77.4	**118**	**77.5**	118	77.6	119	77.4
434.zeusmp	41.0	222	**40.8**	**223**	40.7	223	41.0	222	**40.8**	**223**	40.7	223
435.gromacs	**154**	**46.2**	155	46.2	154	46.3	**154**	**46.2**	155	46.2	154	46.3
436.cactusADM	12.2	982	**12.2**	**980**	12.5	958	12.2	982	**12.2**	**980**	12.5	958
437.leslie3d	25.4	370	**24.5**	**384**	22.2	423	25.4	370	**24.5**	**384**	22.2	423
444.namd	**251**	**31.9**	252	31.9	251	31.9	246	32.6	246	32.6	**246**	**32.6**
447.dealII	167	68.4	166	68.7	**167**	**68.5**	167	68.4	166	68.7	**167**	**68.5**
450.soplex	165	50.6	167	50.0	**166**	**50.2**	165	50.6	167	50.0	**166**	**50.2**
453.povray	82.4	64.6	**83.2**	**63.9**	83.5	63.7	73.9	72.0	73.5	72.4	**73.7**	**72.1**
454.calculix	**151**	**54.6**	151	54.8	152	54.4	134	61.7	**134**	**61.8**	133	61.8
459.GemsFDTD	49.3	215	44.2	240	**45.0**	**236**	40.5	262	**40.6**	**261**	40.8	260
465.tonto	243	40.4	235	41.8	**239**	**41.1**	166	59.2	**166**	**59.1**	167	59.0
470.lbm	**15.8**	**872**	16.4	839	15.2	902	**15.8**	**872**	16.4	839	15.2	902
481.wrf	91.0	123	96.5	116	**91.1**	**123**	91.0	123	96.5	116	**91.1**	**123**
482.sphinx3	**275**	**70.8**	276	70.7	275	70.8	**275**	**70.8**	276	70.7	275	70.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

```
BIOS Configuration:
  Intel Hyperthreading set to Disabled
  Power Profile set to Custom
  Power Regulator set to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C1E State
  Minimum Processor Idle Power Package C-State set to No Package State
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
```

Continued on next page

Platform Notes (Continued)

```
      Processor Power and Utilization Monitoring set to Disabled
      Memory Refresh Rate set to 1x Refresh

    Sysinfo program /cpu2006/config/sysinfo.rev6914
    $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
    running on xl420-g9-pilot Thu Mar  3 18:04:37 2016

    This section contains SUT (System Under Test) info as seen by
    some common utilities.  To remove or add to this section, see:
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
          2 "physical id"s (chips)
          44 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 22
          siblings  : 22
          physical 0: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
          physical 1: cores 0 2 3 4 8 10 11 12 16 17 18 19 20 21 24 25 26 27 28
       cache size : 56320 KB

    From /proc/meminfo
       MemTotal:       263709296 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    From /etc/*release* /etc/*version*
       os-release:
          NAME="Red Hat Enterprise Linux Server"
          VERSION="7.2 (Maipo)"
          ID="rhel"
          ID_LIKE="fedora"
          VERSION_ID="7.2"
          PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
          ANSI_COLOR="0;31"
          CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
       redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

    uname -a:
       Linux xl420-g9-pilot 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
       2015 x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Mar 3 18:01

    SPEC is set to: /cpu2006
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda4      xfs   364G   15G  350G   4% /
```

Continued on next page

Platform Notes (Continued)

```
 Additional information from dmidecode:

    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

   BIOS HP U19 02/22/2016
   Memory:
    8x UNKNOWN NOT AVAILABLE
    8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

 (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_NUM_THREADS = "44"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
 memory using RedHat EL 7.2
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

Continued on next page

Base Portability Flags (Continued)

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

459.GemsFDTD: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-unroll2 -inline-level=0 -opt-prefetch -parallel`

465.tonto: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml`
`http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jun 30 13:14:17 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 April 2016.