Hewlett Packard Enterprise  
ProLiant BL460c Gen9  
(2.20 GHz, Intel Xeon E5-2699 v4)

Test date: Mar-2016  
Hardware Availability: Mar-2016  
Software Availability: Nov-2015

**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE

**Hardware**

- **CPU Name:** Intel Xeon E5-2699 v4  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.60 GHz  
- **CPU MHz:** 2200  
- **FPU:** Integrated  
- **CPU(s) enabled:** 44 cores, 2 chips, 22 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1.2 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core

**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.2 (Maipo)  
  Kernel 3.10.0-327.el7.x86_64  
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
- **Auto Parallel:** Yes  
- **File System:** xfs

**SPECfp®2006 = 125**

**SPECfp_base2006 = 118**
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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
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<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
<th>Seconds Base</th>
<th>Ratio Base</th>
</tr>
</thead>
<tbody>
<tr>
<td>bwaves</td>
<td>24.3</td>
<td>24.6</td>
<td>23.8</td>
<td>23.8</td>
<td>571</td>
<td>571</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>gamess</td>
<td>519</td>
<td>519</td>
<td>519</td>
<td>519</td>
<td>37.7</td>
<td>37.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>mlibc</td>
<td>120</td>
<td>120</td>
<td>120</td>
<td>120</td>
<td>76.6</td>
<td>76.6</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>zeusmp</td>
<td>41.1</td>
<td>41.4</td>
<td>41.3</td>
<td>41.3</td>
<td>220</td>
<td>220</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>gromacs</td>
<td>157</td>
<td>155</td>
<td>154</td>
<td>154</td>
<td>46.3</td>
<td>46.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>cactusADM</td>
<td>12.5</td>
<td>12.5</td>
<td>12.4</td>
<td>12.4</td>
<td>965</td>
<td>965</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Leslie3d</td>
<td>24.9</td>
<td>23.6</td>
<td>25.4</td>
<td>25.4</td>
<td>370</td>
<td>370</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>namd</td>
<td>251</td>
<td>251</td>
<td>251</td>
<td>251</td>
<td>31.9</td>
<td>31.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>dealII</td>
<td>167</td>
<td>167</td>
<td>167</td>
<td>167</td>
<td>68.3</td>
<td>68.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>soplex</td>
<td>168</td>
<td>168</td>
<td>168</td>
<td>168</td>
<td>49.7</td>
<td>49.7</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>povray</td>
<td>82.6</td>
<td>82.6</td>
<td>83.1</td>
<td>83.1</td>
<td>64.0</td>
<td>64.0</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>calculix</td>
<td>154</td>
<td>154</td>
<td>154</td>
<td>154</td>
<td>53.4</td>
<td>53.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>GmFDTD</td>
<td>46.1</td>
<td>45.9</td>
<td>43.4</td>
<td>43.4</td>
<td>245</td>
<td>245</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>tonto</td>
<td>238</td>
<td>237</td>
<td>237</td>
<td>237</td>
<td>41.3</td>
<td>41.3</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>lbm</td>
<td>15.8</td>
<td>15.4</td>
<td>15.3</td>
<td>15.3</td>
<td>890</td>
<td>890</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>wrf</td>
<td>89.5</td>
<td>91.0</td>
<td>91.0</td>
<td>91.0</td>
<td>123</td>
<td>123</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>sphinx3</td>
<td>283</td>
<td>282</td>
<td>282</td>
<td>282</td>
<td>69.1</td>
<td>69.1</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:  
Intel Hyperthreading Option set to Enabled  
Power Profile set to Custom  
Power Regulator set to Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to CIE State  
Minimum Processor Idle Power Package C-State set to No Package State  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Home Snoop

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SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen9
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SPECfp2006 = 125
SPECfp_base2006 = 118

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpuv1.3/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on BL460c-Gen9-B Mon Feb 29 12:10:43 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
  2 "physical id"s (chips)
  88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 22
siblings : 44
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
28
cache size : 56320 KB

From /proc/meminfo
MemTotal:       263834576 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
uname -a:
Linux BL460c-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 29 12:05

SPEC is set to: /home/cpuv1.3/cpu2006

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**SPEC CFP2006 Result**

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**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda5</td>
<td>xfs</td>
<td>314G</td>
<td>96G</td>
<td>219G</td>
<td>31%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/22/2016  
Memory: 8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as: 8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

**General Notes**

Environment variables set by runspec before the start of the run:  
KMP_AFFINITY = "granularity=fine,compact,1,0"  
OMP_NUM_THREADS = "44"  
LD_LIBRARY_PATH = "/home/cpuv1.3/cpu2006/libs/32:/home/cpuv1.3/cpu2006/libs/64:/home/cpuv1.3/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

**Base Compiler Invocation**

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

**Base Portability Flags**

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

- 433.milc: -DSPEC_CPU_LP64
- 434.zesmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64 -nofor_main
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
- -ansi-alias -fp-model fast=2
- -qopt-prefetch-issue-excl-hint

C++ benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
- -fp-model fast=2
- -qopt-prefetch-issue-excl-hint

Fortran benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
- -fp-model fast=2
- -qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
- -ansi-alias -fp-model fast=2
- -qopt-prefetch-issue-excl-hint

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Continued on next page
Peak CompilerInvocation (Continued)

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
  433.milc: basepeak = yes
  470.lbm: basepeak = yes
  482.sphinx3: basepeak = yes

C++ benchmarks:
  444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
            -auto-ilp32
  447.dealII: basepeak = yes
  450.soplex: basepeak = yes
  453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias

Fortran benchmarks:
  410.bwaves: basepeak = yes
  416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

Continued on next page
Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
              -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
             -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-1lp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 19 April 2016.