



# SPEC® CINT2006 Result

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Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant ML110 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECint®2006 = 64.3**

**SPECint\_base2006 = 62.1**

**CPU2006 license:** 3

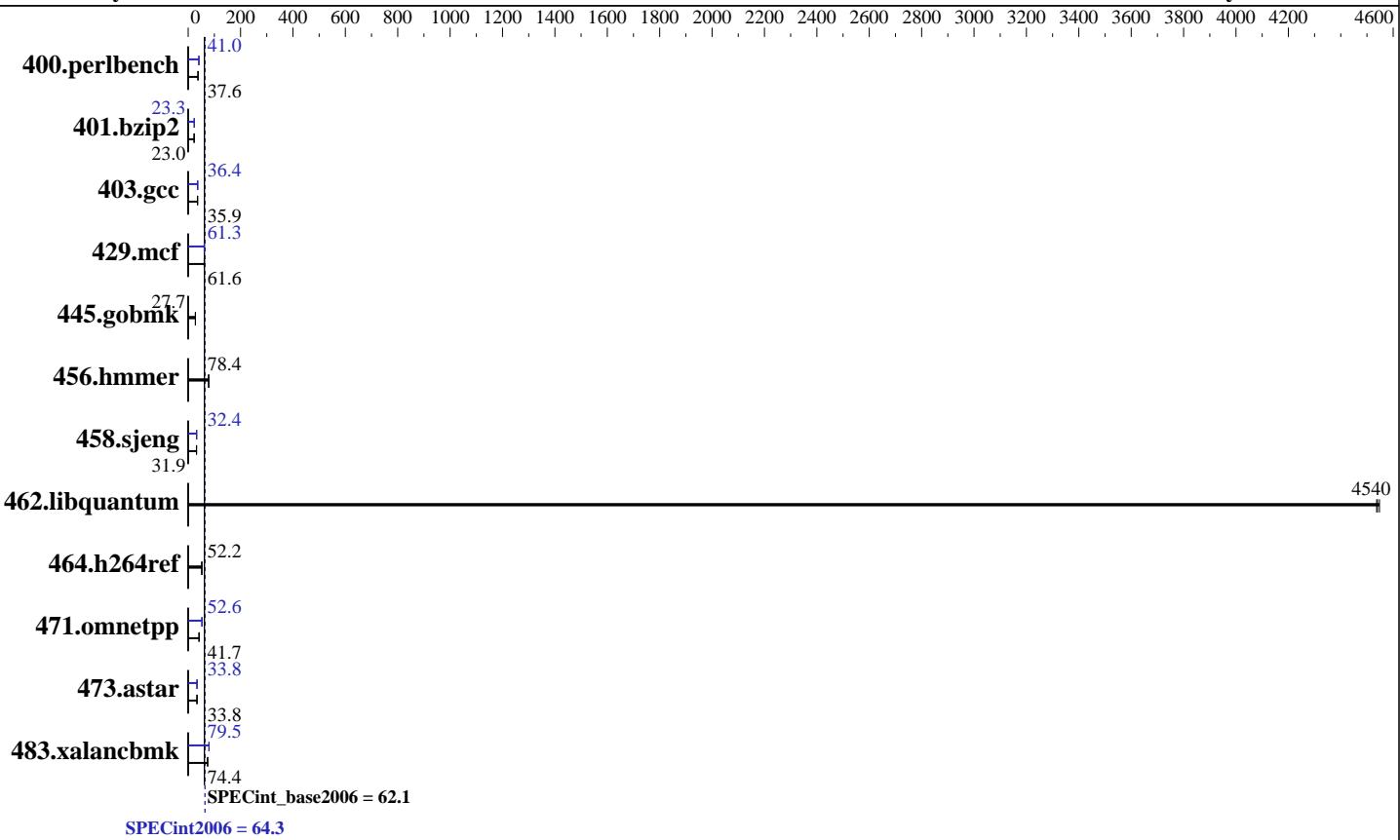
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2660 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 14 cores, 1 chip, 14 cores/chip  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 128 GB (4 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)  
Compiler: Kernel 3.10.0-327.el7.x86\_64  
C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
Auto Parallel: Yes  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	258	37.8	<b>260</b>	<b>37.6</b>	261	37.5	238	41.0	238	41.0	<b>238</b>	<b>41.0</b>
401.bzip2	418	23.1	423	22.8	<b>420</b>	<b>23.0</b>	<b>414</b>	<b>23.3</b>	414	23.3	414	23.3
403.gcc	<b>224</b>	<b>35.9</b>	224	35.9	223	36.0	223	36.2	<b>221</b>	<b>36.4</b>	221	36.4
429.mcf	148	61.8	149	61.0	<b>148</b>	<b>61.6</b>	148	61.6	<b>149</b>	<b>61.3</b>	149	61.1
445.gobmk	<b>379</b>	<b>27.7</b>	379	27.7	379	27.6	<b>379</b>	<b>27.7</b>	379	27.7	379	27.6
456.hmmer	119	78.4	119	78.3	<b>119</b>	<b>78.4</b>	119	78.4	119	78.3	<b>119</b>	<b>78.4</b>
458.sjeng	379	31.9	379	32.0	<b>379</b>	<b>31.9</b>	<b>374</b>	<b>32.4</b>	373	32.4	374	32.4
462.libquantum	4.57	4540	4.56	4550	<b>4.56</b>	<b>4540</b>	4.57	4540	4.56	4550	<b>4.56</b>	<b>4540</b>
464.h264ref	423	52.4	<b>424</b>	<b>52.2</b>	426	51.9	423	52.4	<b>424</b>	<b>52.2</b>	426	51.9
471.omnetpp	151	41.4	149	42.0	<b>150</b>	<b>41.7</b>	119	52.5	119	52.6	<b>119</b>	<b>52.6</b>
473.astar	209	33.7	<b>208</b>	<b>33.8</b>	208	33.8	208	33.8	208	33.8	<b>208</b>	<b>33.8</b>
483.xalancbmk	92.6	74.5	93.2	74.0	<b>92.8</b>	<b>74.4</b>	87.1	79.2	<b>86.8</b>	<b>79.5</b>	86.3	80.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Platform Notes

### BIOS Configuration:

HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C6 State  
Minimum Processor Idle Power Package C-State set to C6 (retention) State  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh  
Intel Hyperthreading set to Disabled

Sysinfo program /home/intel\_library/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date::: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Fri Mar 4 17:42:01 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
Continued on next page



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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
  1 "physical id"s (chips)
  14 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings : 14
  physical 0: cores 0 2 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      131601688 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Mar 4 17:37

```
SPEC is set to: /home/intel_library/cpu2006
Filesystem      Type  Size  Used  Avail Use% Mounted on
/dev/sda5        xfs   411G   68G  344G  17%  /home
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P99 02/22/2016

Memory:

4x UNKNOWN NOT AVAILABLE

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## Platform Notes (Continued)

4x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 128 GB and the dmidecode description should have one line reading as:  
4x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

OMP\_NUM\_THREADS = "14"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

C++ benchmarks (except as noted below):

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

```
473.astar: icpc -m64
```

## Peak Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
403.gcc: -DSPEC_CPU_LP64  
429.mcf: -DSPEC_CPU_LP64  
445.gobmk: -DSPEC_CPU_LP64  
456.hmmr: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX  
464.h264ref: -DSPEC_CPU_LP64  
471.omnetpp: -D_FILE_OFFSET_BITS=64  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

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## Peak Optimization Flags (Continued)

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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