Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen9
(3.20 GHz, Intel Xeon E5-2667 v4)

<table>
<thead>
<tr>
<th>Benchmark</th>
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</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>46.8</td>
</tr>
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</tr>
<tr>
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<td>77.6</td>
</tr>
<tr>
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<td>222</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>62.3</td>
</tr>
<tr>
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<td>364</td>
</tr>
<tr>
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<td>364</td>
</tr>
<tr>
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</tr>
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<td>50.8</td>
</tr>
<tr>
<td>453.povray</td>
<td>70.7</td>
</tr>
<tr>
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<td>62.9</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>59.8</td>
</tr>
<tr>
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<td>59.0</td>
</tr>
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<td>52.8</td>
</tr>
<tr>
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<td>128</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>91.7</td>
</tr>
</tbody>
</table>

**SPECfp®2006 = 127**

**SPECfp_base2006 = 124**

### Hardware
- **CPU Name:** Intel Xeon E5-2667 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.60 GHz
- **CPU MHz:** 3200
- **FPU:** Integrated
- **CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1,2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core

### Software
- **Operating System:** SUSE Linux Enterprise Server 12 (x86_64) SP1
  Kernel 3.12.49-11-default
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
Hewlett Packard Enterprise
ProLiant BL460c Gen9
(3.20 GHz, Intel Xeon E5-2667 v4)

SPEC CFP2006 Result
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SPECfp2006 = 127
SPECfp_base2006 = 124

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 2 x 500 GB SAS HDD 10 K, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>22.7</td>
<td>597</td>
<td>23.6</td>
<td>575</td>
<td>22.4</td>
<td>608</td>
<td>22.7</td>
<td>597</td>
<td>23.6</td>
<td>575</td>
</tr>
<tr>
<td>416.game3s</td>
<td>443</td>
<td>44.2</td>
<td>441</td>
<td>44.4</td>
<td>442</td>
<td>44.3</td>
<td>418</td>
<td>46.8</td>
<td>418</td>
<td>46.8</td>
</tr>
<tr>
<td>433.milc</td>
<td>119</td>
<td>77.4</td>
<td>118</td>
<td>77.8</td>
<td>118</td>
<td>77.6</td>
<td>119</td>
<td>77.4</td>
<td>118</td>
<td>77.4</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>41.5</td>
<td>219</td>
<td>41.0</td>
<td>222</td>
<td>40.9</td>
<td>222</td>
<td>41.5</td>
<td>219</td>
<td>41.0</td>
<td>222</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>115</td>
<td>62.1</td>
<td>115</td>
<td>62.3</td>
<td>115</td>
<td>62.4</td>
<td>115</td>
<td>62.1</td>
<td>115</td>
<td>62.1</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>14.3</td>
<td>835</td>
<td>14.2</td>
<td>841</td>
<td>14.8</td>
<td>808</td>
<td>14.3</td>
<td>835</td>
<td>14.2</td>
<td>841</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>27.1</td>
<td>346</td>
<td>25.8</td>
<td>364</td>
<td>25.3</td>
<td>371</td>
<td>27.1</td>
<td>346</td>
<td>25.8</td>
<td>364</td>
</tr>
<tr>
<td>444.namd</td>
<td>251</td>
<td>32.0</td>
<td>251</td>
<td>32.0</td>
<td>251</td>
<td>32.0</td>
<td>245</td>
<td>32.8</td>
<td>245</td>
<td>32.8</td>
</tr>
<tr>
<td>447.dealII</td>
<td>164</td>
<td>69.6</td>
<td>164</td>
<td>69.8</td>
<td>165</td>
<td>69.4</td>
<td>164</td>
<td>69.6</td>
<td>164</td>
<td>69.6</td>
</tr>
<tr>
<td>450.soplex</td>
<td>164</td>
<td>50.8</td>
<td>165</td>
<td>50.7</td>
<td>164</td>
<td>50.9</td>
<td>164</td>
<td>50.8</td>
<td>165</td>
<td>50.7</td>
</tr>
<tr>
<td>453.povray</td>
<td>84.3</td>
<td>63.1</td>
<td>84.5</td>
<td>62.9</td>
<td>84.7</td>
<td>62.8</td>
<td>75.2</td>
<td>70.8</td>
<td>75.2</td>
<td>70.8</td>
</tr>
<tr>
<td>454.calculix</td>
<td>138</td>
<td>59.8</td>
<td>138</td>
<td>59.7</td>
<td>138</td>
<td>59.9</td>
<td>132</td>
<td>62.7</td>
<td>132</td>
<td>62.7</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>48.6</td>
<td>218</td>
<td>44.6</td>
<td>238</td>
<td>44.6</td>
<td>238</td>
<td>40.5</td>
<td>262</td>
<td>40.7</td>
<td>261</td>
</tr>
<tr>
<td>465.tonto</td>
<td>186</td>
<td>52.9</td>
<td>187</td>
<td>52.8</td>
<td>187</td>
<td>52.6</td>
<td>167</td>
<td>59.0</td>
<td>167</td>
<td>59.0</td>
</tr>
<tr>
<td>470.lbm</td>
<td>19.4</td>
<td>708</td>
<td>20.0</td>
<td>685</td>
<td>19.8</td>
<td>693</td>
<td>19.4</td>
<td>708</td>
<td>20.0</td>
<td>685</td>
</tr>
<tr>
<td>481.wrf</td>
<td>87.3</td>
<td>128</td>
<td>85.7</td>
<td>130</td>
<td>90.2</td>
<td>124</td>
<td>87.3</td>
<td>128</td>
<td>85.7</td>
<td>130</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>213</td>
<td>91.7</td>
<td>212</td>
<td>91.8</td>
<td>213</td>
<td>91.6</td>
<td>213</td>
<td>91.7</td>
<td>212</td>
<td>91.8</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes
BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Home Snoop

Continued on next page
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen9
(3.20 GHz, Intel Xeon E5-2667 v4)

SPECfp2006 = 127
SPECfp_base2006 = 124

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpuv1.3/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on bl460c2-gen9-b Fri Mar  4 09:46:46 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name     : Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
  2 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores     : 8
  siblings      : 16
  physical 0: cores 0 2 3 4 8 10 11 12
  physical 1: cores 0 2 3 4 8 10 11 12
  cache size    : 25600 KB

From /proc/meminfo
  MemTotal:       264328212 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

Continued on next page
Spec CFP2006 Result

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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Platform Notes (Continued)

run-level 5 Mar 4 09:32
SPEC is set to: /home/cpuv1.3/cpu2006
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   424G  135G  290G  32% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 02/22/2016
Memory:
8x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 256 GB and the dmidecode description should have one line reading as:
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
OMP_NUM_THREADS = "16"
LD_LIBRARY_PATH = "/home/cpuv1.3/cpu2006/libs/32:/home/cpuv1.3/cpu2006/libs/64:/home/cpuv1.3/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64
## SPEC CFP2006 Result

### Hewlett Packard Enterprise
  (Test Sponsor: HPE)
  ProLiant BL460c Gen9
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<td>124</td>
</tr>
</tbody>
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- **CPU2006 license:** 3
- **Test sponsor:** HPE
- **Tested by:** HPE
- **Test date:** Mar-2016
- **Hardware Availability:** Mar-2016
- **Software Availability:** Dec-2015

### Base Portability Flags

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`
- 435.gromacs: `-DSPEC_CPU_LP64` `-nofor_main`
- 436.cactusADM: `-DSPEC_CPU_LP64` `-nofor_main`
- 437.leslie3d: `-DSPEC_CPU_LP64`
- 444.namd: `-DSPEC_CPU_LP64`
- 447.dealII: `-DSPEC_CPU_LP64`
- 450.soplex: `-DSPEC_CPU_LP64`
- 453.povray: `-DSPEC_CPU_LP64`
- 454.calculix: `-DSPEC_CPU_LP64` `-nofor_main`
- 459.GemsFDTD: `-DSPEC_CPU_LP64`
- 465.tonto: `-DSPEC_CPU_LP64`
- 470.lbm: `-DSPEC_CPU_LP64`
- 481.wrf: `-DSPEC_CPU_LP64` `-DSPEC_CPU_CASE_FLAG` `-DSPEC_CPU_LINUX`
- 482.sphinx3: `-DSPEC_CPU_LP64`

### Base Optimization Flags

**C benchmarks:**
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-static` `-parallel` `-opt-prefetch`
- `-ansi-alias` `-fp-model fast=2`
- `-qopt-prefetch-issue-excl-hint`

**C++ benchmarks:**
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-static` `-opt-prefetch` `-ansi-alias`
- `-fp-model fast=2`
- `-qopt-prefetch-issue-excl-hint`

**Fortran benchmarks:**
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-static` `-parallel` `-opt-prefetch`
- `-fp-model fast=2`
- `-qopt-prefetch-issue-excl-hint`

**Benchmarks using both Fortran and C:**
- `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-static` `-parallel` `-opt-prefetch`
- `-ansi-alias` `-fp-model fast=2`
- `-qopt-prefetch-issue-excl-hint`

### Peak Compiler Invocation

**C benchmarks:**
- `icc` `-m64`

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Continued on next page
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Tested by: HPE

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ic -m64 ifort -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

Continued on next page
Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
    -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
    -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -03 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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