## Hewlett Packard Enterprise

### ProLiant DL80 Gen9

**(2.00 GHz, Intel Xeon E5-2660 v4)**

**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE  

**SPECint_rate2006 = Not Run**  
**SPECint_rate_base2006 = 1100**  

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Result</th>
</tr>
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<tbody>
<tr>
<td>400.perlbench</td>
<td>56</td>
<td>793</td>
</tr>
<tr>
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<td>56</td>
<td>535</td>
</tr>
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<td>56</td>
<td>822</td>
</tr>
<tr>
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<td>56</td>
<td>1490</td>
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<tr>
<td>445.gobmk</td>
<td>56</td>
<td>719</td>
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<tr>
<td>456.hmmer</td>
<td>56</td>
<td>1550</td>
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<td>458.sjeng</td>
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<td>743</td>
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<td>56</td>
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<td>464.h264ref</td>
<td>56</td>
<td>1340</td>
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<td>56</td>
<td>586</td>
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<td>56</td>
<td>630</td>
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</tbody>
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### Hardware

- **CPU Name:** Intel Xeon E5-2660 v4  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.20 GHz  
- **CPU MHz:** 2000  
- **FPU:** Integrated  
- **CPU(s) enabled:** 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1.2 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core  
- **L3 Cache:** 35 MB I+D on chip per chip  
- **Other Cache:** None  
- **Memory:** 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
- **Disk Subsystem:** 1 x 400 GB SAS SSD, RAID 0  

### Software

- **Operating System:** Red Hat Enterprise Linux Server release 7.2 (Maipo)  
  Kernel 3.10.0-327.el7.x86_64  
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
- **Auto Parallel:** No  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32/64-bit  
- **Other Software:** Microquill SmartHeap V10.2
Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL80 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)  

SPEC CINT2006 Result  

SPECint_rate2006 = Not Run  
SPECint_rate_base2006 = 1100

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Base</td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td></td>
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<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>56</td>
<td>690</td>
<td>793</td>
<td>689</td>
<td>794</td>
<td>690</td>
<td>793</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>401.bzip2</td>
<td>56</td>
<td>1010</td>
<td>535</td>
<td>1011</td>
<td>535</td>
<td>1013</td>
<td>533</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>56</td>
<td>548</td>
<td>822</td>
<td>548</td>
<td>823</td>
<td>549</td>
<td>821</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>429.mcf</td>
<td>56</td>
<td>342</td>
<td>1490</td>
<td>343</td>
<td>1490</td>
<td>342</td>
<td>1490</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>445.gobmk</td>
<td>56</td>
<td>816</td>
<td>720</td>
<td>817</td>
<td>719</td>
<td>817</td>
<td>719</td>
<td></td>
<td></td>
<td></td>
<td></td>
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<td>56</td>
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<td>1550</td>
<td>336</td>
<td>1550</td>
<td>337</td>
<td>1550</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
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<td>56</td>
<td>912</td>
<td>743</td>
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<td>743</td>
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<td>743</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>56</td>
<td>106</td>
<td>10900</td>
<td>106</td>
<td>10900</td>
<td>106</td>
<td>10900</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>464.h264ref</td>
<td>56</td>
<td>925</td>
<td>1340</td>
<td>947</td>
<td>1310</td>
<td>925</td>
<td>1340</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>56</td>
<td>597</td>
<td>586</td>
<td>598</td>
<td>585</td>
<td>598</td>
<td>586</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>473.astar</td>
<td>56</td>
<td>624</td>
<td>630</td>
<td>628</td>
<td>626</td>
<td>623</td>
<td>631</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>56</td>
<td>301</td>
<td>1290</td>
<td>299</td>
<td>1290</td>
<td>300</td>
<td>1290</td>
<td></td>
<td></td>
<td></td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop_caches  
runcspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

Platform Notes

BIOS Configuration:
- HP Power Profile set to Custom  
- HP Power Regulator to HP Static High Performance Mode  
- Minimum Processor Idle Power Core C-State set to C6 State  
- Minimum Processor Idle Power Package C-State set to No Package State  
- QPI Snoop Configuration set to Cluster on Die  
- Collaborative Power Control set to Disabled  
- Thermal Configuration set to Maximum Cooling  
- Processor Power and Utilization Monitoring set to Disabled  
- Memory Refresh Rate set to 1x Refresh

Continued on next page
Platform Notes (Continued)

Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ece88e28219e1

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  cache size : 17920 KB

From /proc/meminfo
MemTotal:       263707436 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.2 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.2"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 2 12:15

SPEC is set to: /home/cpu2006
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/mapper/rhel-home xfs 411G 109G 303G 27% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
Continued on next page
Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U15 02/22/2016
Memory:
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Base Portability Flags

400.perlb benchmark: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

Continued on next page
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL80 Gen9
(2.00 GHz, Intel Xeon E5-2660 v4)

SPECint_rate2006 = Not Run
SPECint_rate_base2006 = 1100

CPU2006 license: 3
Test date: Mar-2016
Test sponsor: HPE
Hardware Availability: Mar-2016
Tested by: HPE
Software Availability: Nov-2015

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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