# SPEC® CINT2006 Result



## Hewlett Packard Enterprise

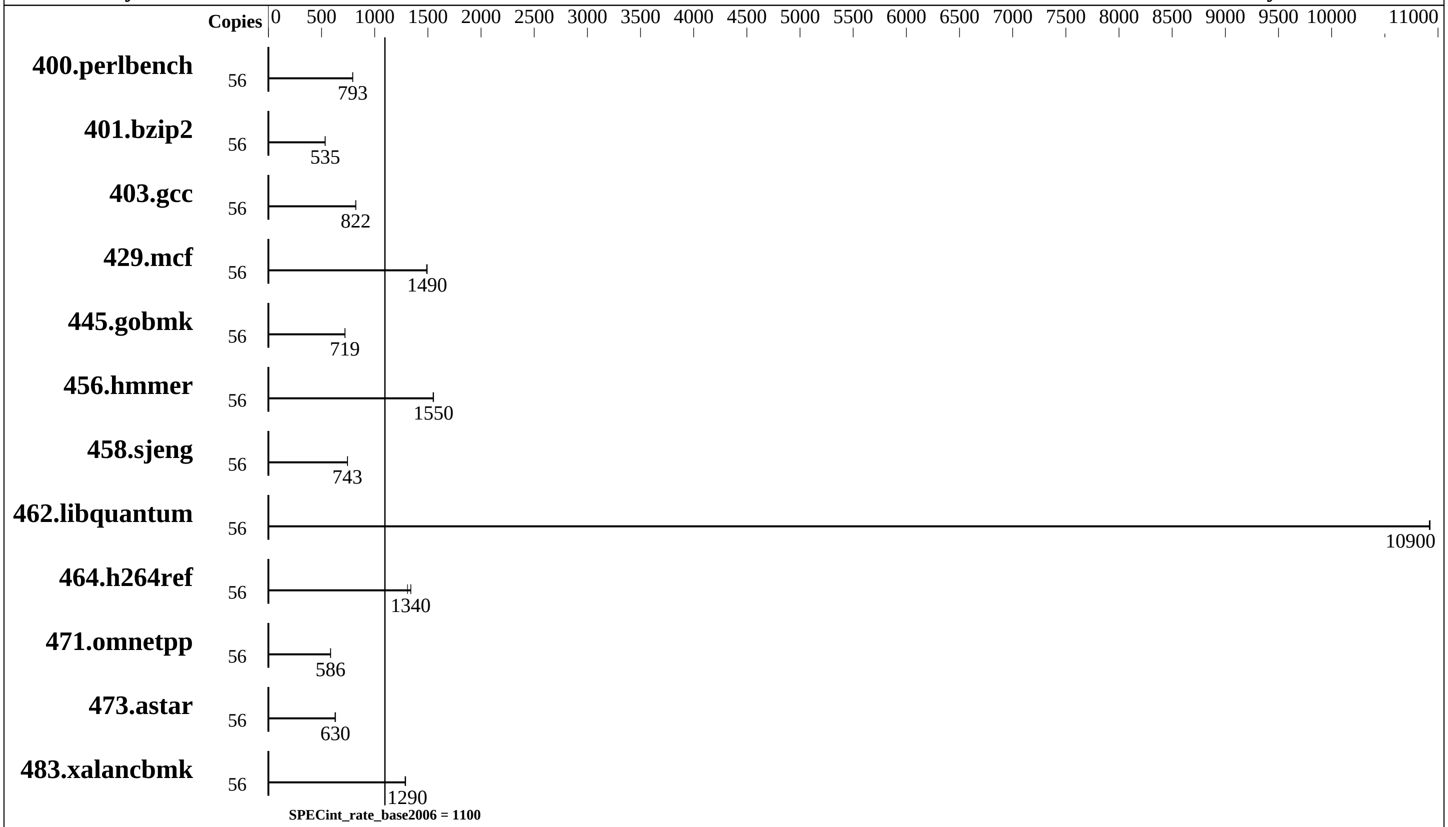
(Test Sponsor: HPE)

ProLiant DL80 Gen9
(2.00 GHz, Intel Xeon E5-2660 v4)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 1100

| | | | |
|---|---|---|---|
| **CPU2006 license:** | 3 | **Test date:** | Mar-2016 |
| **Test sponsor:** | HPE | **Hardware Availability:** | Mar-2016 |
| **Tested by:** | HPE | **Software Availability:** | Nov-2015 |




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2660 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 28 cores, 2 chips, 14 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 35 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R) |
| Disk Subsystem: | 1 x 400 GB SAS SSD, RAID 0 |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 7.2 (Maipo)<br>Kernel 3.10.0-327.el7.x86_64 |
| Compiler: | C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.2 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 56 | **690** | **793** | 689 | 794 | 690 | 793 | | | | | | | |
| 401.bzip2 | 56 | 1010 | 535 | **1011** | **535** | 1013 | 533 | | | | | | | |
| 403.gcc | 56 | **548** | **822** | 548 | 823 | 549 | 821 | | | | | | | |
| 429.mcf | 56 | **342** | **1490** | 343 | 1490 | 342 | 1490 | | | | | | | |
| 445.gobmk | 56 | 816 | 720 | **817** | **719** | 817 | 719 | | | | | | | |
| 456.hmmer | 56 | **336** | **1550** | 336 | 1550 | 337 | 1550 | | | | | | | |
| 458.sjeng | 56 | 912 | 743 | **912** | **743** | 912 | 743 | | | | | | | |
| 462.libquantum | 56 | **106** | **10900** | 106 | 10900 | 106 | 10900 | | | | | | | |
| 464.h264ref | 56 | **925** | **1340** | 947 | 1310 | 925 | 1340 | | | | | | | |
| 471.omnetpp | 56 | 597 | 586 | 598 | 585 | **598** | **586** | | | | | | | |
| 473.astar | 56 | **624** | **630** | 628 | 626 | 623 | 631 | | | | | | | |
| 483.xalancbmk | 56 | 301 | 1290 | 299 | 1290 | **300** | **1290** | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Platform Notes

```
BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C6 State
  Minimum Processor Idle Power Package C-State set to No Package State
  QPI Snoop Configuration set to Cluster on Die
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
  Processor Power and Utilization Monitoring set to Disabled
  Memory Refresh Rate set to 1x Refresh
```

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## Platform Notes (Continued)

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Wed Mar  2 12:18:23 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
      2 "physical id"s (chips)
      56 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 14
      siblings  : 28
      physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
      physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
   cache size : 17920 KB

From /proc/meminfo
   MemTotal:       263707436 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
   EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Mar 2 12:15

SPEC is set to: /home/cpu2006
   Filesystem            Type  Size  Used Avail Use% Mounted on
   /dev/mapper/rhel-home xfs   411G  109G  303G  27% /home
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
```

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## Platform Notes (Continued)

```
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HP U15 02/22/2016
    Memory:
     8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

 (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
```

## Base Compiler Invocation

C benchmarks:
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

## Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

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## Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml



For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.


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