### SPECint® CINT2006 Result

**EXO S.A.**

**SERVER EXO SPRINT X385**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Rate</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>143</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>81.2</td>
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<tr>
<td>403.gcc</td>
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</tr>
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<td>429.mcf</td>
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</tr>
<tr>
<td>445.gobmk</td>
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<tr>
<td>456.hmmer</td>
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<tr>
<td>458.sjeng</td>
<td>127</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>1520</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>233</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>66.7</td>
</tr>
<tr>
<td>473.astar</td>
<td>76.2</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>143</td>
</tr>
</tbody>
</table>

**Copies:**

- SPECint_rate_base2006 = 160

**CPU2006 license:** 4691

**Test sponsor:** EXO S.A.

**Tested by:** EXO S.A.

**Test date:** Feb-2016

**Hardware Availability:** Nov-2013

**Software Availability:** Jun-2015

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### Hardware

- **CPU Name:** Intel Xeon E3-1231 v3
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.80 GHz
- **CPU MHz:** 3400
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 1 chip, 4 cores/chip
- **CPU(s) orderable:** 1 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core
- **L3 Cache:** 8 MB I+D on chip per chip
- **Other Cache:** None
- **Memory:** 8 GB (2 x 4 GB 1Rx8 PC3-12800E-11, ECC)
- **Disk Subsystem:** 2x1TB SATA 7200 RPM Raid 1
- **Other Hardware:** None

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### Software

- **Operating System:** Ubuntu 12.04 LTS 3.2.0-23-generic
- **Compiler:** C/C++: Version 14.0.2.144 of Intel C++ Studio XE for Linux
- **Auto Parallel:** No
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** Not Applicable
- **Other Software:** None
## SPEC CINT2006 Result

### EXO S.A.  
**SERVER EXO SPRINT X385**

<table>
<thead>
<tr>
<th>SPECint_rate2006 =</th>
<th>Not Run</th>
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<tbody>
<tr>
<td>SPECint_rate_base2006 =</td>
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**CPU2006 license:** 4691  
**Test date:** Feb-2016  
**Test sponsor:** EXO S.A.  
**Tested by:** EXO S.A.  
**Hardware Availability:** Jun-2015  
**Software Availability:** Nov-2013

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### Results Table

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<th>Benchmark</th>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Platform Notes

**BIOS Settings:**  
Hyper-Threading Disabled.

Sysinfo program /usr/cpu2006/Docs/sysinfo  
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on ubuntu Tue Feb 23 09:02:07 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) CPU E3-1231 v3 @ 3.40GHz
- 1 "physical id"s (chips)
- 4 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 4
  - siblings : 4
  - physical 0: cores 0 1 2 3
  - cache size : 8192 KB

From /proc/meminfo

MemTotal: 8113744 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
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Platform Notes (Continued)

```
/usr/bin/lsb_release -d
    Ubuntu 12.04 LTS

From /etc/*release* /etc/*version*
    debian_version: wheezy/sid

uname -a:
    Linux ubuntu 3.2.0-23-generic #36-Ubuntu SMP Tue Apr 10 20:39:51 UTC 2012
    x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Feb 23 00:13 last=2
SPEC is set to: /usr/cpu2006

Files
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Base Compiler Invocation

C benchmarks:
    icc  -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -o3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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