Huawei 5288 V3 (Intel Xeon E5-2690 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

**Non-Compliant**

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

CPU2006 license: 3175
Test sponsor: Huawei
Test date: Feb-2016
Hardware Availability: Mar-2016
Software Availability: Aug-2015

<table>
<thead>
<tr>
<th>Test</th>
<th>Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>NC</td>
</tr>
<tr>
<td>416.gamess</td>
<td>NC</td>
</tr>
<tr>
<td>433.milc</td>
<td>NC</td>
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<tr>
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<td>NC</td>
</tr>
<tr>
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<td>NC</td>
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<tr>
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<td>NC</td>
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<td>NC</td>
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<tr>
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<td>NC</td>
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<tr>
<td>447.dealII</td>
<td>NC</td>
</tr>
<tr>
<td>450.soplex</td>
<td>NC</td>
</tr>
<tr>
<td>451.gacad</td>
<td>NC</td>
</tr>
<tr>
<td>452.escicms</td>
<td>NC</td>
</tr>
<tr>
<td>453.specfp_base2006</td>
<td>NC</td>
</tr>
<tr>
<td>454.calculix</td>
<td>NC</td>
</tr>
<tr>
<td>455.ame</td>
<td>NC</td>
</tr>
<tr>
<td>456.ate</td>
<td>NC</td>
</tr>
<tr>
<td>457.asearch</td>
<td>NC</td>
</tr>
<tr>
<td>458.spack</td>
<td>NC</td>
</tr>
<tr>
<td>459.gemsfDTD</td>
<td>NC</td>
</tr>
<tr>
<td>460.ashira</td>
<td>NC</td>
</tr>
<tr>
<td>461.athena</td>
<td>NC</td>
</tr>
<tr>
<td>462.astar</td>
<td>NC</td>
</tr>
<tr>
<td>463.bzip2</td>
<td>NC</td>
</tr>
<tr>
<td>464.batran</td>
<td>NC</td>
</tr>
<tr>
<td>465.tonto</td>
<td>NC</td>
</tr>
<tr>
<td>466.lbm</td>
<td>NC</td>
</tr>
<tr>
<td>470.lbm</td>
<td>NC</td>
</tr>
<tr>
<td>471.leslie3d</td>
<td>NC</td>
</tr>
<tr>
<td>472.leslie3d</td>
<td>NC</td>
</tr>
<tr>
<td>473.milc</td>
<td>NC</td>
</tr>
<tr>
<td>474.namd</td>
<td>NC</td>
</tr>
<tr>
<td>475.dealII</td>
<td>NC</td>
</tr>
<tr>
<td>476.ise</td>
<td>NC</td>
</tr>
<tr>
<td>477.ise</td>
<td>NC</td>
</tr>
<tr>
<td>478.spectre</td>
<td>NC</td>
</tr>
<tr>
<td>479.spectre</td>
<td>NC</td>
</tr>
<tr>
<td>480.specfp</td>
<td>NC</td>
</tr>
<tr>
<td>481.sphinx3</td>
<td>NC</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>NC</td>
</tr>
<tr>
<td>483.sphinx3</td>
<td>NC</td>
</tr>
<tr>
<td>484.zeusmp</td>
<td>NC</td>
</tr>
<tr>
<td>485.gromacs</td>
<td>NC</td>
</tr>
<tr>
<td>486.cactusADM</td>
<td>NC</td>
</tr>
<tr>
<td>487.leslie3d</td>
<td>NC</td>
</tr>
<tr>
<td>488.namd</td>
<td>NC</td>
</tr>
<tr>
<td>489.dealII</td>
<td>NC</td>
</tr>
</tbody>
</table>

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name: Intel Xeon E5-2690 v4</td>
<td>Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo) 3.10.0-123.el7.x86_64</td>
</tr>
<tr>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz</td>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
</tr>
<tr>
<td>CPU MHz: 2600</td>
<td>Auto Parallel: Yes</td>
</tr>
<tr>
<td>FPU: Integrated</td>
<td>File System: xfs</td>
</tr>
<tr>
<td>CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip</td>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>CPU(s) orderable: 1,2 chip</td>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
<td>Peak Pointers: 32/64-bit</td>
</tr>
<tr>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
<td>Other Software: None</td>
</tr>
<tr>
<td>L3 Cache: 35 MB I+D on chip per chip</td>
<td></td>
</tr>
</tbody>
</table>
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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</tr>
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<td>NC</td>
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<td>NC</td>
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<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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<tr>
<td>433.milc</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>444.namd</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>447.dealII</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>450.soplex</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>453.povray</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>454.calculix</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>465.tonto</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>470.lbm</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>481.wrf</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
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<td>NC</td>
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</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue Feb 23 12:49:31 2016

Continued on next page

Non-Compliant
Huawei
Huawei 5288 V3 (Intel Xeon E5-2690 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test date: Feb-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v4 @ 2.60GHz
2 "physical id"s (chips)
28 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 3 4 5 7 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 263569784 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Feb 23 03:55
SPEC is set to: /spec16

Non-Compliant
SPEC CFP2006 Result

Huawei

Huawei 5288 V3 (Intel Xeon E5-2690 v4)

SPECfp2006 = NC
SPECfp_base2006 = NC

CPU2006 license: 3175
Test date: Feb-2016
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Platform Notes (Continued)

Filesystem     Type  Size  Used  Avail Use% Mounted on
/dev/sda2      xfs   449G  111G  339G  25% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met due to there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 7.11 02/14/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = ":/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Continued on next page
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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

- icc -m64 ifort -m64

Base Portability Flags

- 410.bwaves: -DSPEC_CPU_LP64
- 416.gamess: -DSPEC_CPU_LP64
- 433.milc: -DSPEC_CPU_LP64
- 434.zeusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG
- 447.dealII: -DSPEC_CPU_LP64 -nofor_main
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
- ansi-alias

C++ benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:

- xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
- ansi-alias
SPEC CFP2006 Result

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Peak Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
  433.milc: basepeak = yes
  470.lbm: basepeak = yes
  482.sphinx3: basepeak = yes

C benchmarks:
  444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
  -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
  -auto-ilp32
  447.dealII: basepeak = yes
  450.soplex: basepeak = yes
  453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
  -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Peak Optimization Flags (Continued)

453.povray (continued):
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.games: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-sort-all-options=3 -auto-unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

467.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml