Huawei

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

| SPECfp\textsuperscript{®}_rate\textsubscript{2006} = NC | SPECfp_rate_base\textsubscript{2006} = NC |

<table>
<thead>
<tr>
<th>CPU2006 license: 3175</th>
<th>Test date: Apr-2016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor: Huawei</td>
<td>Hardware Availability: Mar-2016</td>
</tr>
<tr>
<td>Tested by: Huawei</td>
<td>Software Availability: Aug-2015</td>
</tr>
</tbody>
</table>

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

<table>
<thead>
<tr>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
</tr>
<tr>
<td>416.gamess</td>
</tr>
<tr>
<td>433.milc</td>
</tr>
<tr>
<td>434.zeusmp</td>
</tr>
<tr>
<td>435.gromacs</td>
</tr>
<tr>
<td>436.cactusADM</td>
</tr>
<tr>
<td>437.leslie3d</td>
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<tr>
<td>444.namd</td>
</tr>
<tr>
<td>447.dealII</td>
</tr>
<tr>
<td>450.soplex</td>
</tr>
<tr>
<td>453.povray</td>
</tr>
<tr>
<td>454.calculix</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
</tr>
<tr>
<td>465.tonto</td>
</tr>
<tr>
<td>47.1bm</td>
</tr>
<tr>
<td>481.wrf</td>
</tr>
<tr>
<td>482.sphinx3</td>
</tr>
</tbody>
</table>
Huawei XH622 V3 (Intel Xeon E5-2690 v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Huawei

CPU Name: Intel Xeon E5-2690 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 500GB SATA, 10000 RPM
Other Hardware: None

Software
Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.
## Huawei XH622 V3 (Intel Xeon E5-2690 v4)

**SPEC CFP2006 Result**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>416.games</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>433.milc</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>444.namd</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>447.dealII</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>450.soplex</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>28</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>453.povray</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>454.calculix</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>465.tonto</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>470.lbm</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>481.wrf</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td></td>
<td>56</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
<td>NC</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).
Huawei

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei
Test date: Apr-2016
Hardware Availability: Mar-2016
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb866 dea2b59b2ceab81e28219e1
running on localhost.localdomain Sun Apr 10 04:29:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v4® 2.60GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 7
siblings : 14
physical 0: cores : 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores : 0 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

From /proc/meminfo
MemTotal: 63566032 kB
Bsize/Pages: Total: 0
HugePages_Total: 0
HugePages_Free: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID=rhel
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME=cpe:/o:redhat:enterprise_linux:7.0:GA:server
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

Continued on next page
SPEC CFP2006 Result

Huawei

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

SPECfp_rate2006 = NC
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CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

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Platform Notes (Continued)

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Wed May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 7 05:14

SPEC is set to: /spec16
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 449G 352G 98G 79% /

Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.12 03/03/2016
Memory:
8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "'/spec16/libs/32:/spec16/libs/64:/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
File system page cache cleared with:
echo 3 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>
The Huawei XH622 V3 and Huawei XH628 V3 and Huawei XH620 V3 are electronically equivalent.
The results have been measured on a Huawei XH620 V3 model.
**SPEC CFP2006 Result**

**Huawei**

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
<td>NC</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Apr-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Aug-2015

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---

**Base Compiler Invocation**

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

---

**Base Portability Flags**

- `410.bwaves: -DSPEC_CPU_LP64`
- `416.gamess: -DSPEC_CPU_LP64`
- `433.milc: -DSPEC_CPU_LP64`
- `434.zeusmp: -DSPEC_CPU_LP64`
- `435.gromacs: -DSPEC_CPU_LP64 -nofor_main`
- `436.cactusADM: -DSPEC_CPU_LP64 -nofor_main`
- `437.leslie3d: -DSPEC_CPU_LP64`
- `444.namd: -DSPEC_CPU_LP64 -nofor_main`
- `447.dealII: -DSPEC_CPU_LP64`
- `450.soplex: -DSPEC_CPU_LP64`
- `453.povray: -DSPEC_CPU_LP64 -nofor_main`
- `454.calculix: -DSPEC_CPU_LP64 -nofor_main`
- `459.GemsFDTD: -DSPEC_CPU_LP64 -nofor_main`
- `465.tonto: -DSPEC_CPU_LP64`
- `481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
- `482.sphinx3: -DSPEC_CPU_LP64`

---

**Base Optimization Flags**

- **C benchmarks:**
  - `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32`
  - `-ansi-alias -opt-mem-layout-trans=3`

---

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -xco-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc  -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.tanHC: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
Huawei

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

<table>
<thead>
<tr>
<th>CPU2006 license</th>
<th>3175</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor</td>
<td>Huawei</td>
</tr>
<tr>
<td>Tested by</td>
<td>Huawei</td>
</tr>
<tr>
<td>Test date</td>
<td>Apr-2016</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Aug-2015</td>
</tr>
</tbody>
</table>

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Peak Portability Flags (Continued)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>450.soplex</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>453.povray</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>454.calculix</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
</tbody>
</table>

Peak Optimization Flags

**C benchmarks:**

- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

**C++ benchmarks:**

- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32
- 447.dealII: basepeak = yes
- 450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -03(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias

Fortran benchmarks:

Continued on next page
SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

**Peak Optimization Flags (Continued)**

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12 -inline-level=0 -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32
436.cactusADM: basepeak = yes
457.calculix: basepeak = yes
481.npt: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml
Huawei

Huawei XH622 V3 (Intel Xeon E5-2690 v4)

SPECfp_rate2006 = NC
SPECfp_rate_base2006 = NC

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Apr-2016
Hardware Availability: Mar-2016
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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