**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant ML350 Gen9  
(2.60 GHz, Intel Xeon E5-2690 v4)

### SPECint Rate 2006 Result

**Copy of SPECint Rate 2006 Result**

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1 Kernel 3.12.49-11-default</td>
<td>CPU Name: Intel Xeon E5-2690 v4</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux</td>
<td>CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz</td>
</tr>
<tr>
<td>Auto Parallel: No</td>
<td>CPU MHz: 2600</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>FPU: Integrated</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>Base Pointers: 32-bit</td>
<td>CPU(s) orderable: 1.2 chips</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>Primary Cache: 32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Other Software: Microquill SmartHeap V10.2</td>
<td>Secondary Cache: 256 KB I+D on chip per core</td>
</tr>
<tr>
<td></td>
<td>L3 Cache: 35 MB I+D on chip per chip</td>
</tr>
<tr>
<td></td>
<td>Other Cache: None</td>
</tr>
<tr>
<td></td>
<td>Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)</td>
</tr>
<tr>
<td></td>
<td>Disk Subsystem: 1 x 800 GB SAS SSD, RAID 0</td>
</tr>
<tr>
<td></td>
<td>Other Hardware: None</td>
</tr>
</tbody>
</table>

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info@spec.org  
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SPEC CINT2006 Result

SPECint_rate2006 = 1380
SPECint_rate_base2006 = 1330

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Apr-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>56</td>
<td>529</td>
<td>1.030</td>
<td>528</td>
<td>1.040</td>
<td>528</td>
<td>1.040</td>
<td>56</td>
<td>433</td>
<td>1.260</td>
<td>433</td>
<td>1.260</td>
</tr>
<tr>
<td>bzip2</td>
<td>56</td>
<td>834</td>
<td>1.648</td>
<td>836</td>
<td>1.647</td>
<td>833</td>
<td>1.649</td>
<td>56</td>
<td>812</td>
<td>1.666</td>
<td>811</td>
<td>1.667</td>
</tr>
<tr>
<td>gcc</td>
<td>56</td>
<td>466</td>
<td>0.968</td>
<td>467</td>
<td>0.966</td>
<td>463</td>
<td>0.973</td>
<td>56</td>
<td>465</td>
<td>0.970</td>
<td>466</td>
<td>0.968</td>
</tr>
<tr>
<td>mcf</td>
<td>56</td>
<td>304</td>
<td>1.680</td>
<td>302</td>
<td>1.690</td>
<td>304</td>
<td>1.680</td>
<td>56</td>
<td>304</td>
<td>1.680</td>
<td>302</td>
<td>1.690</td>
</tr>
<tr>
<td>gobmk</td>
<td>56</td>
<td>658</td>
<td>0.893</td>
<td>658</td>
<td>0.893</td>
<td>658</td>
<td>0.892</td>
<td>56</td>
<td>624</td>
<td>0.942</td>
<td>623</td>
<td>0.943</td>
</tr>
<tr>
<td>hammer</td>
<td>56</td>
<td>272</td>
<td>1.920</td>
<td>271</td>
<td>1.930</td>
<td>269</td>
<td>1.940</td>
<td>56</td>
<td>244</td>
<td>2.140</td>
<td>245</td>
<td>2.130</td>
</tr>
<tr>
<td>sjeng</td>
<td>56</td>
<td>693</td>
<td>0.978</td>
<td>693</td>
<td>0.978</td>
<td>694</td>
<td>0.977</td>
<td>56</td>
<td>654</td>
<td>1.040</td>
<td>654</td>
<td>1.040</td>
</tr>
<tr>
<td>libquantum</td>
<td>56</td>
<td>81.2</td>
<td>1.4300</td>
<td>81.4</td>
<td>1.4300</td>
<td>81.4</td>
<td>1.4300</td>
<td>56</td>
<td>81.2</td>
<td>1.4300</td>
<td>81.4</td>
<td>1.4300</td>
</tr>
<tr>
<td>h264ref</td>
<td>56</td>
<td>775</td>
<td>1.600</td>
<td>768</td>
<td>1.610</td>
<td>767</td>
<td>1.620</td>
<td>56</td>
<td>759</td>
<td>1.630</td>
<td>754</td>
<td>1.640</td>
</tr>
<tr>
<td>omnetpp</td>
<td>56</td>
<td>548</td>
<td>0.638</td>
<td>548</td>
<td>0.638</td>
<td>548</td>
<td>0.639</td>
<td>56</td>
<td>624</td>
<td>0.668</td>
<td>624</td>
<td>0.668</td>
</tr>
<tr>
<td>astar</td>
<td>56</td>
<td>521</td>
<td>0.755</td>
<td>520</td>
<td>0.756</td>
<td>521</td>
<td>0.755</td>
<td>56</td>
<td>521</td>
<td>0.755</td>
<td>520</td>
<td>0.756</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>56</td>
<td>263</td>
<td>1.470</td>
<td>263</td>
<td>1.470</td>
<td>262</td>
<td>1.470</td>
<td>56</td>
<td>263</td>
<td>1.470</td>
<td>263</td>
<td>1.470</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes
BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Cluster On Die
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Double Refresh Rate set to 1x Refresh

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Platform Notes (Continued)

Energy Performance Bias set to Maximum Performance
Sysinfo program /home/specuser/cpu2006/ic16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-szds Fri Apr 15 17:06:15 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v4@ 2.60GHz
  2 "physical id"s (chips)
  56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 14
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB

From /proc/meminfo
MemTotal:       529090292 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:  
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 15 17:05

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Platform Notes (Continued)

SPEC is set to: /home/specuser/cpu2006/ic16  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 703G 279G 424G 40% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  

BIOS HP P92 03/23/2016  
Memory:  
8x UNKNOWN NOT AVAILABLE  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz  

(End of data from sysinfo program)  
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz  

General Notes

Environment variables set by runspec before the start of the run:  
LD_LIBRARY_PATH = */home/specuser/cpu2006/ic16/libs/32:/home/specuser/cpu2006/ic16/libs/64:/home/specuser/cpu2006/ic16/sh*  

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  

Base Compiler Invocation

C benchmarks:  
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin  
C++ benchmarks:  
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin  

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32  
401.bzip2: -D_FILE_OFFSET_BITS=64  
403.gcc: -D_FILE_OFFSET_BITS=64  
429.mcf: -D_FILE_OFFSET_BITS=64  
445.gobmk: -D_FILE_OFFSET_BITS=64  
456.hmmer: -D_FILE_OFFSET_BITS=64  
458.sjeng: -D_FILE_OFFSET_BITS=64  

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Base Portability Flags (Continued)

462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

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Peak Portability Flags (Continued)

401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
            -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
           -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
              -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
  -opt-ra-region-strategy=block -Wl,-z,muldefs
  -L/sh -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml

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