



# SPEC<sup>®</sup> CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL380 Gen9

(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp<sup>®</sup>2006 =

111

SPECfp\_base2006 =

104

CPU2006 license: 3

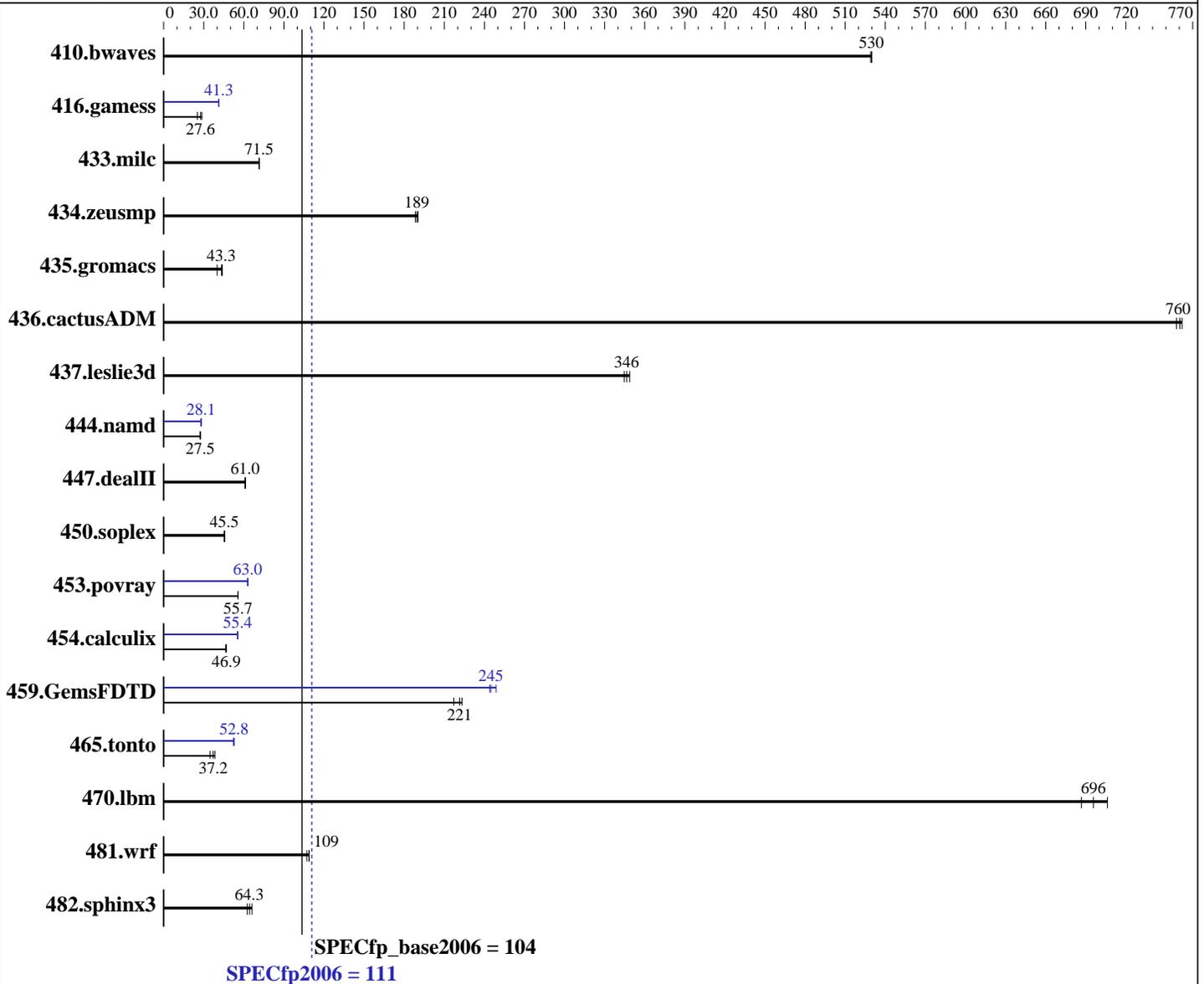
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Nov-2015



## Hardware

CPU Name: Intel Xeon E5-2630 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.10 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: Red Hat Enterprise Linux Server release 7.2, (Maipo)  
 Kernel 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs

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L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
 Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
 Other Hardware: None

System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	<u>25.7</u>	<u>530</u>	25.7	529	25.6	530	<u>25.7</u>	<u>530</u>	25.7	529	25.6	530
416.gamess	683	28.7	<u>709</u>	<u>27.6</u>	773	25.3	<u>474</u>	<u>41.3</u>	474	41.3	474	41.3
433.milc	<u>128</u>	<u>71.5</u>	128	71.5	129	71.4	<u>128</u>	<u>71.5</u>	128	71.5	129	71.4
434.zeusmp	48.3	188	47.8	190	<u>48.0</u>	<u>189</u>	48.3	188	47.8	190	<u>48.0</u>	<u>189</u>
435.gromacs	<u>165</u>	<u>43.3</u>	162	44.0	178	40.0	<u>165</u>	<u>43.3</u>	162	44.0	178	40.0
436.cactusADM	15.8	758	<u>15.7</u>	<u>760</u>	15.7	762	15.8	758	<u>15.7</u>	<u>760</u>	15.7	762
437.leslie3d	27.0	349	27.3	345	<u>27.1</u>	<u>346</u>	27.0	349	27.3	345	<u>27.1</u>	<u>346</u>
444.namd	292	27.5	<u>292</u>	<u>27.5</u>	292	27.5	<u>286</u>	<u>28.1</u>	285	28.1	286	28.0
447.dealII	<u>187</u>	<u>61.0</u>	188	61.0	187	61.2	<u>187</u>	<u>61.0</u>	188	61.0	187	61.2
450.soplex	<u>183</u>	<u>45.5</u>	184	45.4	182	45.8	<u>183</u>	<u>45.5</u>	184	45.4	182	45.8
453.povray	95.6	55.6	<u>95.5</u>	<u>55.7</u>	95.5	55.7	84.4	63.0	<u>84.4</u>	<u>63.0</u>	84.6	62.9
454.calculix	178	46.4	176	47.0	<u>176</u>	<u>46.9</u>	<u>149</u>	<u>55.4</u>	149	55.5	149	55.4
459.GemsFDTD	<u>47.9</u>	<u>221</u>	48.9	217	47.5	223	<u>43.3</u>	<u>245</u>	42.7	249	43.5	244
465.tonto	<u>265</u>	<u>37.2</u>	256	38.5	283	34.8	186	52.8	188	52.2	<u>186</u>	<u>52.8</u>
470.lbm	19.5	706	20.0	687	<u>19.7</u>	<u>696</u>	19.5	706	20.0	687	<u>19.7</u>	<u>696</u>
481.wrf	<u>103</u>	<u>109</u>	103	109	104	107	<u>103</u>	<u>109</u>	103	109	104	107
482.sphinx3	295	66.1	<u>303</u>	<u>64.3</u>	312	62.5	295	66.1	<u>303</u>	<u>64.3</u>	312	62.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

## Platform Notes

BIOS Configuration:

HP Power Profile set to Custom

HP Power Regulator to HP Static High Performance Mode

Minimum Processor Idle Power Core C-State set to C1E State

Minimum Processor Idle Power Package C-State set to No Package State

QPI Snoop Configuration set to Home Snoop

Collaborative Power Control set to Disabled

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**111**

**SPECfp\_base2006 =**

**104**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Mar-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Nov-2015

## Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on DL380Gen9allbin Sun Apr 3 22:12:36 2016

This section contains SUT (System Under Test) info as seen by  
some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 20
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      528065708 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux DL380Gen9allbin 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Apr 3 22:01
```

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
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## Platform Notes (Continued)

/dev/sda5 xfs 318G 142G 177G 45% /home  
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 03/10/2016

Memory:

8x UNKNOWN NOT AVAILABLE

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

OMP\_NUM\_THREADS = "20"

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Fortran benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -fp-model fast=2
-qopt-prefetch-issue-excl-hint

```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

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## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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