**Lenovo Group Limited**

Lenovo ThinkServer RD450  
(2.10 GHz, Intel Xeon E5-2620 v4)

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>108</th>
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<tbody>
<tr>
<td>SPECfp_base2006</td>
<td>102</td>
</tr>
</tbody>
</table>

**Software**

- Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)  
  Kernel 3.12.49-11-default
- Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- Auto Parallel: Yes
- File System: xfs
- System State: Run level 3 (multi-user)

**Hardware**

- CPU Name: Intel Xeon E5-2620 v4
- CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
- CPU MHz: 2100
- FPU: Integrated
- CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
- CPU(s) orderable: 1.2 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core

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**Test date:** Apr-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Dec-2015
Lenovo Group Limited

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SPECfp2006 = 108
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CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited
L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHZ)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

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<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>26.7</td>
<td>509</td>
<td>26.7</td>
<td>509</td>
<td>26.8</td>
<td>507</td>
<td>26.7</td>
<td>509</td>
<td>26.7</td>
<td>507</td>
</tr>
<tr>
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<td>598</td>
<td>32.7</td>
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<td>598</td>
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<td>470</td>
<td>41.7</td>
<td>470</td>
<td>41.7</td>
</tr>
<tr>
<td>433.milc</td>
<td>137</td>
<td>67.2</td>
<td>136</td>
<td>67.3</td>
<td>138</td>
<td>66.6</td>
<td>137</td>
<td>67.2</td>
<td>136</td>
<td>67.3</td>
</tr>
<tr>
<td>434.zesmp</td>
<td>57.6</td>
<td>158</td>
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<td>154</td>
<td>58.2</td>
<td>156</td>
<td>57.6</td>
<td>158</td>
<td>59.2</td>
<td>154</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>158</td>
<td>45.1</td>
<td>155</td>
<td>46.0</td>
<td>155</td>
<td>46.1</td>
<td>158</td>
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<td>155</td>
<td>46.0</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>17.1</td>
<td>700</td>
<td>17.0</td>
<td>703</td>
<td>17.1</td>
<td>700</td>
<td>17.1</td>
<td>700</td>
<td>17.1</td>
<td>700</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>32.0</td>
<td>294</td>
<td>34.5</td>
<td>273</td>
<td>33.5</td>
<td>290</td>
<td>32.0</td>
<td>294</td>
<td>34.5</td>
<td>273</td>
</tr>
<tr>
<td>444.namd</td>
<td>285</td>
<td>28.2</td>
<td>284</td>
<td>28.2</td>
<td>285</td>
<td>28.2</td>
<td>276</td>
<td>29.0</td>
<td>276</td>
<td>29.0</td>
</tr>
<tr>
<td>447.dealII</td>
<td>186</td>
<td>61.5</td>
<td>187</td>
<td>61.3</td>
<td>186</td>
<td>61.4</td>
<td>186</td>
<td>61.5</td>
<td>187</td>
<td>61.3</td>
</tr>
<tr>
<td>450.soplex</td>
<td>193</td>
<td>43.3</td>
<td>195</td>
<td>42.8</td>
<td>195</td>
<td>42.9</td>
<td>193</td>
<td>43.3</td>
<td>195</td>
<td>42.8</td>
</tr>
<tr>
<td>453.povray</td>
<td>93.1</td>
<td>57.1</td>
<td>93.4</td>
<td>57.0</td>
<td>92.9</td>
<td>57.2</td>
<td>82.3</td>
<td>64.7</td>
<td>82.7</td>
<td>64.3</td>
</tr>
<tr>
<td>454.calculx</td>
<td>156</td>
<td>53.0</td>
<td>156</td>
<td>53.0</td>
<td>155</td>
<td>53.1</td>
<td>141</td>
<td>58.4</td>
<td>141</td>
<td>58.4</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>58.4</td>
<td>182</td>
<td>53.5</td>
<td>198</td>
<td>54.6</td>
<td>194</td>
<td>46.7</td>
<td>227</td>
<td>47.1</td>
<td>225</td>
</tr>
<tr>
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<td>246</td>
<td>40.0</td>
<td>249</td>
<td>39.5</td>
<td>245</td>
<td>40.1</td>
<td>182</td>
<td>54.2</td>
<td>180</td>
<td>54.5</td>
</tr>
<tr>
<td>470.lbm</td>
<td>19.2</td>
<td>715</td>
<td>19.1</td>
<td>718</td>
<td>19.1</td>
<td>719</td>
<td>19.2</td>
<td>715</td>
<td>19.1</td>
<td>718</td>
</tr>
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<td>481.wrf</td>
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<td>108</td>
<td>104</td>
<td>109</td>
<td>103</td>
<td>108</td>
<td>103</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>280</td>
<td>69.6</td>
<td>280</td>
<td>69.6</td>
<td>280</td>
<td>69.6</td>
<td>280</td>
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<td>69.6</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Cluster On Die set to Disabled
Early Snoop set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed

Continued on next page
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| SPECfuel2006 | 108 |
| SPECfuel_base2006 | 102 |

**CPU2006 license:** 9017  
**Test date:** Apr-2016  
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**Hardware Availability:** Mar-2016  
**Tested by:** Lenovo Group Limited  
**Software Availability:** Dec-2015

**Platform Notes (Continued)**

Memory Power Savings set to Disabled  
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914  
$Rev: 6914 $ $Date:: 2014-06-25 # e3fbb8667b5a285932ceab81e28219e1  
running on RD450-MLK Tue Apr 5 13:44:32 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

From /proc/meminfo

```
MemTotal:       264558924 kB
HugePages_Total:       0
Hugepagesize:       2048 kB
```

From /etc/*release* /etc/*version*

```
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
  x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 5 08:18
```

```
SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem     Type Size  Used Avail Use% Mounted on
```

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Platform Notes (Continued)
/dev/sda3      xfs   693G  3.9G  690G   1% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO VB3TS362 03/24/2016
Memory:
  16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at
  2133 MHz

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation
C benchmarks:
icc   -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Base Portability Flags
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64

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SPEC CFP2006 Result

Lenovo Group Limited
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Base Portability Flags (Continued)

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Lenovo Group Limited

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CPU2006 license: 9017
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Hardware Availability: Mar-2016
Tested by: Lenovo Group Limited
Software Availability: Dec-2015

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
            -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
            -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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# SPEC CFP2006 Result

**Lenovo Group Limited**

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**CPU2006 license:** 9017  
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**Software Availability:** Dec-2015

## Peak Optimization Flags (Continued)

465.tonto (continued):

- `opt-malloc-options=3`  
- `auto`  
- `unroll4`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: `-xCORE-AVX2`  
- `ipo -O3`  
- `-no-prec-div`  
- `auto-ilp32`  
- `ansi-alias`

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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For other inquiries, please contact webmaster@spec.org.

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