**SPEC® CFP2006 Result**

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
ProLiant DL380 Gen9  
(1.80 GHz, Intel Xeon E5-2630L v4)

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>SPECfp_base2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>104</td>
<td>98.1</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE

<table>
<thead>
<tr>
<th>Test date:</th>
<th>Apr-2016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Mar-2016</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Nov-2015</td>
</tr>
</tbody>
</table>

**Hardware**

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E5-2630L v4</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Characteristics:</td>
<td>Intel Turbo Boost Technology up to 2.90 GHz</td>
</tr>
<tr>
<td>CPU MHz:</td>
<td>1800</td>
</tr>
<tr>
<td>FPU:</td>
<td>Integrated</td>
</tr>
<tr>
<td>CPU(s) enabled:</td>
<td>20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td>CPU(s) orderable:</td>
<td>1,2 chip</td>
</tr>
<tr>
<td>Primary Cache:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Secondary Cache:</td>
<td>256 KB I+D on chip per core</td>
</tr>
</tbody>
</table>

**Software**

<table>
<thead>
<tr>
<th>Operating System:</th>
<th>Red Hat Enterprise Linux Server release 7.2, (Maipo)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux</td>
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<tr>
<td>Auto Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
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**SPECfp®2006 = 104**

**SPECfp_base2006 = 98.1**

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**ProLiant DL380 Gen9**  
**CPU2006 license:** 3  
**Test date:** Apr-2016  
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### Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>410.bwaves</td>
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<td>191</td>
<td>17.1</td>
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<td>695</td>
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<td>701</td>
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<td>416.gamess</td>
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<td>16.9</td>
<td>707</td>
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<td>338</td>
<td>57.7</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
```
    echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

### Platform Notes

**BIOS Configuration:**  
HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C1E State  
Minimum Processor Idle Power Package C-State set to No Package State  
QPI Snoop Configuration set to Home Snoop  
Collaborative Power Control set to Disabled  
Continued on next page
SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(1.80 GHz, Intel Xeon E5-2630L v4)

SPECfp2006 = 104
SPECfp_base2006 = 98.1

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh

Sysinfo program /home/new_fp/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on DL380Gen9allbin Sun Apr 17 21:37:01 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
cautions.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal: 528065708 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
uname -a:
Linux DL380Gen9allbin 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT
2015 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Apr 17 21:25

SPEC is set to: /home/new_fp/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
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Platform Notes (Continued)
/dev/sda5 xfs 318G 88G 231G 28% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 03/23/2016  
Memory:  
8x UNKNOWN NOT AVAILABLE  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:  
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"  
OMP_NUM_THREADS = "20"  
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB memory using RedHat EL 7.2

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64

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Tested by: HPE

Base Portability Flags (Continued)

433. milc: -DSPEC_CPU_LP64
434. zemu: -DSPEC_CPU_LP64
435. gromacs: -DSPEC_CPU_LP64 -nofor_main
436. cactusADM: -DSPEC_CPU_LP64 -nofor_main
437. lesie3d: -DSPEC_CPU_LP64
444. namd: -DSPEC_CPU_LP64 -nofor_main
447. dealII: -DSPEC_CPU_LP64
450. soplex: -DSPEC_CPU_LP64
453. povray: -DSPEC_CPU_LP64
454. calculix: -DSPEC_CPU_LP64 -nofor_main
459. GemsFDTD: -DSPEC_CPU_LP64
465. tonto: -DSPEC_CPU_LP64
470. lbm: -DSPEC_CPU_LP64
481. wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482. sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc  -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
     -opt-prefetch -ansi-alias
     -fp-model fast=2
     -qopt-prefetch-issue-excl-hint -funroll-all-loops
```

C++ benchmarks:

```
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
     -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
     -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
     -auto-ilp32
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
     -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
     -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
     -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
     -opt-prefetch
     -fp-model fast=2
     -qopt-prefetch-issue-excl-hint -funroll-all-loops
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
     -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
     -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
     -inline-level=0 -scalar-rep-
```

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
              -inline-level=0 -opt-prefetch -parallel
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
           -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-BDW-revE.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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