Lenovo Group Limited

Lenovo ThinkServer RD550
(3.40 GHz, Intel Xeon E5-2643 v4)

SPECint®2006 = 70.9
SPECint_base2006 = 67.5

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Apr-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

400.perlbench 41.5 67.5
401.bzip2 26.8
403.gcc 88.4 88.4
429.mcf 31.5 87.4
445.gobmk 31.7 87.1
456.hmmer 36.5
458.sjeng 36.1
462.libquantum
464.h264ref 63.1
471.omnetpp 33.4 88.8
473.astar 30.0 88.8
483.xalancbmk 31.7 87.4

SPECint2006 = 70.9

Hardware

Software

CPU Name: Intel Xeon E5-2643 v4
Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
CPU MHz: 3400
Auto Parallel: Yes
FPU: Integrated
File System: xfs
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
System State: Run level 3 (multi-user)
CPU(s) orderable: 1,2 chips
Base Pointers: 32/64-bit
Primary Cache: 32 KB I + 32 KB D on chip per core
Peak Pointers: 32/64-bit
Secondary Cache: 256 KB I+D on chip per core
Other Software: Microquill SmartHeap V10.2
L3 Cache: 20 MB I+D on chip per chip
Other Hardware: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Cache: None

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
Lenovo Group Limited

Lenovo ThinkServer RD550
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>224</td>
<td>43.6</td>
<td>225</td>
<td>43.4</td>
<td>225</td>
<td>43.5</td>
<td>206</td>
<td>47.4</td>
<td>206</td>
<td>47.5</td>
<td>206</td>
<td>47.5</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>361</td>
<td>26.8</td>
<td>361</td>
<td>26.8</td>
<td>359</td>
<td>26.9</td>
<td>354</td>
<td>27.3</td>
<td>354</td>
<td>27.3</td>
<td>353</td>
<td>27.3</td>
</tr>
<tr>
<td>403.mcf</td>
<td>210</td>
<td>38.4</td>
<td>210</td>
<td>38.4</td>
<td>210</td>
<td>38.4</td>
<td>212</td>
<td>37.9</td>
<td>213</td>
<td>37.7</td>
<td>211</td>
<td>38.1</td>
</tr>
<tr>
<td>429.gcc</td>
<td>136</td>
<td>67.3</td>
<td>134</td>
<td>68.0</td>
<td>135</td>
<td>67.5</td>
<td>133</td>
<td>68.4</td>
<td>132</td>
<td>68.9</td>
<td>133</td>
<td>68.3</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>331</td>
<td>31.7</td>
<td>331</td>
<td>31.7</td>
<td>331</td>
<td>31.7</td>
<td>333</td>
<td>31.5</td>
<td>333</td>
<td>31.5</td>
<td>333</td>
<td>31.5</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>102</td>
<td>91.1</td>
<td>102</td>
<td>91.3</td>
<td>102</td>
<td>91.0</td>
<td>102</td>
<td>91.1</td>
<td>102</td>
<td>91.3</td>
<td>102</td>
<td>91.0</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>335</td>
<td>36.1</td>
<td>335</td>
<td>36.1</td>
<td>335</td>
<td>36.1</td>
<td>332</td>
<td>36.5</td>
<td>331</td>
<td>36.5</td>
<td>331</td>
<td>36.5</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4.18</td>
<td>4960</td>
<td>4.17</td>
<td>4970</td>
<td>4.16</td>
<td>4980</td>
<td>4.18</td>
<td>4960</td>
<td>4.17</td>
<td>4970</td>
<td>4.16</td>
<td>4980</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>351</td>
<td>63.1</td>
<td>350</td>
<td>63.2</td>
<td>352</td>
<td>62.9</td>
<td>351</td>
<td>63.1</td>
<td>350</td>
<td>63.2</td>
<td>352</td>
<td>62.9</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>208</td>
<td>30.0</td>
<td>208</td>
<td>30.1</td>
<td>208</td>
<td>30.0</td>
<td>144</td>
<td>43.5</td>
<td>144</td>
<td>43.3</td>
<td>144</td>
<td>43.4</td>
</tr>
<tr>
<td>473.astar</td>
<td>181</td>
<td>38.7</td>
<td>181</td>
<td>38.9</td>
<td>181</td>
<td>38.8</td>
<td>180</td>
<td>38.9</td>
<td>181</td>
<td>38.8</td>
<td>182</td>
<td>38.7</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>89.6</td>
<td>77.0</td>
<td>89.2</td>
<td>77.4</td>
<td>89.1</td>
<td>77.4</td>
<td>79.2</td>
<td>87.1</td>
<td>79.6</td>
<td>86.7</td>
<td>79.0</td>
<td>87.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Cluster On Die set to Disabled
Early Snoop set to Enabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on rd550-mlk-rackA02 Fri Apr 22 15:31:28 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

Continued on next page
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Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz
2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 6 7
physical 1: cores 0 1 2 3 6 7
cache size : 20480 KB

From /proc/meminfo
MemTotal:  264558460 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  Linux rd550-mlk-rackA02 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
  2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Apr 22 15:28

SPEC is set to: /home/cpu2006-1.2-ic16.0

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB1TS362 03/24/2016
Memory:
Continued on next page
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Platform Notes (Continued)

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Continued on next page
# Lenovo Group Limited

**Lenovo ThinkServer RD550**  
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</tbody>
</table>

## Base Optimization Flags (Continued)

C++ benchmarks:
- -xCORE-AVX2
- -ipo
- -O3
- -no-prec-div
- -opt-prefetch
- -auto-p32
- -Wl,-z,muldefs
- -L/sh
- -lsmartheap64

## Base Other Flags

C benchmarks:
- 403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
- icc -m64

    400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

    445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
- icpc -m32

    473.astar: icpc -m64

## Peak Portability Flags

<table>
<thead>
<tr>
<th>C benchmarks</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>403.gcc</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
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<td>429.mcf</td>
<td>-DSPEC_CPU_LP64</td>
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<tr>
<td>462.libquantum</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
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<td>471.omnetpp</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
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<td>-DSPEC_CPU_LP64</td>
</tr>
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Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap
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Tested with SPEC CPU2006 v1.2.
Originally published on 1 June 2016.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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Peak Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml