## SPEC® CFP2006 Result

### Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant BL460c Gen9

(2.10 GHz, Intel Xeon E5-2620 v4)

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>105</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_base2006</td>
<td>98.8</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon E5-2620 v4
- **CPU Characteristics:** Intel Turbo Boost Technology up to 3.00 GHz
- **CPU MHz:** 2100
- **FPU:** Integrated
- **CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip, 2 threads/core
- **CPU(s) orderable:** 1.2 chip
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 256 KB I+D on chip per core

### Software

- **Operating System:** SuSE Linux Enterprise Server 12 (x86_64) SP 1, Kernel 3.12.49-11-default
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
- **Auto Parallel:** Yes
- **File System:** xfs
- **System State:** Run level 5 (multi-user, w/GUI)

### Test Details

- **CPU2006 license:** 3
- **Test date:** Mar-2016
- **Hardware Availability:** Mar-2016
- **Tested by:** HPE
- **Software Availability:** Dec-2015
- **Test sponsor:** HPE

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### Performance Results

- **SPECfp2006 = 105**
- **SPECfp_base2006 = 98.8**

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Hewlett Packard Enterprise  
(2.10 GHz, Intel Xeon E5-2620 v4)

SPECfp2006 = 105  
SPECfp_base2006 = 98.8

Results Table

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<th>Seconds</th>
<th>Ratio</th>
<th>Base Pointers: 64-bit</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Pointers: 32/64-bit</th>
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<td>31.0</td>
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<td>481.wrf</td>
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<td>105</td>
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<td>482.sphinx3</td>
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<td>64.2</td>
<td>304</td>
<td>64.2</td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
Intel Hyperthreading Option set to Enabled
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C1E State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled

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SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant BL460c Gen9
(2.10 GHz, Intel Xeon E5-2620 v4)

SPECfp2006 = 105
SPECfp_base2006 = 98.8

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Mar-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Platform Notes (Continued)

QPI Snoop Configuration set to Home Snoop
Thermal Configuration set to Maximum Cooling
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpuv1.5/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on bl460c2-gen9-b Fri May 27 11:10:31 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

From /proc/meminfo
MemTotal:       264328212 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

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**SPEC CFP2006 Result**

**SPECfp2006 =** 105
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</tbody>
</table>

**Platform Notes (Continued)**

(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 May 27 11:04

SPEC is set to: /home/cpuv1.5/cpu2006

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda4</td>
<td>xfs</td>
<td>424G</td>
<td>199G</td>
<td>226G</td>
<td>47%</td>
<td>/home</td>
</tr>
</tbody>
</table>

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 04/12/2016
Memory:
8x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

**General Notes**

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpuv1.5/cpu2006/libs/32:/home/cpuv1.5/cpu2006/libs/64:/home/cpuv1.5/cpu2006/sh"
OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

**Base Compiler Invocation**

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Hewlett Packard Enterprise
(Test Sponsor: HPE)
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SPECfp2006 = 105
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CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc  -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

C++ benchmarks:

- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
  -auto-ilk32
- 447.dealII: basepeak = yes
- 450.soplex: basepeak = yes
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
  -ansi-alias

Fortran benchmarks:

- 410.bwaves: basepeak = yes
- 416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
  -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
  -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
  -inline-level=0 -scalar-rep-
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
## SPEC CFP2006 Result

**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant BL460c Gen9  
(2.10 GHz, Intel Xeon E5-2620 v4)  

| SPECfp2006 = | 105 |
| SPECfp_base2006 = | 98.8 |

**CPU2006 license:** 3  
**Test date:** Mar-2016  
**Test sponsor:** HPE  
**Hardware Availability:** Mar-2016  
**Tested by:** HPE  
**Software Availability:** Dec-2015

### Peak Optimization Flags (Continued)

**459.GemsFDTD:**  
-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ip0(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

**465.tonto:**  
-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ip0(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

**435.gromacs:** basepeak = yes  
**436.cactusADM:** basepeak = yes  
**454.calculix:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias  
481.wrf: basepeak = yes

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The flags files that were used to format this result can be browsed at  
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html  
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml  
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

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For other inquiries, please contact webmaster@spec.org.

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