SPEC® CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

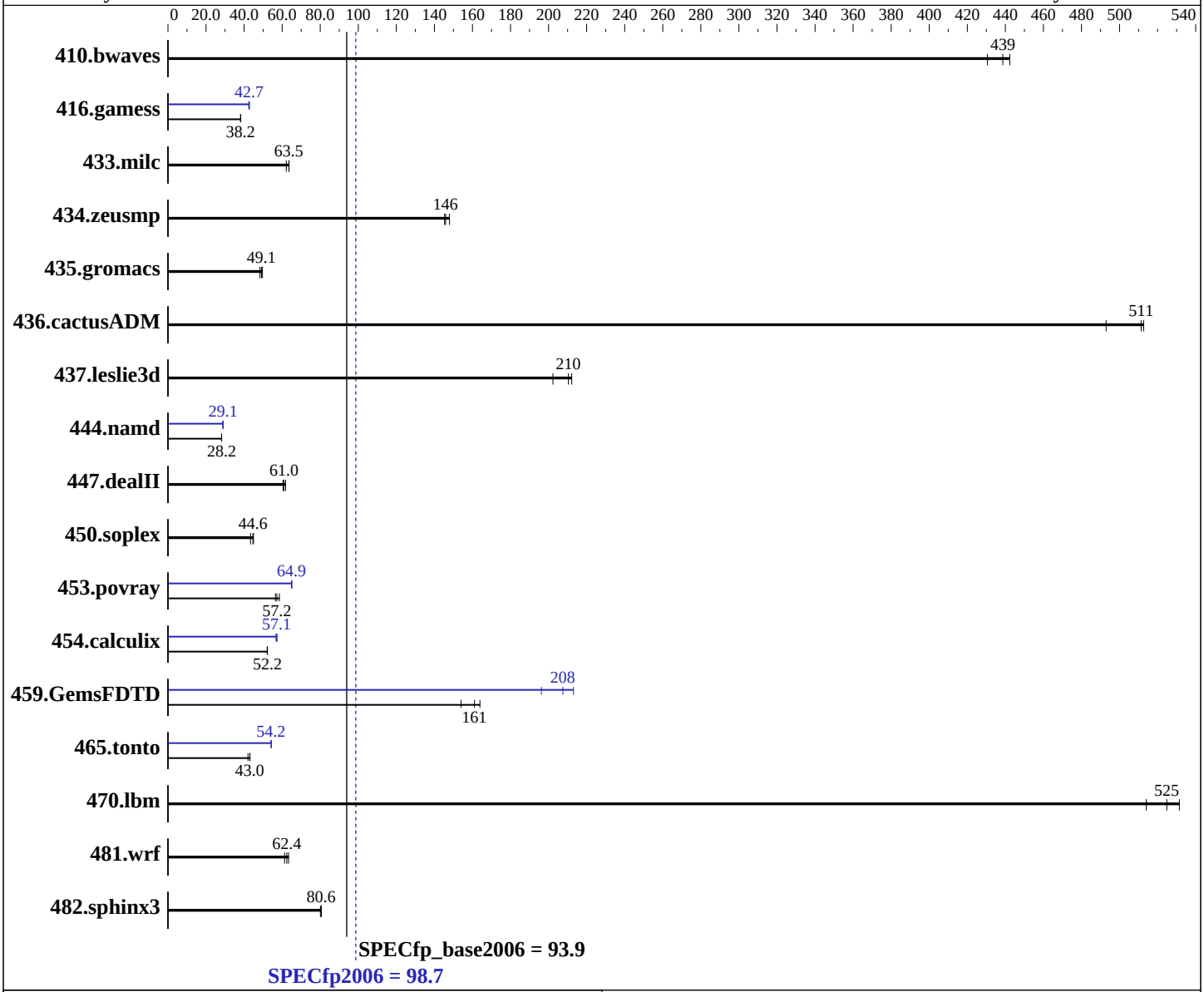
PowerEdge FC830 (Intel Xeon E5-4627 v4, 2.60 GHz)

SPECfp®2006 = 98.7

SPECfp_base2006 = 93.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016



Hardware

CPU Name:	Intel Xeon E5-4627 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	btrfs
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge FC830 (Intel Xeon E5-4627 v4, 2.60 GHz)

SPECfp2006 = 98.7

SPECfp_base2006 = 93.9

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: May-2016
Hardware Availability: Jun-2016
Software Availability: Mar-2016

L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 2Rx8 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	31.6	431	30.7	442	**31.0**	**439**	31.6	431	30.7	442	**31.0**	**439**
416.gamess	512	38.3	513	38.1	**513**	**38.2**	458	42.7	461	42.5	**458**	**42.7**
433.milc	**144**	**63.5**	148	62.2	144	63.6	**144**	**63.5**	148	62.2	144	63.6
434.zeusmp	62.6	145	61.5	148	**62.4**	**146**	62.6	145	61.5	148	**62.4**	**146**
435.gromacs	148	48.3	144	49.7	**145**	**49.1**	148	48.3	144	49.7	**145**	**49.1**
436.cactusADM	24.2	493	**23.4**	**511**	23.3	513	24.2	493	**23.4**	**511**	23.3	513
437.leslie3d	44.3	212	46.5	202	**44.7**	**210**	44.3	212	46.5	202	**44.7**	**210**
444.namd	**285**	**28.2**	285	28.2	284	28.2	279	28.7	**276**	**29.1**	276	29.1
447.dealII	**188**	**61.0**	189	60.5	185	61.8	**188**	**61.0**	189	60.5	185	61.8
450.soplex	**187**	**44.6**	192	43.4	186	44.9	**187**	**44.6**	192	43.4	186	44.9
453.povray	**93.0**	**57.2**	90.8	58.6	94.2	56.5	82.0	64.9	81.6	65.2	**82.0**	**64.9**
454.calculix	158	52.3	158	52.1	**158**	**52.2**	145	56.8	144	57.3	**144**	**57.1**
459.GemsFDTD	68.9	154	**65.9**	**161**	64.7	164	49.8	213	54.1	196	**51.1**	**208**
465.tonto	228	43.1	234	42.0	**229**	**43.0**	181	54.3	181	54.2	**181**	**54.2**
470.lbm	25.9	531	**26.2**	**525**	26.7	514	25.9	531	**26.2**	**525**	26.7	514
481.wrf	176	63.4	**179**	**62.4**	182	61.3	176	63.4	**179**	**62.4**	182	61.3
482.sphinx3	242	80.6	243	80.1	**242**	**80.6**	242	80.6	243	80.1	**242**	**80.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS settings:
Virtualization Technology disabled
System Profile set to custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Energy Efficient Turbo disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
```

Continued on next page

Platform Notes (Continued)

```
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on linux-vs2o Tue May 17 14:31:11 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4627 v4 @ 2.60GHz
      4 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 10
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
      physical 2: cores 0 1 2 3 4 8 9 10 11 12
      physical 3: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       529195644 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 12 SP1

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 1
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP1"
      VERSION_ID="12.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
   Linux linux-vs2o 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
   (8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page

Platform Notes (Continued)

```
run-level 3 May 17 09:16 last=5

SPEC is set to: /root/cpu2006-1.2
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda2      xfs   365G   11G  354G   3% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Dell Inc. 2.0.2 04/14/2016
  Memory:
   20x 00AD00B300AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz
   12x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz
   16x Not Specified Not Specified

(End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
OMP_NUM_THREADS = "40"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

459.GemsFDTD: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-unroll2 -inline-level=0 -opt-prefetch -parallel`

465.tonto: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jun 28 17:29:56 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 June 2016.