Lenovo Group Limited

Lenovo Flex System x240 M5
(2.20 GHz, Intel Xeon E5-2630 v4)

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

SPECfp®2006 = 113
SPECfp_base2006 = 106

Hardware

<table>
<thead>
<tr>
<th>Test</th>
<th>SPECfp</th>
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<tbody>
<tr>
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<tr>
<td>416.gamess</td>
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<td>436.cactusADM</td>
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<tr>
<td>482.sphinx3</td>
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</tr>
</tbody>
</table>

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Kernel 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
## Lenovo Group Limited

Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)  

| SPECfp2006 | 113 |
| SPECfp_base2006 | 106 |

| CPU2006 license: | 9017 |
| Test sponsor: | Lenovo Group Limited |
| Tested by: | Lenovo Group Limited |
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz) |
| Disk Subsystem: | 2 x 300 GB 10000 RPM SAS, RAID 0 |
| Other Hardware: | None |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

### Results Table

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<thead>
<tr>
<th>Benchmark</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>67.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

- **BIOS Configuration:**
  - Operating Mode set to Maximum Performance
  - Hyper-Threading set to Disabled
  - COD Preference set to Disable
- **Sysinfo program**:
  - `/home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914`
  - `$Rev: 6914 $ $Date:: 2014-06-25 $$ e3fbb8667b5a285932ceab81e28219e1`
  - Running on x240m5-cpu2006 Wed May 4 15:20:49 2016

This section contains SUT (System Under Test) info as seen by

Continued on next page
Lenovo Group Limited

Lenovo Flex System x240 M5
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp2006 = 113
SPECfp_base2006 = 106

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: May-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2630 v4 @ 2.20GHz
  2 "physical id"s (chips)
  20 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
  cpu cores : 10
  siblings : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

From /proc/meminfo
  MemTotal:       263829640 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  Linux x240m5-cpu2006 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 4 10:00

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4   xfs  515G 7.1G 508G 2% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
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Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO -[C4E123H-2.10]- 03/25/2016
Memory:
16x Hynix HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz
8x NO DIMM Unknown

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh*
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages disabled with:
echo never > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64 -nfor_main
436.cactusADM: -DSPEC_CPU_LP64 -nfor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

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Lenovo Group Limited
Lenovo Flex System x240 M5
(2.20 GHz, Intel Xeon E5-2630 v4)

SPECfp2006 = 113
SPECfp_base2006 = 106

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: May-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Base Portability Flags (Continued)

450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc  -m64 ifort  -m64

Peak Portability Flags

Same as Base Portability Flags
Lenovo Group Limited  
Lenovo Flex System x240 M5  
(2.20 GHz, Intel Xeon E5-2630 v4)  

**SPEC CFP2006 Result**  

<table>
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CPU2006 license: 9017  
Test sponsor: Lenovo Group Limited  
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Test date: May-2016  
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**Peak Optimization Flags**

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)  
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)  
-par-num-threads=1 (pass 1) -prof-use (pass 2) -fno-alias  
-auto-llp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes

453.povray: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)  
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)  
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll4  
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)  
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)  
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)  
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)  
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)  
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)  
-par-num-threads=1 (pass 1) -prof-use (pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Lenovo Group Limited
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**Software Availability:** Dec-2015

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**Peak Optimization Flags (Continued)**

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-llp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml