Lenovo Group Limited
Lenovo ThinkServer RD450
(2.40 GHz, Intel Xeon E5-2640 v4)

**CPU2006 license:** 9017
**Test date:** May-2016
**Test sponsor:** Lenovo Group Limited
**Hardware Availability:** Mar-2016
**Tested by:** Lenovo Group Limited
**Software Availability:** Dec-2015

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
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<th>SPECint_rate2006</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>40</td>
<td>610</td>
<td>610</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>40</td>
<td>406</td>
<td>406</td>
</tr>
<tr>
<td>403.gcc</td>
<td>40</td>
<td>616</td>
<td>616</td>
</tr>
<tr>
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<td>40</td>
<td>1090</td>
<td>1090</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>40</td>
<td>563</td>
<td>563</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>40</td>
<td>1470</td>
<td>1470</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>40</td>
<td>1260</td>
<td>1260</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>40</td>
<td>571</td>
<td>571</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>40</td>
<td>1040</td>
<td>1040</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>40</td>
<td>444</td>
<td>444</td>
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<tr>
<td>473.astar</td>
<td>40</td>
<td>493</td>
<td>493</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>40</td>
<td>963</td>
<td>963</td>
</tr>
</tbody>
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**Hardware**

| CPU Name: | Intel Xeon E5-2640 v4 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.40 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 20 cores, 2 chips, 10 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 25 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz) |
| Disk Subsystem: | 1 x 800 GB SATA SSD |
| Other Hardware: | None |

**Operating System:** SUSE Linux Enterprise Server 12 SP1 (x86_64) Kernel 3.12.49-11-default
**Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
**Auto Parallel:** No
**File System:** xfs
**System State:** Run level 3 (multi-user)
**Base Pointers:** 32-bit
**Peak Pointers:** 32/64-bit
**Other Software:** Microquill SmartHeap V10.2
Lenovo Group Limited

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SPEC_rate2006 = 872
SPEC_rate_base2006 = 832

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
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<td></td>
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<td>Base</td>
<td></td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>400.perlbench</td>
<td>40</td>
<td>640</td>
<td>611</td>
<td>641</td>
<td>610</td>
<td>40</td>
<td>521</td>
<td>750</td>
<td>611</td>
<td>522</td>
<td>748</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>40</td>
<td>979</td>
<td>394</td>
<td>980</td>
<td>394</td>
<td>40</td>
<td>951</td>
<td>406</td>
<td>979</td>
<td>406</td>
<td>979</td>
</tr>
<tr>
<td>403.gcc</td>
<td>40</td>
<td>525</td>
<td>613</td>
<td>525</td>
<td>613</td>
<td>40</td>
<td>522</td>
<td>616</td>
<td>525</td>
<td>616</td>
<td>525</td>
</tr>
<tr>
<td>429.mcf</td>
<td>40</td>
<td>334</td>
<td>1090</td>
<td>332</td>
<td>1100</td>
<td>40</td>
<td>334</td>
<td>1090</td>
<td>332</td>
<td>1100</td>
<td>332</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>40</td>
<td>760</td>
<td>552</td>
<td>762</td>
<td>551</td>
<td>40</td>
<td>745</td>
<td>563</td>
<td>750</td>
<td>562</td>
<td>750</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>40</td>
<td>296</td>
<td>1260</td>
<td>296</td>
<td>1260</td>
<td>40</td>
<td>253</td>
<td>1470</td>
<td>296</td>
<td>1470</td>
<td>296</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>40</td>
<td>846</td>
<td>572</td>
<td>847</td>
<td>571</td>
<td>40</td>
<td>799</td>
<td>606</td>
<td>847</td>
<td>606</td>
<td>847</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>40</td>
<td>97.3</td>
<td>8520</td>
<td>97.2</td>
<td>8530</td>
<td>40</td>
<td>97.3</td>
<td>8520</td>
<td>97.2</td>
<td>8530</td>
<td>97.2</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>40</td>
<td>870</td>
<td>1020</td>
<td>871</td>
<td>1020</td>
<td>40</td>
<td>854</td>
<td>1040</td>
<td>875</td>
<td>1010</td>
<td>854</td>
</tr>
<tr>
<td>471.onnetpp</td>
<td>40</td>
<td>599</td>
<td>417</td>
<td>598</td>
<td>418</td>
<td>40</td>
<td>562</td>
<td>444</td>
<td>606</td>
<td>444</td>
<td>606</td>
</tr>
<tr>
<td>473.astar</td>
<td>40</td>
<td>570</td>
<td>493</td>
<td>570</td>
<td>493</td>
<td>40</td>
<td>570</td>
<td>493</td>
<td>570</td>
<td>493</td>
<td>570</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>40</td>
<td>286</td>
<td>965</td>
<td>287</td>
<td>963</td>
<td>40</td>
<td>286</td>
<td>965</td>
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<td>965</td>
<td>286</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches

Platform Notes

BIOS Configuration:
Cluster On Die set to Disabled
Early Snoop set to Disabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on RD450-MLK Sat May 21 00:51:44 2016

Continued on next page
Lenovo Group Limited

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SPECint_rate2006 = 872
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### Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) CPU E5-2640 v4 @ 2.40GHz
- 2 "physical id"s (chips)
- 40 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 10
  - siblings: 20
  - physical 0: cores 0 1 2 3 4 8 9 10 11 12
  - physical 1: cores 0 1 2 3 4 8 9 10 11 12
  - cache size: 25600 KB

From /proc/meminfo
- MemTotal: 264555368 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
- VERSION = 12
- PATCHLEVEL = 1
- # This file is deprecated and will be removed in a future service pack or release.
- # Please check /etc/os-release for details about this release.

```
uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 21 00:50
```

SPEC is set to: /home/cpu2006-1.2-ic16.0

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 693G 4.0G 689G 1% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately Continued on next page
## Lenovo Group Limited

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<td></td>
<td>872</td>
<td>832</td>
</tr>
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</table>

### CPU2006 license: 9017
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### Platform Notes (Continued)

"determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO VB3TS332 12/28/2015
Memory:
16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:
- `LD_LIBRARY_PATH` = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh*

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
- `echo always > /sys/kernel/mm/transparent_hugepage/enabled`
Filesystem page cache cleared with:
- `echo 1 > /proc/sys/vm/drop_caches`
runspec command invoked through numactl i.e.: `numactl --interleave=all runspec <etc>`

### Base Compiler Invocation

C benchmarks:
```
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```
C++ benchmarks:
```
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
```

### Base Portability Flags

- `400.perlbench`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
- `401.bzip2`: `-D_FILE_OFFSET_BITS=64`
- `403.gcc`: `-D_FILE_OFFSET_BITS=64`
- `429.mcf`: `-D_FILE_OFFSET_BITS=64`
- `445.gobmk`: `-D_FILE_OFFSET_BITS=64`
- `456.hmmer`: `-D_FILE_OFFSET_BITS=64`
- `458.sjeng`: `-D_FILE_OFFSET_BITS=64`
- `462.libquantum`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
- `464.h264ref`: `-D_FILE_OFFSET_BITS=64`
- `471.omnetpp`: `-D_FILE_OFFSET_BITS=64`
- `473.astar`: `-D_FILE_OFFSET_BITS=64`
- `483.xalancbmk`: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`
SPEC CINT2006 Result

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Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
456.hmmer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
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Peak Portability Flags (Continued)

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
-opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
-auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes
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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-BDW-revC.xml

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Tested with SPEC CPU2006 v1.2.
Originally published on 28 June 2016.