Lenovo Group Limited

Lenovo ThinkServer RD550 (1.80 GHz, Intel Xeon E5-2630L v4)

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

SPECint®2006 = 58.7
SPECint_base2006 = 56.0

Hardware

CPU Name: Intel Xeon E5-2630L v4
CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
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<td>400.perlbench</td>
<td>283</td>
<td>34.5</td>
<td>283</td>
<td>34.5</td>
<td>284</td>
<td>34.5</td>
<td>259</td>
<td>37.7</td>
<td>259</td>
<td>37.7</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>449</td>
<td>21.5</td>
<td>449</td>
<td>21.5</td>
<td>449</td>
<td>21.5</td>
<td>442</td>
<td>21.8</td>
<td>440</td>
<td>21.9</td>
</tr>
<tr>
<td>403.mcf</td>
<td>246</td>
<td>32.7</td>
<td>246</td>
<td>32.7</td>
<td>246</td>
<td>32.7</td>
<td>248</td>
<td>32.5</td>
<td>248</td>
<td>32.4</td>
</tr>
<tr>
<td>429.gobmk</td>
<td>416</td>
<td>25.2</td>
<td>416</td>
<td>25.2</td>
<td>416</td>
<td>25.2</td>
<td>421</td>
<td>24.9</td>
<td>421</td>
<td>24.9</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>419</td>
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<td>420</td>
<td>28.8</td>
<td>420</td>
<td>28.8</td>
<td>415</td>
<td>29.2</td>
<td>414</td>
<td>29.2</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4.58</td>
<td>4530</td>
<td>4.56</td>
<td>4540</td>
<td>4.56</td>
<td>4540</td>
<td>4.58</td>
<td>4530</td>
<td>4.56</td>
<td>4540</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>470</td>
<td>47.1</td>
<td>470</td>
<td>47.1</td>
<td>469</td>
<td>47.2</td>
<td>470</td>
<td>47.1</td>
<td>470</td>
<td>47.1</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>203</td>
<td>30.8</td>
<td>213</td>
<td>29.4</td>
<td>201</td>
<td>31.0</td>
<td>141</td>
<td>44.2</td>
<td>142</td>
<td>43.9</td>
</tr>
<tr>
<td>473.astar</td>
<td>225</td>
<td>31.2</td>
<td>224</td>
<td>31.4</td>
<td>227</td>
<td>30.9</td>
<td>226</td>
<td>31.0</td>
<td>224</td>
<td>31.3</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>108</td>
<td>63.8</td>
<td>108</td>
<td>63.7</td>
<td>108</td>
<td>64.0</td>
<td>97.3</td>
<td>70.9</td>
<td>97.1</td>
<td>71.0</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The config file option 'submit' was used.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Cluster On Die set to Disabled
Early Snoop set to Enabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on RD550-MLK Thu May 19 18:06:01 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

Standard Performance Evaluation Corporation
info@spec.org
http://www.spec.org/
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**Platform Notes (Continued)**

```plaintext
model name : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
2 "physical id"s (chips)
20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 10
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

---

From /proc/meminfo
MemTotal: 263674140 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 19 18:05

SPEC is set to: /home/cpu2006-1.2-ic16.0

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB1TS335 01/09/2016

Memory:
```
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Platform Notes (Continued)

16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz, configured at
2133 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "*/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

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CPU2006 license: 9017
Test date: May-2016

Test sponsor: Lenovo Group Limited
Hardware Availability: Mar-2016

Tested by: Lenovo Group Limited
Software Availability: Dec-2015

Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

C benchmarks:
403.gcc: -Dalloca=_alloca

Base Other Flags

C benchmarks (except as noted below):
400.perlbench: icc -m64
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
473.astar: icpc -m64

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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Tested by: Lenovo Group Limited
Software Availability: Dec-2015

Peak Optimization Flags

C benchmarks:
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
    -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div
    -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
    -opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
    -opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
    -opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
    -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
    -par-num-threads=1(pass 1) -prof-use(pass 2)
    -opt-ra-region-strategy=block -ansi-alias
    -Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap
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Software Availability: Dec-2015

Peak Other Flags

C benchmarks:

403.gcc -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Originally published on 28 June 2016.