# SPEC® CFP2006 Result

**Lenovo Group Limited**

**Lenovo ThinkServer RD650**  
(3.20 GHz, Intel Xeon E5-2667 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
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<tr>
<td>SPECfp_rate_base2006</td>
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**CPU2006 license:** 9017  
**Test date:** Jun-2016

**Test sponsor:** Lenovo Group Limited  
**Hardware Availability:** Mar-2016

**Tested by:** Lenovo Group Limited  
**Software Availability:** Dec-2015

## Hardware

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<tr>
<th>Component</th>
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<tbody>
<tr>
<td><strong>CPU Name:</strong></td>
<td>Intel Xeon E5-2667 v4</td>
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<tr>
<td><strong>CPU Characteristics:</strong></td>
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</tr>
<tr>
<td><strong>CPU MHz:</strong></td>
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</tr>
<tr>
<td><strong>FPU:</strong></td>
<td>Integrated</td>
</tr>
<tr>
<td><strong>CPU(s) enabled:</strong></td>
<td>16 cores, 2 chips, 8 cores/chip, 2 threads/core</td>
</tr>
<tr>
<td><strong>CPU(s) orderable:</strong></td>
<td>1,2 chips</td>
</tr>
<tr>
<td><strong>Primary Cache:</strong></td>
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<tr>
<td><strong>Operating System:</strong></td>
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<td>Kernel 3.12.49-11-default</td>
</tr>
<tr>
<td><strong>Compiler:</strong></td>
<td>C/C++: Version 16.0.0.101 of Intel C++ Studio XE</td>
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<td></td>
<td>for Linux;</td>
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</tr>
<tr>
<td><strong>Auto Parallel:</strong></td>
<td>No</td>
</tr>
<tr>
<td><strong>File System:</strong></td>
<td>xfs</td>
</tr>
<tr>
<td><strong>System State:</strong></td>
<td>Run level 3 (multi-user)</td>
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Continued on next page

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/  
Page 1
Lenovo Group Limited
Lenovo ThinkServer RD650
(3.20 GHz, Intel Xeon E5-2667 v4)

SPEC CFP2006 Result

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
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L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

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SPECfp_rate_base2006 = 706

Test date: Jun-2016
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Software Availability: Dec-2015

Results Table

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<thead>
<tr>
<th>Benchmark</th>
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<th>Base</th>
<th>Peak</th>
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<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
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<td>544</td>
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<td>416.gamess</td>
<td>32</td>
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<td>433.milc</td>
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<td>563</td>
<td>522</td>
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<td>434.zeusmp</td>
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<td>435.gromacs</td>
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<td>436.cactusADM</td>
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<tr>
<td>437.leslie3d</td>
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<td>444.namd</td>
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<td>447.dealII</td>
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<td>450.soplex</td>
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<td>454.calculix</td>
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<td>1160</td>
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<td>459.GemsFDTD</td>
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<td>869</td>
<td>391</td>
<td>871</td>
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<td>465.tonto</td>
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<td>470.lbm</td>
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<td>575</td>
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<td>481.wrf</td>
<td>32</td>
<td>506</td>
<td>707</td>
<td>503</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>32</td>
<td>988</td>
<td>631</td>
<td>992</td>
</tr>
</tbody>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
Platform Notes

BIOS Configuration:
- Early Snoop set to Disabled
- Performance Profile set to Custom
- C1E Support set to Disabled
- Core C3 set to Disabled
- Core C6 set to Disabled
- Thermal Profile set to High Fan Speed
- Memory Power Savings set to Disabled

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on rd650-mlk-rackA01 Tue Jun 14 03:56:05 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) CPU E5-2667 v4 @ 3.20GHz
- 2 "physical id"s (chips)
- 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 8
  - siblings : 16
  - physical 0: cores 0 2 3 4 8 10 11 12
  - physical 1: cores 0 2 3 4 8 10 11 12
- cache size : 25600 KB

From /proc/meminfo
- MemTotal: 264392728 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
- SuSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 1
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.
- os-release:
  - NAME="SLES"
  - VERSION="12-SP1"
  - VERSION_ID="12.1"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  - ID="sles"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
Lenovo Group Limited
Lenovo ThinkServer RD650
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Platform Notes (Continued)

Linux rd650-mlk-rackA01 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 13 19:07

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 100G 11G 90G 11% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB2TS335 01/09/2016
Memory:
16x Hynix Semiconductor HMA42GR7AFR4N-UH 16 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "*/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Lenovo Group Limited
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Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.games: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
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Peak Compiler Invocation (Continued)

Fortran benchmarks:
    ifort -m64

Benchmarks using both Fortran and C:
    icc -m64 ifort -m64

Peak Portability Flags

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416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:
    433.milc: basepeak = yes
    470.lbm: basepeak = yes
    482.sphinx3: basepeak = yes

C++ benchmarks:
    444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -fno-alias -auto-ilp32
    447.dealII: basepeak = yes

Continued on next page
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SPEC CFP2006 Result

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Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html  
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml  
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