Lenovo Group Limited
Lenovo ThinkServer RD650
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECfp®2006 = 125
SPECfp_base2006 = 117

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jun-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Hardware
CPU Name: Intel Xeon E5-2698 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip
CPU(s) orderable: 1.2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software
Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
          Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Lenovo Group Limited

SPEC CFP2006 Result

Lenovo ThinkServer RD650
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L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 800 GB SATA SSD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>24.4</td>
<td>557</td>
<td>24.4</td>
<td>557</td>
<td>24.3</td>
<td>558</td>
<td>24.4</td>
<td>557</td>
<td>24.3</td>
<td>558</td>
<td></td>
<td></td>
</tr>
<tr>
<td>416.gamess</td>
<td>530</td>
<td>37.0</td>
<td>532</td>
<td>36.8</td>
<td>531</td>
<td>36.9</td>
<td>422</td>
<td>46.4</td>
<td>422</td>
<td>46.4</td>
<td>421</td>
<td>46.5</td>
</tr>
<tr>
<td>433.milc</td>
<td>126</td>
<td>72.8</td>
<td>129</td>
<td>70.9</td>
<td>128</td>
<td>71.5</td>
<td>126</td>
<td>72.8</td>
<td>129</td>
<td>70.9</td>
<td>128</td>
<td>71.5</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>43.6</td>
<td>209</td>
<td>43.2</td>
<td>211</td>
<td>43.2</td>
<td>211</td>
<td>43.6</td>
<td>209</td>
<td>43.2</td>
<td>211</td>
<td>43.2</td>
<td>211</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>157</td>
<td>45.3</td>
<td>159</td>
<td>44.9</td>
<td>157</td>
<td>45.5</td>
<td>157</td>
<td>45.3</td>
<td>159</td>
<td>44.9</td>
<td>157</td>
<td>45.5</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>10.5</td>
<td>1140</td>
<td>10.6</td>
<td>1130</td>
<td>10.5</td>
<td>1140</td>
<td>10.5</td>
<td>1140</td>
<td>10.6</td>
<td>1130</td>
<td>10.5</td>
<td>1140</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>24.6</td>
<td>381</td>
<td>24.9</td>
<td>377</td>
<td>25.0</td>
<td>376</td>
<td>24.6</td>
<td>381</td>
<td>24.9</td>
<td>377</td>
<td>25.0</td>
<td>376</td>
</tr>
<tr>
<td>444.namd</td>
<td>253</td>
<td>31.6</td>
<td>254</td>
<td>31.6</td>
<td>254</td>
<td>31.6</td>
<td>246</td>
<td>32.6</td>
<td>246</td>
<td>32.6</td>
<td>246</td>
<td>32.6</td>
</tr>
<tr>
<td>447.dealII</td>
<td>171</td>
<td>66.9</td>
<td>172</td>
<td>66.6</td>
<td>172</td>
<td>66.4</td>
<td>171</td>
<td>66.9</td>
<td>172</td>
<td>66.6</td>
<td>172</td>
<td>66.4</td>
</tr>
<tr>
<td>450.soplex</td>
<td>170</td>
<td>49.1</td>
<td>170</td>
<td>49.1</td>
<td>173</td>
<td>48.3</td>
<td>170</td>
<td>49.1</td>
<td>170</td>
<td>49.1</td>
<td>173</td>
<td>48.3</td>
</tr>
<tr>
<td>453.povray</td>
<td>82.9</td>
<td>64.2</td>
<td>83.2</td>
<td>64.0</td>
<td>84.0</td>
<td>63.4</td>
<td>73.9</td>
<td>72.0</td>
<td>74.5</td>
<td>71.4</td>
<td>74.0</td>
<td>71.9</td>
</tr>
<tr>
<td>454.calculix</td>
<td>149</td>
<td>55.2</td>
<td>150</td>
<td>55.0</td>
<td>150</td>
<td>54.9</td>
<td>132</td>
<td>62.7</td>
<td>131</td>
<td>63.0</td>
<td>131</td>
<td>62.9</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>45.9</td>
<td>231</td>
<td>45.3</td>
<td>234</td>
<td>45.3</td>
<td>234</td>
<td>38.2</td>
<td>277</td>
<td>38.6</td>
<td>275</td>
<td>38.5</td>
<td>276</td>
</tr>
<tr>
<td>465.tonto</td>
<td>243</td>
<td>40.5</td>
<td>258</td>
<td>38.1</td>
<td>240</td>
<td>41.0</td>
<td>167</td>
<td>58.9</td>
<td>168</td>
<td>58.7</td>
<td>167</td>
<td>58.8</td>
</tr>
<tr>
<td>470.lbm</td>
<td>15.3</td>
<td>901</td>
<td>15.2</td>
<td>903</td>
<td>15.3</td>
<td>900</td>
<td>15.3</td>
<td>901</td>
<td>15.2</td>
<td>903</td>
<td>15.3</td>
<td>900</td>
</tr>
<tr>
<td>481.wrf</td>
<td>96.5</td>
<td>116</td>
<td>94.5</td>
<td>118</td>
<td>97.4</td>
<td>115</td>
<td>96.5</td>
<td>116</td>
<td>94.5</td>
<td>118</td>
<td>97.4</td>
<td>115</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>287</td>
<td>67.9</td>
<td>288</td>
<td>67.6</td>
<td>290</td>
<td>67.2</td>
<td>287</td>
<td>67.9</td>
<td>288</td>
<td>67.6</td>
<td>290</td>
<td>67.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Hyper-Threading set to Disabled
Cluster On Die set to Disabled
Early Snoop set to Enabled
Performance Profile set to Custom
C1E Support set to Disabled
Core C3 set to Disabled
Core C6 set to Disabled
Thermal Profile set to High Fan Speed
Memory Power Savings set to Disabled

Continued on next page
Lenovo Group Limited

Lenovo ThinkServer RD650
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Platform Notes (Continued)

Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceb81e8219e1
running on RD650-MLK Mon Jun 27 18:41:10 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 20
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
cache size : 51200 KB

From /proc/meminfo
MemTotal:       264554956 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 1
  # This file is deprecated and will be removed in a future service pack or
  release.
  # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 27 13:55

SPEC is set to: /home/cpu2006-1.2-ic16.0
Filesystem     Type   Size  Used  Avail Use% Mounted on
/dev/sda3      xfs    691G  7.9G  684G  2%  /home

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## Lenovo Group Limited

### Lenovo ThinkServer RD650

(2.20 GHz, Intel Xeon E5-2698 v4)

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**CPU2006 license:** 9017

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### Platform Notes (Continued)

Additional information from dmidecode:

> Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS LENOVO PB2TS335 01/09/2016

Memory:
- 8x NO DIMM NO DIMM
- 16x Samsung M393A2G40DB1-CRC16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

- `KMP_AFFINITY = "granularity=fine,compact"`
- `LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"`
- `OMP_NUM_THREADS = "40"`

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages disabled with:
- `echo never > /sys/kernel/mm/transparent_hugepage/enabled`

### Base Compiler Invocation

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

### Base Portability Flags

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`

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Base Portability Flags (Continued)

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
463.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peek Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64
Lenovo Group Limited
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Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
            -auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
             -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
             -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
               -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
           -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
           -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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**Lenovo Group Limited**  
**Lenovo ThinkServer RD650**  
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</tr>
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</tr>
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</tr>
<tr>
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</tbody>
</table>

**Peak Optimization Flags (Continued)**

465.tonto (continued):
- `-opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

- 435.gromacs: `basepeak = yes`
- 436.cactusADM: `basepeak = yes`
- 454.calculix: `-xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilkp32 -ansi-alias`
- 481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html  
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml  
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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