Huawei

Huawei CH225 V3 (Intel Xeon E5-2699 v4)

| SPECfp_rate2006 = | NC |
| SPECfp_rate_base2006 = | NC |

CPU2006 license: 3175  
Test date: Jan-2016  
Test sponsor: Huawei  
Tested by: Huawei  
Hardware Availability: Mar-2016  
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

<table>
<thead>
<tr>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
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<tr>
<td>416.gamess</td>
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<td>433.milc</td>
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<td>434.zeusmp</td>
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<td>435.gromacs</td>
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<td>436.cactusADM</td>
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<td>447.dealII</td>
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<td>450.soplex</td>
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<td>453.povray</td>
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<td>459.GemsFDTD</td>
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<tr>
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<td>481.wrf</td>
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<td>482.sphinx3</td>
</tr>
</tbody>
</table>

Non-Compliant
Huawei
Huawei CH225 V3 (Intel Xeon E5-2699 v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006 =</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006 =</td>
<td>NC</td>
</tr>
</tbody>
</table>

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

CPU Name: Intel Xeon E5-2699 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 44 cores, 2 chips, 22 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 55 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
### SPEC CFP2006 Result

**Huawei**

Huawei CH225 V3 (Intel Xeon E5-2699 v4)

| SPECfp_rate2006 = NC | SPECfp_rate_base2006 = NC |

**CPU2006 license:** 3175  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Test date:** Jan-2016  
**Hardware Availability:** Mar-2016  
**Software Availability:** Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>410</td>
<td>NC</td>
<td>NC</td>
<td>410</td>
<td>NC</td>
<td>410</td>
<td>NC</td>
<td>410</td>
<td>NC</td>
<td>410</td>
<td>NC</td>
</tr>
<tr>
<td>416.gamess</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>416</td>
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<td>NC</td>
<td>416</td>
<td>NC</td>
<td>416</td>
<td>NC</td>
</tr>
<tr>
<td>433.milc</td>
<td>88</td>
<td>NC</td>
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<td>434.zeusmp</td>
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<td>435.gromacs</td>
<td>88</td>
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<td>NC</td>
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<tr>
<td>436.cactusADM</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
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<td>NC</td>
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<td>436</td>
<td>NC</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>88</td>
<td>NC</td>
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<td>444.namd</td>
<td>88</td>
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<td>NC</td>
</tr>
<tr>
<td>447.dealII</td>
<td>88</td>
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<td>447</td>
<td>NC</td>
<td>447</td>
<td>NC</td>
</tr>
<tr>
<td>450.soplex</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>450</td>
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<td>453.povray</td>
<td>88</td>
<td>NC</td>
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<td>NC</td>
<td>453</td>
<td>NC</td>
<td>453</td>
<td>NC</td>
</tr>
<tr>
<td>454.calculix</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
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<td>454</td>
<td>NC</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>459</td>
<td>NC</td>
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<td>459</td>
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<td>NC</td>
<td>459</td>
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<td>459</td>
<td>NC</td>
</tr>
<tr>
<td>465.tonto</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
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<td>NC</td>
<td>465</td>
<td>NC</td>
</tr>
<tr>
<td>470.lbm</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>470</td>
<td>NC</td>
<td>NC</td>
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<td>NC</td>
<td>470</td>
<td>NC</td>
<td>470</td>
<td>NC</td>
</tr>
<tr>
<td>481.wrf</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>481</td>
<td>NC</td>
<td>NC</td>
<td>481</td>
<td>NC</td>
<td>481</td>
<td>NC</td>
<td>481</td>
<td>NC</td>
<td>481</td>
<td>NC</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>88</td>
<td>NC</td>
<td>NC</td>
<td>482</td>
<td>NC</td>
<td>NC</td>
<td>482</td>
<td>NC</td>
<td>482</td>
<td>NC</td>
<td>482</td>
<td>NC</td>
<td>482</td>
<td>NC</td>
</tr>
</tbody>
</table>

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

Non-Compliant
Huawei
Huawei CH225 V3 (Intel Xeon E5-2699 v4)

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Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Performance
Set Snoop Mode to COD mode
Set Patrol Scrub to Disable

Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb66 fbea285f02ceab81e28219e1
running on localhost.localdomain Fri Jun  3 14:23:09 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699 v4 @ 2.20GHz
  2 "physical id"s (chips)
  88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 22
siblings : 44
physical 0: cores 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
cache size : 28160 KB

From /proc/meminfo
MemTotal: 263560548 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

Continued on next page
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### Platform Notes (Continued)

```
uname -a:
    Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
    EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jun 1 14:36
SPEC is set to: /spec16
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      xfs   535G   11G  525G   2% /
```

Additional information from dmidecode:

```
Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.
```

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
- 8x NO DIMM NO DIMM   3 rank
- 8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz
- 8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:
```
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```
Filesystem page cache cleared with:
```
echo 1 > /proc/sys/vm/drop_caches
```
runcspec command invoked through numactl i.e.:
```
numactl --interleave=all runspec <etc>
```

---

Non-Compliant
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---

Base Compiler Invocation

C benchmarks:
icc  -m64

C++ benchmarks:
icpc  -m64

Fortran benchmarks:
ifort  -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort  -m64

---

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64 -nofor_main
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64 -nofor_main
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

---

Base Optimization Flags

C benchmarks:
-xCORE-AVX2  -ipo  -O3  -no-prec-div  -opt-prefetch  -auto-p32  
-ansi-alias  -opt-mem-layout-trans=3
Huawei

Huawei CH225 V3 (Intel Xeon E5-2699 v4)

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**Base Optimization Flags (Continued)**

C++ benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
- -ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
- -ansi-alias -opt-mem-layout-trans=3

**Peak Compiler Invocation**

C benchmarks:
icc -m64

C++ benchmarks (except as noted below):
icpc -m64

450.soplex: icpc -O3 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

**Peak Portability Flags**

410.bwaves: -DSPEC_CPU_LP64
416.fadomip: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

Continued on next page
SPEC CFP2006 Result

Huawei
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SPECfp_rate2006 = NC
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Peak Portability Flags (Continued)

450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:
444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
454.calculix: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
470.lbm: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
481.wrf: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)

Fortran benchmarks:

Non-Compliant
Huawei

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll=12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -prof-use (pass 2) -unroll=14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2 (pass 2) -prof-gen:threadsafe (pass 1)
-ipo (pass 2) -03 (pass 2) -no-prec-div (pass 2)
-par-num-threads=1 (pass 1) -opt-mem-layout-trans=3 (pass 2)
-prof-use (pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.xml
Huawei CH225 V3 (Intel Xeon E5-2699 v4)

SPECfp_rate2006 = NC
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CPU2006 license: 3175
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Hardware Availability: Mar-2016
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by <a href="http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2">SPEC CPU rule 1.3.2</a> and the SPEC Open Systems Group policy on <a href="https://www.spec.org/osg/policy.html#AppendixC">general availability</a>.

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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