SPEC® CFP2006 Result

Huawei

Huawei CH226 V3 (Intel Xeon E5-2690 v4)

<table>
<thead>
<tr>
<th>SPECfp®2006</th>
<th>SPECfp_base2006</th>
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<tbody>
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</table>

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: May-2016
Hardware Availability: Mar-2016
Software Availability: Aug-2015

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on [general availability](https://www.spec.org/osg/policy.html#AppendixC).

Non-Compliant
Huawei CH226 V3 (Intel Xeon E5-2690 v4)

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SPEC CFP2006 Result

SPECfp2006 = NC
SPECfp_base2006 = NC

Huawei

CPU Name: Intel Xeon E5-2690 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip
CPU(s) orderable: 2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 500 GB SATA, 10000 RPM
Other Hardware: None

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.11.1.el7.x86_64
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: /xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

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Huawei

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SPECfp2006 = NC

Results Table

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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Set Power Efficiency Mode to Custom
Set Snoop Mode to HS mode
Set Patrol Scrub to Disable
Set Hyper-Threading to Disable
Sysinfo program /spec16/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Tue May 17 12:08:34 2016
Continued on next page

Non-Compliant

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Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v4 @ 2.60GHz
2 "physical id"s (chips)
28 "processors"
cpus, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 14
siblings : 14
physical 0: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
physical 1: cores 0 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB

From /proc/meminfo
MemTotal: 263568492 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="redhat"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:ga:server"
Redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 17 12:06
SPEC is set to: /spec16

Continued on next page
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Platform Notes (Continued)

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 535G 152G 383G 29% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met where there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
8x NO DIMM NO DIMM 3 rank
8x Samsung M393A2G40EB1 CRC 16 GB 1 rank 2400 MHz
8x Samsung M393A2G40EB1 CRC 16 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"
OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
runcspec command invoked through numactl 1.e.:
numactl --interleave=all runspec <etc>

Base Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Continued on next page
### Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
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<td>410.bwaves</td>
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<tr>
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<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
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</table>

### Base Optimization Flags

- **C benchmarks:**
  ```
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
  -ansi-alias
  ```

- **C++ benchmarks:**
  ```
  -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
  ```

- **Fortran benchmarks:**
  ```
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
  ```

- **Benchmarks using both Fortran and C:**
  ```
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
  -ansi-alias
  ```

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### Peak Compiler Invocation

- **C benchmarks:**
  - `icc -m64`

- **C++ benchmarks:**
  - `icpc -m64`

- **Fortran benchmarks:**
  - `ifort -m64`

- **Benchmarks using both Fortran and C:**
  - `icc -m64 ifort -m64`

---

### Peak Portability Flags

Same as Base Portability Flags

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### Peak Optimization Flags

- **C benchmarks:**
  - 433.milc: `basepeak = yes`
  - 470.lbm: `basepeak = yes`
  - 482.sphinx3: `basepeak = yes`

- **C benchmarks:**
  - 444.namd: `-xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32`
  - 447.dealII: `basepeak = yes`
  - 450.soplex: `basepeak = yes`
  - 453.povray: `-xCORE-AVX2(pass 2) -prof-gen:threadsafepass 1 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4`

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Non-Compliant
SPEC CFP2006 Result

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Peak Optimization Flags (Continued)

453.povray (continued):
-ansi-alias

Fortran benchmarks:
410.bwaves: basepeak = yes
416.games: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-special-alloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html
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You can also download the XML flags sources by saving the following links:
- http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
- http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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