Huawei

Huawei CH226 V3 (Intel Xeon E5-2618L v4)

SPECfp\textsuperscript{®} _rate2006 = NC
SPECfp\textsuperscript{®} _rate_base2006 = NC

CPU2006 license: 3175
Test date: Jul-2016
Test sponsor: Huawei
Hardware Availability: Mar-2016
Tested by: Huawei
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.
### SPEC CFP2006 Result

**Huawei**

Huawei CH226 V3 (Intel Xeon E5-2618L v4)

<table>
<thead>
<tr>
<th>SPECfp_rate2006</th>
<th>NC</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
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</tbody>
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- **CPU2006 license:** 3175
- **Test sponsor:** Huawei
- **Tested by:** Huawei
- **Test date:** Jul-2016
- **Hardware Availability:** Mar-2016
- **Software Availability:** Mar-2016

---

**Non-Compliant**

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

---

**Hardware**

<table>
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<tr>
<th>Feature</th>
<th>Specification</th>
</tr>
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<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon E5-2618L v4</td>
</tr>
<tr>
<td>CPU Characteristics</td>
<td>Intel Turbo Boost Technology up to 3.20 GHz</td>
</tr>
<tr>
<td>CPU MHz</td>
<td>2200</td>
</tr>
<tr>
<td>FPU</td>
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</tr>
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<td>20 cores, 2 chips, 10 cores/chip, 2 threads/core</td>
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<tr>
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<tr>
<td>Primary Cache</td>
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<td>Secondary Cache</td>
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<td>L3 Cache</td>
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<td>Other Hardware</td>
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**Software**

<table>
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<th>Feature</th>
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<tbody>
<tr>
<td>Operating System</td>
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<td>Auto Parallel</td>
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<td>System State</td>
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<tr>
<td>Base Pointers</td>
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<td>Peak Pointers</td>
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SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability.

Results Table

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<th>Benchmark</th>
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<th>Second</th>
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<td>NC</td>
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<td>NC</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
## SPEC CFP2006 Result

**Huawei**

Huawei CH226 V3 (Intel Xeon E5-2618L v4)

<table>
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</tr>
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<tbody>
<tr>
<td>SPECfp_rate_base2006</td>
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**CPU2006 license:** 3175  
**Test date:** Jul-2016  
**Test sponsor:** Huawei  
**Tested by:** Huawei  
**Hardware Availability:** Mar-2016  
**Software Availability:** Mar-2016

**SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by [SPEC CPU rule 1.3.2](http://spec.org/cpu2006/Docs/runrules.html#rule_1.3.2) and the SPEC Open Systems Group policy on general availability.**

### Platform Notes

**BIOS configuration:**
Set Power Efficiency Mode to Performance  
Set Snoop Mode to ES mode  
Set Patrol Scrub to Disable

Sysinfo program /spec/cpu/spec16/config/sysinfo.rev6914  
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb6b5a285b02ceab81e28219el  

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: http://www.spec.org/cpu2006/Docs/config.html#sysinfo

**From /proc/cpuinfo**

- **model name:** Intel(R) Xeon(R) CPU E5-2618L v4 @ 2.20GHz  
- **2 "physical id"s (chips)  
- **40 "processors"**

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

- **cpu cores : 10**
- **siblings : 20**
- **physical 0: cores 1 2 3 4 8 9 10 11 12**
- **physical 1: cores 1 2 3 4 8 9 10 11 12**
- **cache size : 25600 KB**

**From /proc/meminfo**

- **MemTotal:** 63567004 kB  
- **Bufs**: 0  
- **Huge pages total:** 2048 kB

**From /etc/*release*/etc/*version**

- **NAME="Red Hat Enterprise Linux Server"**
- **VERSION="7.2 (Maipo)"**
- **ID="rhel"**
- **ID_LIKE="fedora"**
- **VERSION_ID="7.2"**
- **PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"**
- **ANSI_COLOR=0;31**
- **CPE_NAME=cpe:/o:redhat:enterprise_linux:7.2:GA:server**
- **redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)**
- **system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)**

---

Non-compliant
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Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/speccpu/spec16/libs/32:/speccpu/spec16/libs/64:/speccpu/spec16/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
umactl --interleave=all runspec <etc>

Platform Notes (Continued)

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jun 29 07:30
SPEC is set to: /speccpu/spec16
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda2      ext4  591G   80G  481G  15% /
Additional information from dmidecode:
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Insyde Corp. 3.09 02/22/2016
Memory:
  8x NO DIMM NO DIMM  3 rank
  8x Samsung M393A2G40EB1-CRC 16 GB 1 rank 2400 MHz, configured at 2133 MHz
  8x Samsung M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz, configured at 2133 MHz
(End of data from sysinfo program)
Huawei

Huawei CH226 V3 (Intel Xeon E5-2618L v4)

| SPECfp_rate2006 = | NC |
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---

**Base Compiler Invocation**

- **C benchmarks:**
  - `icc  -m64`
- **C++ benchmarks:**
  - `icpc  -m64`
- **Fortran benchmarks:**
  - `ifort  -m64`
- **Benchmarks using both Fortran and C:**
  - `icc  -m64 ifort  -m64`

---

**Base Portability Flags**

- 410.bwaves: `-DSPEC_CPU_LP64`
- 416.gamess: `-DSPEC_CPU_LP64`
- 433.milc: `-DSPEC_CPU_LP64`
- 434.zeusmp: `-DSPEC_CPU_LP64`
- 435.gromacs: `-DSPEC_CPU_LP64` `-nofor_main`
- 436.cactusADM: `-DSPEC_CPU_LP64` `-nofor_main`
- 437.leslie3d: `-DSPEC_CPU_LP64`
- 444.namd: `-DSPEC_CPU_LP64` `-nofor_main`
- 447.dealII: `-DSPEC_CPU_LP64`
- 450.soplex: `-DSPEC_CPU_LP64`
- 453.povray: `-DSPEC_CPU_LP64`
- 454.calculix: `-DSPEC_CPU_LP64` `-nofor_main`
- 459.GemsFDTD: `-DSPEC_CPU_LP64` `-nofor_main`
- 465.tonto: `-DSPEC_CPU_LP64`
- 470.lbm: `-DSPEC_CPU_LP64`
- 481.wrf: `-DSPEC_CPU_LP64` `-DSPEC_CPU_CASE_FLAG` `-DSPEC_CPU_LINUX`
- 482.sphinx3: `-DSPEC_CPU_LP64`

---

**Base Optimization Flags**

- **C benchmarks:**
  - `-xCORE-AVX2` `-ipo` `-O3` `-no-prec-div` `-opt-prefetch` `-auto-p32`
  - `-ansi-alias` `-opt-mem-layout-trans=3`

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Continued on next page
Huawei

Huawei CH226 V3 (Intel Xeon E5-2618L v4)

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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks (except as noted below):
icpc  -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.cambridge: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64

Continued on next page
### SPEC CFP2006 Result

<table>
<thead>
<tr>
<th>Huawei CH226 V3 (Intel Xeon E5-2618L v4)</th>
<th>SPECfp_rate2006 = NC</th>
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</thead>
<tbody>
<tr>
<td>SPECfp_rate_base2006 = NC</td>
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### Peak Portability Flags (Continued)

<table>
<thead>
<tr>
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<th>Flags</th>
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<td>-D_FILE_OFFSET_BITS=64</td>
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<td>453.povray</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>454.calculix</td>
<td>-DSPEC_CPU_LP64 -nofor_main</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>465.tonto</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>470.lbm</td>
<td>-DSPEC_CPU_LP64</td>
</tr>
<tr>
<td>481.wrf</td>
<td>-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>-DSPEC_CPU_LP64</td>
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### Peak Optimization Flags

**C benchmarks:**

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<th>Benchmark</th>
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<td>basepeak = yes</td>
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<tr>
<td>470.lbm</td>
<td>basepeak = yes</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>basepeak = yes</td>
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**C++ benchmarks:**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
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<tbody>
<tr>
<td>444.namd</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:thousandsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32</td>
</tr>
<tr>
<td>447.dealII</td>
<td>basepeak = yes</td>
</tr>
<tr>
<td>450.soplex</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:thousandsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -opt-malloc-options=3</td>
</tr>
<tr>
<td>453.povray</td>
<td>-xCORE-AVX2(pass 2) -prof-gen:thousandsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias</td>
</tr>
</tbody>
</table>

**Fortran benchmarks:**

Continued on next page
Huawei CH226 V3 (Intel Xeon E5-2618L v4)

CPU2006 license: 3175
Test sponsor: Huawei
Tested by: Huawei

Test date: Jul-2016
Hardware Availability: Mar-2016
Software Availability: Mar-2016

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the memory was not available as required by SPEC CPU rule 1.3.2 and the SPEC Open Systems Group policy on general availability. The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.html

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You can also download the XML flags sources by saving the following links:

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http://www.spec.org/cpu2006/Flags/Huawei-Platform-Settings-BDW-V1.0.xml

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipc(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes
## SPEC CFP2006 Result

**Huawei**

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For other inquiries, please contact webmaster@spec.org.

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