**Hewlett Packard Enterprise**  
(Test Sponsor: HPE)  
ProLiant DL360 Gen9  
(1.80 GHz, Intel Xeon E5-2630L v4)  

**CPU2006 license:** 3  
**Test sponsor:** HPE  
**Tested by:** HPE  

**HPE**  
(1.80 GHz, Intel Xeon E5-2630L v4)  
ProLiant DL360 Gen9  

**SPECfp®2006 =** 103  
**SPECfp_base2006 =** 96.8

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**Hardware**

- **CPU Name:** Intel Xeon E5-2630L v4  
- **CPU Characteristics:** Intel Turbo Boost Technology up to 2.90 GHz  
- **CPU MHz:** 1800  
- **FPU:** Integrated  
- **CPU(s) enabled:** 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
- **CPU(s) orderable:** 1.2 chip  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 256 KB I+D on chip per core

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**Software**

- **Operating System:** Red Hat Enterprise Linux Server release 7.2 (Maipo)  
- **Kernel:** 3.10.0-327.el7.x86_64  
- **Compiler:** C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
  Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
- **Auto Parallel:** Yes  
- **File System:** xfs

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**Test Sponsor:** HPE  
**Hardware Availability:** Mar-2016  
**Software Availability:** Nov-2015  
**Test date:** Jun-2016

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**SPECfp2006 = 103**  
**SPECfp_base2006 = 96.8**

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**410.bwaves**  
**416.gamess**  
**433.milc**  
**434.zeusmp**  
**435.gromacs**  
**436.cactusADM**  
**437.leslie3d**  
**444.namd**  
**447.dealII**  
**450.soplex**  
**453.povray**  
**454.calculix**  
**459.GemsFDTD**  
**465.tonto**  
**470.lbm**  
**481.wrf**  
**482.sphinx3**

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### SPEC CFP2006 Result

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ProLiant DL360 Gen9  
(1.80 GHz, Intel Xeon E5-2630L v4)

<table>
<thead>
<tr>
<th>SPECfp2006</th>
<th>SPECfp_base2006</th>
</tr>
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<tbody>
<tr>
<td>= 103</td>
<td>= 96.8</td>
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</table>

**CPU2006 license:** 3  
**Test date:** Jun-2016  
**Test sponsor:** HPE  
**Hardware Availability:** Mar-2016

**Tested by:** HPE  
**Software Availability:** Nov-2015

| L3 Cache: | 25 MB I+D on chip per chip  
| Other Cache: | None  
| Memory: | 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
| Disk Subsystem: | 2 x 400 GB SAS SSD, RAID 1  
| Other Hardware: | None  
| System State: | Run level 3 (multi-user)  
| Base Pointers: | 64-bit  
| Peak Pointers: | 32/64-bit  
| Other Software: | None  

**Tested by:** HPE

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
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<td>435.gromacs</td>
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<td>450.soplex</td>
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<td>453.povray</td>
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<td>481.wrf</td>
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<td>103</td>
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<td>482.sphinx3</td>
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<td>342</td>
<td>56.9</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Operating System Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Stack size set to unlimited using &quot;ulimit -s unlimited&quot;</td>
</tr>
<tr>
<td>Transparent Huge Pages enabled with: echo always &gt; /sys/kernel/mm/transparent_hugepage/enabled</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Platform Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>BIOS Configuration:</td>
</tr>
<tr>
<td>HP Power Profile set to Custom</td>
</tr>
<tr>
<td>HP Power Regulator to HP Static High Performance Mode</td>
</tr>
<tr>
<td>Minimum Processor Idle Power Core C-State set to C1E State</td>
</tr>
<tr>
<td>Minimum Processor Idle Power Package C-State set to No Package State</td>
</tr>
<tr>
<td>QPI Snoop Configuration set to Home Snoop</td>
</tr>
<tr>
<td>Collaborative Power Control set to Disabled</td>
</tr>
</tbody>
</table>

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CPU2006 license: 3
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Platform Notes (Continued)

Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Intel Hyperthreading set to Enabled
Sysinfo program /cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Mon Jun 13 20:25:34 2016

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630L v4 @ 1.80GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB

From /proc/meminfo
MemTotal:       528065720 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release*/etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 13 14:43

SPEC is set to: /cpu2006

Filesystem     Type  Size  Used Avail Use% Mounted on

Continued on next page
**Platform Notes (Continued)**

/dev/sda4       xfs      368G  125G  243G  34% /
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 02/22/2016
Memory:
  8x UNKNOWN NOT AVAILABLE
  16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:

16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

**General Notes**

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "20"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

**Base Compiler Invocation**

C benchmarks:
  icc   -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  icc   -m64 ifort -m64

**Base Portability Flags**

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
SPEC CFP2006 Result

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Hardware Availability: Mar-2016
Tested by: HPE
Software Availability: Nov-2015

Base Portability Flags (Continued)

- 433.milc: -DSPEC_CPU_LP64
- 434.teusmp: -DSPEC_CPU_LP64
- 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
- 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
- 437.leslie3d: -DSPEC_CPU_LP64
- 444.namd: -DSPEC_CPU_LP64 -nofor_main
- 447.dealII: -DSPEC_CPU_LP64
- 450.soplex: -DSPEC_CPU_LP64
- 453.povray: -DSPEC_CPU_LP64 -nofor_main
- 454.calculix: -DSPEC_CPU_LP64 -nofor_main
- 459.GemsFDTD: -DSPEC_CPU_LP64
- 465.tonto: -DSPEC_CPU_LP64
- 470.lbm: -DSPEC_CPU_LP64
- 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
- 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
- -ansi-alias

C++ benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
- -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
- -ansi-alias

Peak Compiler Invocation

C benchmarks:
- icc -m64

C++ benchmarks:
- icpc -m64

Fortran benchmarks:
- ifort -m64

Benchmarks using both Fortran and C:
- icc -m64 ifort -m64
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SPECfp_base2006 = 96.8

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Peak Optimization Flags (Continued)

465.tonto (continued):
-optimization-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml

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