SPEC® CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint®2006 = 73.7
SPECint_base2006 = 71.5

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Jul-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

400.perlbench
401.bzip2
403.gcc
429.mcf
445.gobmk
456.hmmer
458.sjeng
462.libquantum
464.h264ref
471.omnetpp
473.astar
483.xalancbmk

SPECint2006 = 73.7
SPECint_base2006 = 71.5

Hardware

CPU Name: Intel Xeon E5-2698 v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 40 cores, 2 chips, 20 cores/chip
CPU(s) orderable: 1.2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 50 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP1
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V10.2
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint2006 = 73.7
SPECint_base2006 = 71.5

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Base</td>
<td>Peak</td>
<td>Base</td>
<td>Peak</td>
<td>Base</td>
<td>Peak</td>
<td>Base</td>
<td>Peak</td>
</tr>
<tr>
<td>400.perlbench</td>
<td>234.2</td>
<td>41.7</td>
<td>234.2</td>
<td>41.7</td>
<td>234.2</td>
<td>41.7</td>
<td>214.2</td>
<td>45.8</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>390.0</td>
<td>24.7</td>
<td>389.0</td>
<td>24.8</td>
<td>388.0</td>
<td>24.9</td>
<td>382.0</td>
<td>25.3</td>
</tr>
<tr>
<td>403.mcf</td>
<td>211.0</td>
<td>38.2</td>
<td>210.0</td>
<td>38.4</td>
<td>211.0</td>
<td>38.2</td>
<td>211.0</td>
<td>38.2</td>
</tr>
<tr>
<td>429.gcc</td>
<td>144.0</td>
<td>63.3</td>
<td>143.0</td>
<td>64.0</td>
<td>146.0</td>
<td>62.5</td>
<td>144.0</td>
<td>63.3</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>340.0</td>
<td>30.8</td>
<td>340.0</td>
<td>30.9</td>
<td>340.0</td>
<td>30.8</td>
<td>340.0</td>
<td>30.8</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>106.0</td>
<td>88.1</td>
<td>106.0</td>
<td>88.1</td>
<td>106.0</td>
<td>88.2</td>
<td>106.0</td>
<td>88.1</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>338.0</td>
<td>35.8</td>
<td>338.0</td>
<td>35.8</td>
<td>338.0</td>
<td>35.8</td>
<td>338.0</td>
<td>35.8</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>2.31</td>
<td>8990</td>
<td>2.30</td>
<td>9020</td>
<td>2.29</td>
<td>9050</td>
<td>2.31</td>
<td>9020</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>84.0</td>
<td>57.7</td>
<td>83.0</td>
<td>57.8</td>
<td>83.0</td>
<td>57.8</td>
<td>83.0</td>
<td>57.8</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>128.0</td>
<td>48.4</td>
<td>129.0</td>
<td>48.4</td>
<td>131.0</td>
<td>47.6</td>
<td>112.0</td>
<td>55.9</td>
</tr>
<tr>
<td>473.astar</td>
<td>191.0</td>
<td>36.8</td>
<td>191.0</td>
<td>36.7</td>
<td>191.0</td>
<td>36.8</td>
<td>190.0</td>
<td>36.9</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>89.0</td>
<td>77.5</td>
<td>87.4</td>
<td>79.0</td>
<td>89.0</td>
<td>78.4</td>
<td>79.2</td>
<td>87.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Platform Notes

BIOS Configuration:
HP Power Profile set to Balanced Power and Performance
QPI Snoop Configuration set to Home Snoop
Collaborative Power Control set to Disabled
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Intel Hyperthreading set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on d1380_manju Fri Jul 8 09:48:33 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo
From /proc/cpuinfo
Platform Notes (Continued)

model name : Intel(R) Xeon(R) CPU E5-2698 v4 @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 20
  siblings : 20
  physical 0: cores 0 2 3 4 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1: cores 0 2 3 4 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  cache size : 51200 KB

From /proc/meminfo
  MemTotal:       529311612 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release*/etc/*version*
  SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or release.
    # Please check /etc/os-release for details about this release.
  os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:
  Linux dl380_manju 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 8 09:47

SPEC is set to: /home/cpu2006

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 331G 25G 306G 8% /home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P89 04/12/2016
Memory:
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint2006 = 73.7
SPECint_base2006 = 71.5

Platform Notes (Continued)

8x UNKNOWN NOT AVAILABLE
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "40"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Base Compiler Invocation

C benchmarks:
  icc -m64

C++ benchmarks:
  icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Continued on next page
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint2006 = 73.7
SPECint_base2006 = 71.5

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Jul-2016
Hardware Availability: Mar-2016
Software Availability: Dec-2015

Base Optimization Flags (Continued)
C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -Ismartheap64

Base Other Flags
C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation
C benchmarks (except as noted below):
icc  -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc -m64

Peak Portability Flags
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSETBITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSETBITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags
C benchmarks:

Continued on next page
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint2006 = 73.7
SPECint_base2006 = 71.5

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Peak Optimization Flags (Continued)

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes
445.gobmk: basepeak = yes
456.hmmer: basepeak = yes
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca
SPEC CINT2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen9
(2.20 GHz, Intel Xeon E5-2698 v4)

SPECint2006 = 73.7
SPECint_base2006 = 71.5

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Aug 9 17:03:24 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 August 2016.