



# SPEC® CFP2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen9

(2.20 GHz, Intel Xeon E5-4669 v4)

SPECfp®\_rate2006 = 2180

SPECfp\_rate\_base2006 = 2120

CPU2006 license: 3

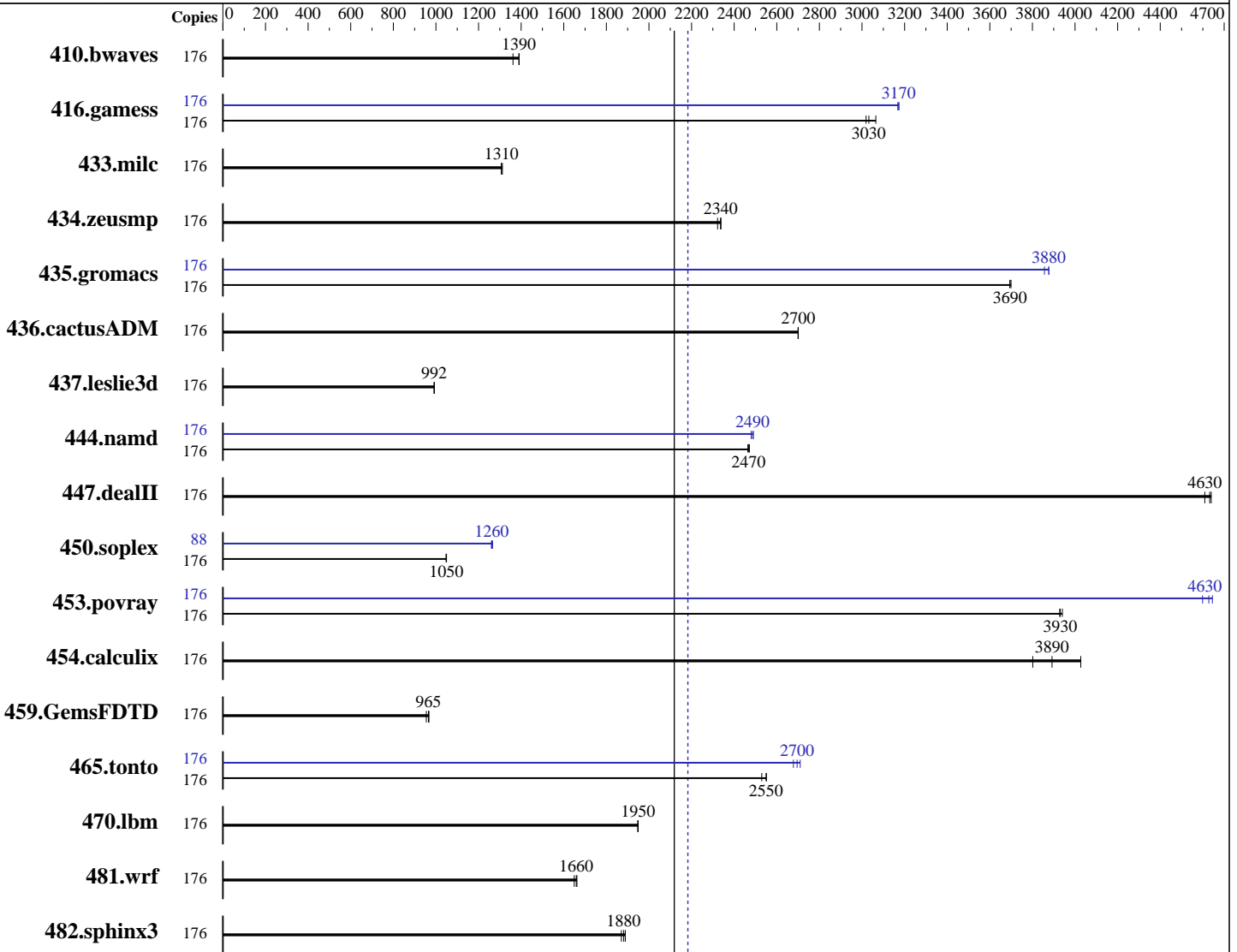
Test sponsor: HPE

Tested by: HPE

Test date: Jul-2016

Hardware Availability: Jul-2016

Software Availability: Dec-2015



SPECfp\_rate\_base2006 = 2120

SPECfp\_rate2006 = 2180

### Hardware

CPU Name: Intel Xeon E5-4669 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1, Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 55 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	176	1756	1360	<u>1722</u>	<u>1390</u>	1720	1390	176	1756	1360	<u>1722</u>	<u>1390</u>	1720	1390
416.gamess	176	1124	3060	1141	3020	<u>1136</u>	<u>3030</u>	176	1088	3170	<u>1086</u>	<u>3170</u>	1086	3170
433.milc	176	<u>1236</u>	<u>1310</u>	1236	1310	1231	1310	176	<u>1236</u>	<u>1310</u>	1236	1310	1231	1310
434.zeusmp	176	<u>686</u>	<u>2340</u>	690	2320	685	2340	176	<u>686</u>	<u>2340</u>	690	2320	685	2340
435.gromacs	176	340	3700	<u>340</u>	<u>3690</u>	340	3690	176	326	3860	324	3880	<u>324</u>	<u>3880</u>
436.cactusADM	176	779	2700	<u>779</u>	<u>2700</u>	779	2700	176	779	2700	<u>779</u>	<u>2700</u>	779	2700
437.leslie3d	176	<u>1668</u>	<u>992</u>	1667	992	1668	992	176	<u>1668</u>	<u>992</u>	1667	992	1668	992
444.namd	176	571	2470	573	2460	<u>572</u>	<u>2470</u>	176	<u>568</u>	<u>2490</u>	567	2490	569	2480
447.dealII	176	437	4610	<u>435</u>	<u>4630</u>	434	4640	176	437	4610	<u>435</u>	<u>4630</u>	434	4640
450.soplex	176	1401	1050	<u>1399</u>	<u>1050</u>	1398	1050	88	<u>581</u>	<u>1260</u>	582	1260	580	1270
453.povray	176	238	3940	<u>238</u>	<u>3930</u>	238	3930	176	202	4640	204	4600	<u>202</u>	<u>4630</u>
454.calculix	176	361	4030	382	3800	<u>373</u>	<u>3890</u>	176	361	4030	382	3800	<u>373</u>	<u>3890</u>
459.GemsFDTD	176	1956	955	1931	967	<u>1935</u>	<u>965</u>	176	1956	955	1931	967	<u>1935</u>	<u>965</u>
465.tonto	176	679	2550	<u>679</u>	<u>2550</u>	685	2530	176	<u>643</u>	<u>2700</u>	647	2680	639	2710
470.lbm	176	1242	1950	<u>1241</u>	<u>1950</u>	1241	1950	176	1242	1950	<u>1241</u>	<u>1950</u>	1241	1950
481.wrf	176	<u>1185</u>	<u>1660</u>	1192	1650	1183	1660	176	<u>1185</u>	<u>1660</u>	1192	1650	1183	1660
482.sphinx3	176	<u>1823</u>	<u>1880</u>	1816	1890	1834	1870	176	<u>1823</u>	<u>1880</u>	1816	1890	1834	1870

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>



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**Tested by:** HPE

**Test date:** Jul-2016

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## Platform Notes

### BIOS Configuration:

HP Power Profile set to Balanced Power and Performance  
QPI Snoop Configuration set to Cluster on Die  
Collaborative Power Control set to Disabled  
Thermal Configuration set to Maximum Cooling  
Memory Refresh Rate set to 1x Refresh  
Processor Power and Utilization Monitoring set to Disabled

Sysinfo program /home/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on sles12biswadm1560 Sun Jul 17 18:56:27 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

### From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) CPU E5-4669 v4 @ 2.20GHz
 4 "physical id"s (chips)
 176 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores      : 22
siblings       : 44
physical 0:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
physical 1:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
physical 2:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
physical 3:    cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27
                28
cache size     : 28160 KB
```

### From /proc/meminfo

```
MemTotal:      529296052 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

### From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
```

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## Platform Notes (Continued)

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

```
Linux sles12biswadl560 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jul 17 18:54

SPEC is set to: /home/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4        xfs   331G   97G  234G  30% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP P85 07/01/2016

Memory:

16x UNKNOWN NOT AVAILABLE

32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 512 GB and the dmidecode description should have one line reading as: 32x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Continued on next page

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Page 4



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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page



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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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