Lenovo Group Limited

Lenovo System x3950 X6
(Intel Xeon E7-8893 v4, 3.20 GHz)

SPECint®2006 = 72.5
SPECint_base2006 = 70.7

CPU2006 license: 9017
Test sponsor: Lenovo Group Limited
Tested by: Lenovo Group Limited

Test date: Jul-2016
Hardware Availability: Jun-2016
Software Availability: Dec-2015

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECint2006</th>
<th>SPECint_base2006</th>
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<tbody>
<tr>
<td>perlbench</td>
<td>40.9</td>
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<tr>
<td>bzip2</td>
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<td>24.0</td>
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<tr>
<td>gcc</td>
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<td>mcf</td>
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<td>58.7</td>
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<tr>
<td>gobmk</td>
<td>30.1</td>
<td>30.0</td>
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<tr>
<td>hmmer</td>
<td>86.1</td>
<td>86.1</td>
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<td>sjeng</td>
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<td>libquantum</td>
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<tr>
<td>h264ref</td>
<td>68.0</td>
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<tr>
<td>omnetpp</td>
<td>86.5</td>
<td>86.5</td>
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<tr>
<td>astar</td>
<td>36.6</td>
<td>36.5</td>
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<tr>
<td>xalancbmk</td>
<td>77.8</td>
<td>77.7</td>
</tr>
</tbody>
</table>

SPECint2006 = 72.5

Hardware

- CPU Name: Intel Xeon E7-8893 v4
- CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
- CPU MHZ: 3200
- FPU: Integrated
- CPU(s) enabled: 32 cores, 8 chips, 4 cores/chip
- CPU orderable: 4.8 chips
- Primary Cache: 32 KB I + 32 KB D on chip per core
- Secondary Cache: 256 KB I+D on chip per core
- L3 Cache: 60 MB I+D on chip per chip
- Other Cache: None
- Memory: 1 TB (64 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
- Disk Subsystem: 1 x 800 GB SATA SSD
- Other Hardware: None

Software

- Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64)
  Kernel 3.12.49-11-default
- Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
- Auto Parallel: Yes
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 32/64-bit
- Peak Pointers: 32/64-bit
- Other Software: Microquill SmartHeap V10.2
Lenovo Group Limited

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<td>41.0</td>
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<td>59.2</td>
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<td>59.2</td>
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<tr>
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<tr>
<td>483.xalancbmk</td>
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<td>81.0</td>
<td>85.2</td>
<td>81.1</td>
<td>85.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:
Operating Mode set to "Maximum Performance"
Hyper-Threading set to Disable
Sysinfo program /home/cpu2006-1.2-ic16.0/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on X3950-01-SLES12SP1 Mon Jul 25 18:36:50 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E7-8893 v4 @ 3.20GHz
8 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
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Platform Notes (Continued)

siblings : 4
physical 0: cores 12 13 25 26
physical 1: cores 12 13 25 26
physical 2: cores 12 13 25 26
physical 3: cores 12 13 25 26
physical 4: cores 12 13 25 26
physical 5: cores 12 13 25 26
physical 6: cores 12 13 25 26
physical 7: cores 12 13 25 26
cache size : 61440 KB

From /proc/meminfo
MemTotal: 1058567692 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.

uname -a:
Linux X3950-01-SLES12SP1 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC
2015 (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 25 18:35

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS IBM -[A9E135CUS-3.10]- 06/16/2016
Memory:
128x NO DIMM Unknown

Continued on next page
Lenovo Group Limited
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Platform Notes (Continued)
64x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz, configured at 1600 MHz

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic16.0/libs/32:/home/cpu2006-1.2-ic16.0/libs/64:/home/cpu2006-1.2-ic16.0/sh"
OMP_NUM_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation
C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags
C benchmarks:
xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

Continued on next page
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Base Optimization Flags (Continued)

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-W1,-z,muldefs -L/sh -lsmartheap64

C benchmarks:
403.gcc: -Dalloca=_alloca

Base Other Flags

Peak Compiler Invocation

C benchmarks (except as noted below):
icc  -m64
400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin
473.astar: icpc  -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
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**Peak Optimization Flags**

**C benchmarks:**

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div -inline-cALLOC
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-opt-prefetch -auto-p32`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias`

456.hmmer: `basepeak = yes`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

**C++ benchmarks:**

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap`

473.astar: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64`

483.xalancbmk: `-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap`
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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-BDW-B.xml

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For other inquiries, please contact webmaster@spec.org.

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