Cisco Systems
Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

SPECfp®2006 = 122
SPECfp_base2006 = 117

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems

Hardware
CPU Name: Intel Xeon E5-2697A v4
CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software
Operating System: SUSE Linux Enterprise Server 12 SP1 (x86_64) 3.12.49-11-default
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;
Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
## SPEC CFP2006 Result

### Cisco Systems

Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

**SPECfp2006 = 122**  
**SPECfp_base2006 = 117**

<table>
<thead>
<tr>
<th>CPU2006 license:</th>
<th>9019</th>
<th>Test date:</th>
<th>Jul-2016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test sponsor:</td>
<td>Cisco Systems</td>
<td>Hardware Availability:</td>
<td>Apr-2016</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Cisco Systems</td>
<td>Software Availability:</td>
<td>Dec-2015</td>
</tr>
</tbody>
</table>

L3 Cache: 40 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 300 GB SAS, 15k RPM  
Other Hardware: None  

**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** None  

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>19.8</td>
<td>686</td>
<td>20.9</td>
<td>649</td>
<td>19.7</td>
<td>689</td>
<td>19.8</td>
<td>686</td>
<td>20.9</td>
<td>649</td>
<td>19.7</td>
</tr>
<tr>
<td>416.gamess</td>
<td>531</td>
<td>36.8</td>
<td>531</td>
<td>36.8</td>
<td>532</td>
<td>36.8</td>
<td>473</td>
<td>41.4</td>
<td>474</td>
<td>41.3</td>
<td>473</td>
</tr>
<tr>
<td>433.milc</td>
<td>126</td>
<td>72.7</td>
<td>125</td>
<td>72.8</td>
<td>128</td>
<td>71.7</td>
<td>126</td>
<td>72.7</td>
<td>125</td>
<td>72.8</td>
<td>128</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>43.8</td>
<td>208</td>
<td>43.5</td>
<td>209</td>
<td>43.3</td>
<td>210</td>
<td>43.8</td>
<td>208</td>
<td>43.5</td>
<td>209</td>
<td>43.3</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>141</td>
<td>50.5</td>
<td>141</td>
<td>50.6</td>
<td>138</td>
<td>51.6</td>
<td>141</td>
<td>50.5</td>
<td>138</td>
<td>51.6</td>
<td></td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>12.8</td>
<td>930</td>
<td>13.3</td>
<td>902</td>
<td>13.1</td>
<td>911</td>
<td>12.8</td>
<td>930</td>
<td>13.3</td>
<td>902</td>
<td>13.1</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>26.8</td>
<td>350</td>
<td>27.6</td>
<td>340</td>
<td>30.1</td>
<td>312</td>
<td>26.8</td>
<td>350</td>
<td>27.6</td>
<td>340</td>
<td>30.1</td>
</tr>
<tr>
<td>444.namd</td>
<td>254</td>
<td>31.6</td>
<td>253</td>
<td>31.6</td>
<td>253</td>
<td>31.7</td>
<td>246</td>
<td>32.6</td>
<td>246</td>
<td>32.6</td>
<td>246</td>
</tr>
<tr>
<td>447.dealII</td>
<td>173</td>
<td>66.3</td>
<td>173</td>
<td>66.2</td>
<td>175</td>
<td>65.5</td>
<td>173</td>
<td>66.3</td>
<td>173</td>
<td>66.2</td>
<td>175</td>
</tr>
<tr>
<td>450.soplex</td>
<td>169</td>
<td>49.4</td>
<td>169</td>
<td>49.4</td>
<td>168</td>
<td>49.6</td>
<td>169</td>
<td>49.4</td>
<td>169</td>
<td>49.4</td>
<td>168</td>
</tr>
<tr>
<td>453.povray</td>
<td>95.9</td>
<td>55.5</td>
<td>96.1</td>
<td>55.4</td>
<td>95.9</td>
<td>55.4</td>
<td>84.5</td>
<td>63.0</td>
<td>85.0</td>
<td>62.6</td>
<td>83.7</td>
</tr>
<tr>
<td>454.calculix</td>
<td>156</td>
<td>52.8</td>
<td>156</td>
<td>52.8</td>
<td>156</td>
<td>52.9</td>
<td>145</td>
<td>56.7</td>
<td>144</td>
<td>57.5</td>
<td>144</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>47.7</td>
<td>222</td>
<td>45.6</td>
<td>233</td>
<td>46.0</td>
<td>231</td>
<td>38.4</td>
<td>276</td>
<td>38.7</td>
<td>274</td>
<td>38.2</td>
</tr>
<tr>
<td>465.tonto</td>
<td>226</td>
<td>43.6</td>
<td>234</td>
<td>42.1</td>
<td>228</td>
<td>43.2</td>
<td>189</td>
<td>52.1</td>
<td>189</td>
<td>52.2</td>
<td>188</td>
</tr>
<tr>
<td>470.lbm</td>
<td>15.8</td>
<td>872</td>
<td>15.9</td>
<td>866</td>
<td>15.7</td>
<td>877</td>
<td>15.8</td>
<td>872</td>
<td>15.9</td>
<td>866</td>
<td>15.7</td>
</tr>
<tr>
<td>481.wrf</td>
<td>92.1</td>
<td>121</td>
<td>92.1</td>
<td>121</td>
<td>96.8</td>
<td>115</td>
<td>92.1</td>
<td>121</td>
<td>92.1</td>
<td>121</td>
<td>96.8</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>255</td>
<td>76.4</td>
<td>256</td>
<td>76.2</td>
<td>256</td>
<td>76.2</td>
<td>255</td>
<td>76.4</td>
<td>256</td>
<td>76.2</td>
<td>256</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

**BIOS Settings:**  
Intel Hyper-Threaded Technology option set to Disabled  
CPU performance set to Enterprise  
Power Technology set to Energy Efficient  
Energy Performance BIAS setting set to Balanced Performance  
Memory RAS configuration set to Maximum Performance  
Memory Power Saving Mode set to Disabled  
QPI Snoop Mode set to Home Directory Snoop with OSB  
Sysinfo program /opt/cpu2006-1.2/config/sysinfo.rev6914  
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1  
Continued on next page
Cisco Systems
Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

| SPECfp2006 = | 122 |
| SPECfp_base2006 = | 117 |

**CPU2006 license:** 9019

**Test sponsor:** Cisco Systems

**Tested by:** Cisco Systems

**Test date:** Jul-2016

**Hardware Availability:** Apr-2016

**Software Availability:** Dec-2015

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**Platform Notes (Continued)**

running on linux-6mvl Tue Jul 19 10:07:19 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name: Intel(R) Xeon(R) CPU E5-2697A v4 @ 2.60GHz
- 2 "physical id"s (chips)
- 32 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 16
  - siblings: 16
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  - physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
- cache size: 40960 KB

From /proc/meminfo

- MemTotal: 264567868 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

- SUSE-release:
  - SUSE Linux Enterprise Server 12 (x86_64)
  - VERSION = 12
  - PATCHLEVEL = 1
  - # This file is deprecated and will be removed in a future service pack or release.
  - # Please check /etc/os-release for details about this release.

- os-release:
  - NAME="SLES"
  - VERSION="12-SP1"
  - VERSION_ID="12.1"
  - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
  - ID="sles"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

  (8d714a0) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 14 17:56

SPEC is set to: /opt/cpu2006-1.2

Filesystem| Type   | Size   | Used   | Avail  | Use%   | Mounted on  |
---|---|---|---|---|---|---|
/dev/sda1 | xfs   | 279G   | 16G    | 263G   | 6%     | /   |

Additional information from dmidecode:

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Cisco Systems
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CPU2006 license: 9019
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Platform Notes (Continued)
Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Cisco Systems, Inc. C220M4.2.0.10c.0.032320160810 03/23/2016
Memory:
16x 0xCE00 M393A2G40EB1-CRC 16 GB 2 rank 2400 MHz
8x NO DIMM NO DIMM

(End of data from sysinfo program)

General Notes
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/opt/cpu2006-1.2/libs/32:/opt/cpu2006-1.2/libs/64:/opt/cpu2006-1.2/sh"
OMP_NUM_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation
C benchmarks:
   icc -m64
C++ benchmarks:
   icpc -m64
Fortran benchmarks:
   ifort -m64
Benchmarks using both Fortran and C:
   icc -m64 ifort -m64

Base Portability Flags
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

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Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

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Hardware Availability: Apr-2016
Software Availability: Dec-2015

Base Portability Flags (Continued)

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64
-DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
  icc  -m64

C++ benchmarks:
  icpc  -m64

Fortran benchmarks:
  ifort  -m64

Benchmarks using both Fortran and C:
  icc  -m64 ifort  -m64
Cisco Systems
Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

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Tested by: Cisco Systems

Test date: Jul-2016
Hardware Availability: Apr-2016
Software Availability: Dec-2015

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
- 433.milc: basepeak = yes
- 470.lbm: basepeak = yes
- 482.sphinx3: basepeak = yes

C++ benchmarks:
- 444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias -auto-ilp32
- 447.dealII: basepeak = yes
- 450.soplex: basepeak = yes
- 453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:
- 410.bwaves: basepeak = yes
- 416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-
- 434.zeusmp: basepeak = yes
- 437.leslie3d: basepeak = yes
- 459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel
- 465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc

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Cisco Systems
Cisco UCS C220 M4 (Intel Xeon E5-2697A v4, 2.60 GHz)

SPECfp2006 = 122
SPECfp_base2006 = 117

Peak Optimization Flags (Continued)

465.tonto (continued):
   -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias
481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revE.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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